



User's Guide

NHD-160128WG-BTGH-VZ# LCM

(Liquid Crystal Display Graphic Module) RoHS Compliant

NHD- Newhaven Display **160128-** 160 x 128 Dots

WG- W- Version Line G- Display Type= Graphic

B - Model/ Serial NumberT- White LED B/LG- STN- Gray

H- Transflective, 6:00 View, Wide Temperature $(-20 \sim +70c)$ VZ#- V: Build in Negative Voltage Z: IC NT7086 #: RoHS

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1. Module Classification Information

<u>NHD</u> <u>160128</u> <u>W</u> G <u> B T G H - VZ#</u>

① ② ③ ④ ⑤ ⑥ ⑦ ® 9

① Brand: Newhaven Display

② Display Font: 160* 128 Dots

3 Factory Line: W

4 Display Type : H \rightarrow Character Type, G \rightarrow Graphic Type, C \rightarrow Color, X \rightarrow Tab Type

Model / Serial number:

⑥ Backlight Type: $N \rightarrow Without backlight$ $T \rightarrow White LED$

 $B\rightarrow EL$, Blue green $A\rightarrow LED$, Amber $D\rightarrow EL$, Green $R\rightarrow LED$, Red $W\rightarrow EL$, White $O\rightarrow LED$, Orange $F\rightarrow CCFL$, White $G\rightarrow LED$, Green

Y→LED, Yellow Green

 \bigcirc LCD Mode : B \rightarrow TN Positive, Gray T \rightarrow FSTN Negative

 $N\rightarrow TN$ Negative, Y $\rightarrow STN$ Positive, Yellow Green

G→STN Positive, Gray M→STN Negative, Blue

F→FSTN Positive

Temperature range/ D→Reflective, N.T, 12:00 K→Transflective, W.T,12:00 View direction

w direction $G \rightarrow Reflective, W. T, 6:00$ $C \rightarrow Transmissive, N.T, 6:00$

J→Reflective, W. T, 12:00 F→Transmissive, N.T,12:00 B→Transflective, N.T,6:00 I→Transmissive, W. T, 6:00

E→Transflective, N.T.12:00 L→Transmissive, W.T,12:00

9 Special Code V: Build in Negative Voltage Z: IC NT7086;

#: Fit in with the ROHS Directions and regulations

2. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering:only to the I/O terminals.
- (7) Storage:please storage in anti-static electricity container and clean environment.

3. General Specification

ITEM	STANDARD VALUE	UNIT	
Number of Dots	160 ×128		
Module dimension	129.0×102.0×16.5(MAX)	mm	
View area	101.0×82.0	mm	
Active area	95.96 x 76.76	mm	
Dot size	0.56×0.56	mm	
Dot pitch	0.60 ×0.60	mm	
LCD type	STN Positive, Gray,	Γransflective	
Duty	1/128		
View direction	6 o'clock		
Backlight	LED, Whit	te	

4. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Operating Temperature	T_{OP}	-20	_	+70	$^{\circ}$
Storage Temperature	T_{ST}	-30	_	+80	$^{\circ}\!\mathbb{C}$
Input Voltage	$V_{\rm I}$	V_{SS}	_	V_{DD}	V
Supply Voltage For Logic	Vcc-Vss	-0.3	_	+7	V
Supply Voltage For LCD	V _{CC} -V _{EE}	0	_	28	V

5. Electrical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage For Logic	V_{DD} - V_{SS}	_	4.75	5.0	5.25	V
		Ta=-20°C	_	_	22.1	V
Supply Voltage For LCD	V_{DD} - V_0	Ta=25°C	_	19.2	_	V
		Ta=70°C	16.8	_	_	V
Input High Volt.	V_{IH}	_	V _{DD} -2.2	_	$V_{ m DD}$	V
Input Low Volt.	V_{IL}	_	0	_	0.8	V
Output High Volt.	V_{OH}	_	V _{DD} -0.3	_	$V_{ m DD}$	V
Output Low Volt.	V _{OL}	_	0	_	0.3	V
Supply Current	I_{DD}	V _{DD} =5.0V	30	42	50	mA

6. Optical Characteristics

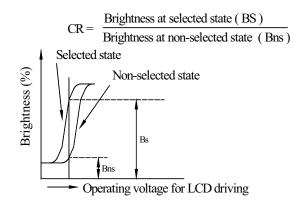
ITEM	SYMBAL	CONDITION	MIN.	TYP.	MAX.	UNIT
	(V) θ	CR≧2	20		40	deg
View Angle	(H) φ	CR≧2	-30		30	deg
Contrast Ratio	CR	_		3		_
	T rise	_		200	300	ms
Response Time	T fall	_		200	300	ms

6.1 **Definitions**

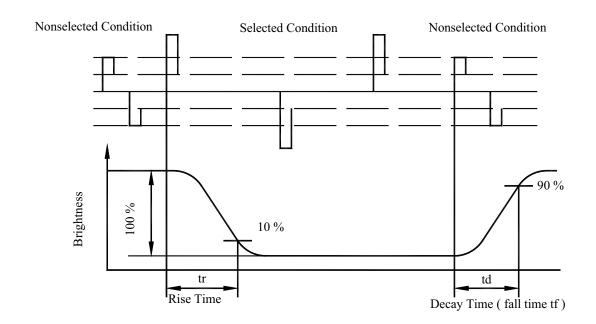
View Angles

$\begin{array}{c|c} Z & \text{(Visual angle direction)} \\ \theta & & \\ \hline & Y \\ \text{(Best visual angle direction)} \end{array}$

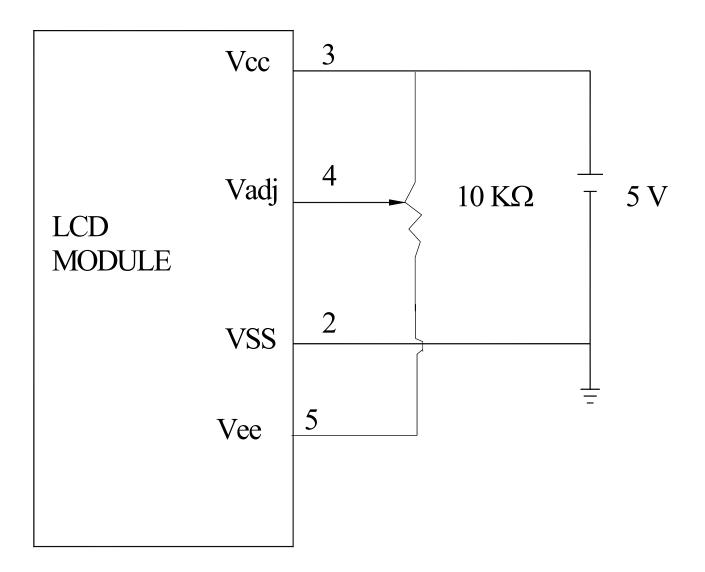
Contrast Ratio



Response Time

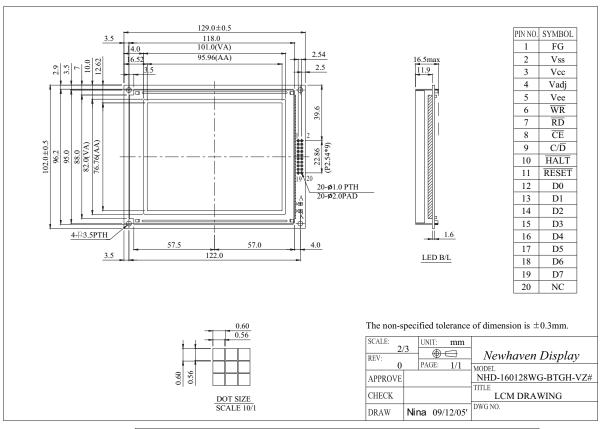


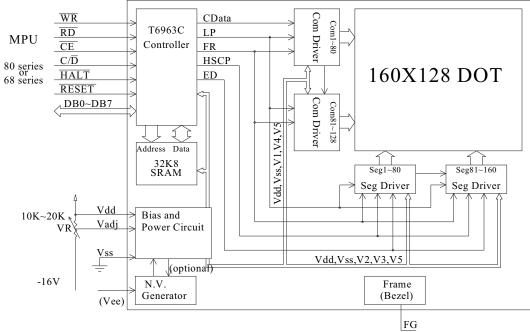
7. Power Supply for LCD Module and Contrast Adjust



VDD-V0:LCD Operating Voltage

8.Outline dimension & block diagram





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9. Interface Pin Function

Pin No.	Symbol	Level	Description
1	FG		Frame ground
2	Vss	0V	Ground
3	VDD	5.0V	Power supply for logic
4	Vadj		Power supply for LCD contrast adjustment
5	Vee		Negative voltage Output
6	/WR	L	Data write. Write data into T6963C when /WR = L
7	/RD	L	Data read. Read data from T6963C when RD = L
8	/CE	L	Chip enable the controller T6963C
9	C/D	H/L	WR=L, C/D=H: Command Write C/D=L: Data write RD=L, C/D=H: Status Read C/D=L: Data read
10	/HALT	L	Clock operating stop signal
11	/RESET	L	Reset signal
12	DB0	H/L	Data bus line
13	DB1	H/L	Data bus line
14	DB2	H/L	Data bus line
15	DB3	H/L	Data bus line
16	DB4	H/L	Data bus line
17	DB5	H/L	Data bus line
18	DB6	H/L	Data bus line
19	DB7	H/L	Data bus line
20	NC		NC

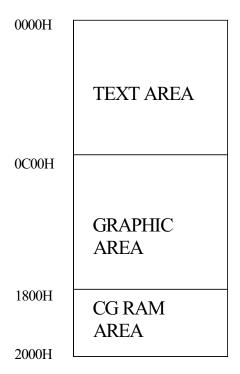
10. Display control instruction

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM (refer to Table 1.), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

•RAM Interface

The external RAM is used to store display data(text, graphic and external CG data). It can be freely allocated to the memory area(8 Kbyte max).

Recommend



• Flowchart of communications with MPU

(1)Status Read

A status check must be performed before data is read or written.

Status check

The Status of T6963C can be read from the data lines.

 RD
 L

 WR
 H

 CE
 L

 C/D
 H

 Do to D7
 H

The T6963C status word format is as follows:

 MSB
 LSB

 STA7
 STA6
 STA5
 STA4
 STA3
 STA2
 STA1
 STA0

STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

STA0	Check command execution capability	0:Disable 1:Enable
STA1	Check data read/write Capability	0:Disable
	One data read, write Supusiney	1:Enable
STA2	Check Auto mode data read capability	0:Disable
SIAZ	Check Auto mode data read capability	1:Enable
STA3	Charle Auto made data verita canability	0:Disable
SIAS	Check Auto mode data write capability	1:Enable
STA4	Not used	
CTA 5	Charle controller anamation comphility	0:Disable
STA5	Check controller operation capability	1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy	0:No error
SIAO	commands.	1:Error
CT-A-7		0:Disable off
STA7	Check the blink condition	1:Normal display

(Note 1) It is necessary to check STA0 and STA1 at the same time.

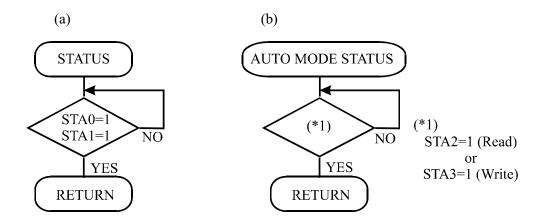
There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

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Status Checking flow



(Note 4) When using the MSB=0 command, a Status Read must be performed.

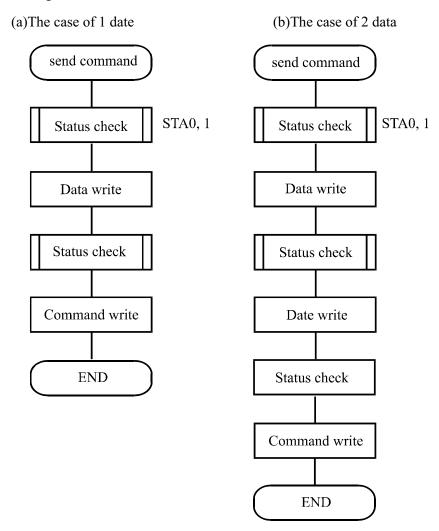
If a status check is not carried out, the T6963C cannot operate normally, even after a delay time.

The hardware interrupt occurs during the address calculation period (at the end of each line). If a MSB=0 command is sent to the T6963C during this period, the T6963C enters Wait status. If a status check is not carried out in this state before the next command is sent, there is the possibility that the command or data date will not be received.

(2)Setting date

When using the T6963C, first set the data, then set the command.

Procedure for sending a command



(Note) When sending more than two data, the last datum (or last two data)is valid.

• COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
	00100001	X address	Y address	Set Cursor Pointer
REGISTERS SETTING	00100010	Date	00H	Set Offset Register
	00100100	Low address	High address	Set Address Pointer
	01000000	Low address	High address	Set Text Home Address
SET CONTROL WORD	01000001	Columns	00H	Set Text Area
BET CONTROL WORLD	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00H	Set Graphic Area
	1000x000	_	_	OR mode
	1000x001	_	_	EXOR mode
MODE SET	1000x011	_	_	AND mode
WIODE SET	1000x100	_	_	Text Attribute mode
	10000xxx	_	_	Internal CG ROM mode
	10001xxx	_	_	External CG RAM mode
	10010000	_	_	Display off
	10010000 1001xx10	_	_	Cursor on, blink off
	1001xx10	_	_	Cursor on, blink on
DISPLAY MODE	100101xx	_	_	Text on, graphic off
	100110xx	_	_	Text off, graphic on
	100111xx	_	_	Text on, graphic on
	10100000			1-line cursor
	10100000	_		2-line cursor
	10100001	_	_	3-line cursor
CURSOR PATTERN	10100011	_	_	4-line cursor
SELECT	10100100	_	_	5-line cursor
	10100101	_	_	6-line cursor
	10100110	_	_	7-line cursor
	10100111	_	_	8-line cursor
DATA ALITO	10110000	_	_	Set Data Auto Write
DATA AUTO	10110001	_	_	Set Data Auto Read
READ/WRITE	10110010	_	_	Auto Reset
	11000000	Data		Data Write and Increment ADP
	11000000	Data _	_	Data Read and Increment ADP
	11000001	Data	_	Data Write and Decrement ADP
DATA READ/WRITE	11000010 11000011	Data _		Data Read and Decrement ADP
	11000011	Data	_	Data Write and Non-variable
	11000100	Data —	_	ADP
			_	Data Read and Non-variable ADP
SCREEN PEEK	11100000	_	_	Screen Peek
SCREEN COPY	11101000			Screen Copy
	11110xxx	_	_	Bit Reset
	11111111111111111111111111111111111111	_	_	Bit Set
	1111x000	_	_	Bit 0 (LSB)
	1111x001	_	_	Bit 1
DIT CET/DECET	1111x010	_	_	Bit 2
BIT SET/RESET	1111x011	_	_	Bit 3
	1111x100	_	_	Bit 4
	1111x101	_	_	Bit 5
	1111x110	_	_	Bit 6
	1111x111	_	_	Bit 7 (MSB)

Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	YADRS
00100010	22H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1)Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only be moved by this command. Data read/write from the MPU never changes the cursor pointer. X ADRS and Y ADRS are specified as follows.

X ADRS 00H to 4FH (lower 7 bits are valid)

Y ADRS 00H to 1FH (lower 5 bits are valid)

Single-Scan

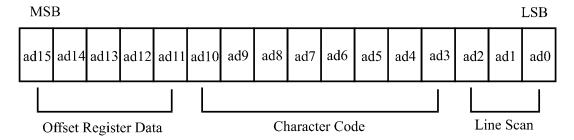
X ADRS 00 to 4FH

Y ADRS 00H to 0FH

(2)Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.



T6963C assign External character generator, when character code set 80H TO FFH in using internal character generator. Character code 00H to 80H assign External character generator, when External generator mode.

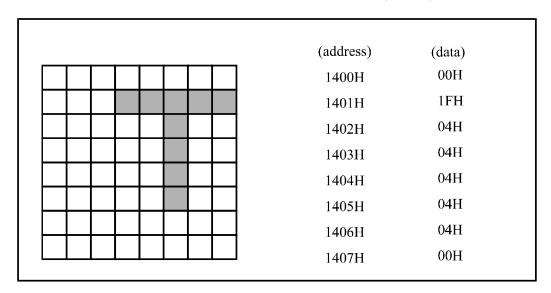
The senior five bits define the start address in external memory of the CG RAM area. The next eight bits represent the character code of the character. In internal CG ROM, character codes 00H to 7FH

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represent the predefined "internal" CG ROM characters, and codes 80H to FFH represent the user's own "external" characters. In external CG ROM mode, all 256 codes from 00H to FFH can be used to represent the user's own characters. The three least significant bits indicate one of the eight rows of eight dots that define the character's shape.

The relationship between display RAM address and offset register

Offset register data	CG RAM hex. address (start to end)
00000	0000 to 07 FFH
00001	0800 to 0FFFH
00010	1000 to 17FFH
11100	E000 to E7FFH
11101	E800 to EFFFH
11110	F000 to F7FFH
11111	F800 to FFFFH
(Example 1)	
Offset register	02H
Character code	80H
Character generator RAM start address	0001 0100 0000 0000
	1 4 0 0 H



(Example 2) The relationship between display RAM data and display characters

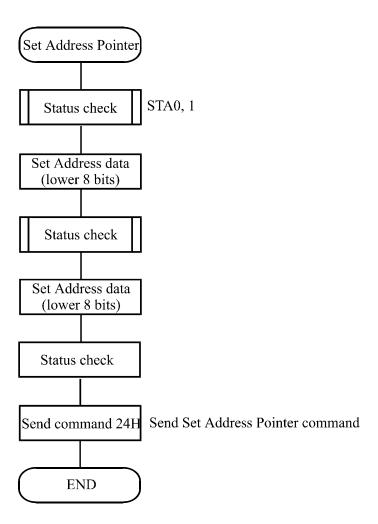
	(RAM DATA)	(Character)
AB γ DE ζ GHIJKLM	21H	A
	22Н	В
	83Н	γ
	24Н	D
	25H	E
	J 86H	ζ
Display character		

 $[\]gamma$ and ζ are displayed by character generator RAM.

(3)Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from)external RAM.

The Flowchart for Set Address Pointer command



Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00Н
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1)Set Text Home Address

The starting address in the external display RAM for text display is defined by this command.

The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH	TH+CL
TH+TA	TH+TA+CL
(TH+TA)+TA	TH+2TA+CL
(TH+2TA)+TA	TH+3TA+CL
TH+(n-1)TA	TH+(n-1)TA+CL

TH: Text home address

TA: Text area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Text home address : 0000H
Text area : 0020H

: 32 Columns

: 4 Lines

0000Н	0001H	001EH	001FH
0020H	0021H	003EH	002FH
0040H	0041H	005EH	005FH
0060Н	0061H	007EH	007FH

(2)Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this command.

The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH	GH+GL
GH+GA	GH+GA+CL
(GH+GA)+GA	GH+2GA+CL
(GH+2GA)+GA	GH+3GA+CL
GH+(n-1)GA	GH+(n-1)GA+CL

GH: Graphic home address

GA: Graphic area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Graphic home address : 0000H

Graphic area : 0020H

: 32 Columns

: 2 Lines

0000Н	0001H	001EH	001FH
0020H	0021H	003EH	003FH
0040H	0041H	005EH	005FH
0060Н	0061H	007EH	007FH
0080Н	0081H	009EH	009FH
00A0H	00A1H	00BEH	00BFH
00C0H	00C1H	00DEH	00DFH
00E0H	00E1H	00FEH	00FFH
0100H	0101H	011EH	011FH
0120H	0121H	013EH	013FH
0140H	0141H	015EH	014FH
0160H	0161H	017EH	017FH
0180H	0181H	109EH	019FH
01A0H	01A1H	01BEH	01BFH
01C0H	01C1H	01DEH	01DFH
01E0H	01E1H	01FEH	01FFH

(3)Set Text Area

The display columns are defined by the hardware Setting. This command can be used to adjust the columns of the display.

(Example)

LCD size 20 columns, 4lines

Text home address 0000H

Text area 0014H

Set 32 columns, 4 Lines

0000	0001	•••••	0013	0014	•••••	001F
0014	0015	•••••	0027	0028	•••••	0033
0028	0029		003B	003C		0047
003C	003D		004F	0050		005B



(4)Set Graphic Area

The display columns are defined by the hardware setting. This command can be used to adjust the columns of the graphic display.

(Example)

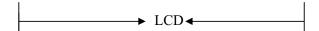
LCD size 20 columns, 2lines

Graphic home address : 0000H

Graphic are : 0014H

Set 32 columns, 2 Lines

0000	0001	•••••	0013	0014	•••••	001F
0014	0015	•••••	0027	0028	•••••	0033
0028	0029	•••••	003B	003C	•••••	0047
003C	003D	•••••	004F	0050	•••••	005B
0050	0051	•••••	0063	0064	•••••	006F
0064	0065		0077	0078		0083
0078	0079		008B	008C		0097
008C	008D		009F	00A0	•••••	00AB
00A0	00A1		00B3	00B4		00BF
00B4	00B5		00C7	00C8	•••••	00D3
00C8	00C9		00DB	00DC		00E7
00DC	00DD	•••••	00EF	00F0	•••••	00FD
00F0	00F1		0103	0104		011F
0104	0105	•••••	0127	0128	•••••	0123
0128	0129		013B	0013C	•••••	00147
013C	013D		014F	0150		015B



If the graphic area setting is set to match the desired number of columns on the LCD, the addressing scheme will be automatically modified so that the start address of each line equals the end address of the previous line +1.

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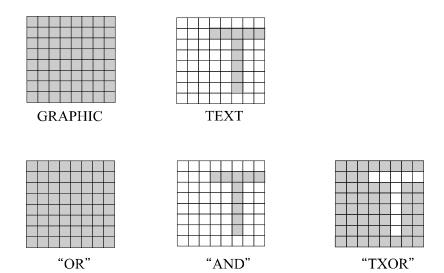
Mode set

CODE	FUNCTION	OPERAND
1000x000	OR Mode	_
1000x001	EXOR Mode	_
1000x011	AND Mode	_
1000x100	TEXT ATTRIBUTE Mode	_
10000xxx	Internal Character Generator Mode	_
10001xxx	External Character Generator Mode	_

X: invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

(Example)



(Note)Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

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The attribute data for each character in the text area is written to the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1byte

x x x x	d3	d2	d1	d0
---------	----	----	----	----

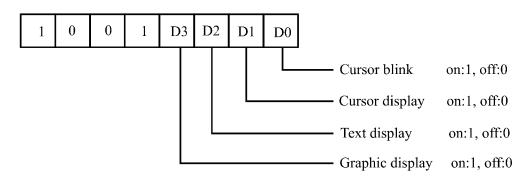
d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

X: invalid

• Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	
1001xx10	Cursor on, blink off	_
1001xx11	Cursor on, blink on	
100101xx	Text on, graphic off	
100110xx	Text off, graphic on	_
100111xx	Text on, graphic on	_

X: invalid



(Note)It is necessary to turn on "Text display" and "Graphic display" in the following cases.

- a)Combination of text/graphic display
- b)Attribute function

· Cursor pattern select

CODE	FUNCTION	OPERAND
10100000	1-line cursor	l
10100001	2-line cursor	
10100010	3-line cursor	_
10100011	4-line cursor	_
10100100	5-line cursor	_
10100101	6-line cursor	_
10100110	7-line cursor	_
10100111	8-line cursor	_

When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines.

The cursor address is defined by the Cursor Pointer Set command.



1-line cursor



2-line cursor



8-line cursor

· Data Auto Read/Write

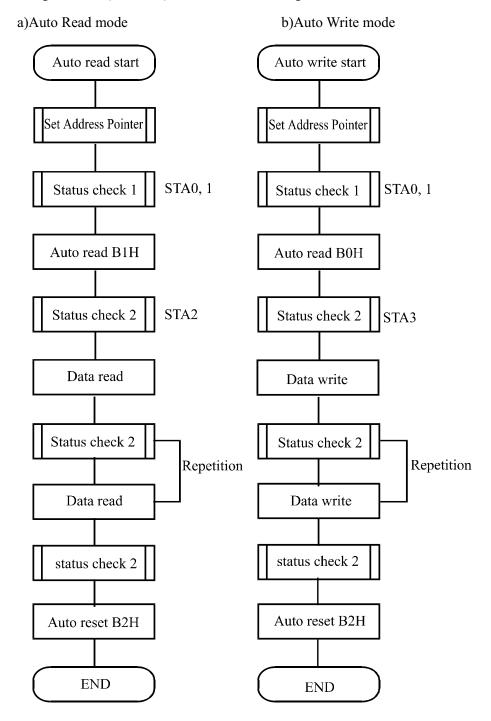
CODE	HEX.	FUNCTION	OPERAND
10110000	ВОН	Set Data Auto Write	ı
10110001	В1Н	Set Data Auto Read	_
10110010	В2Н	Auto Reset	_

The command is convenient for sending a full screen of data from the external display RAM. After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command. After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

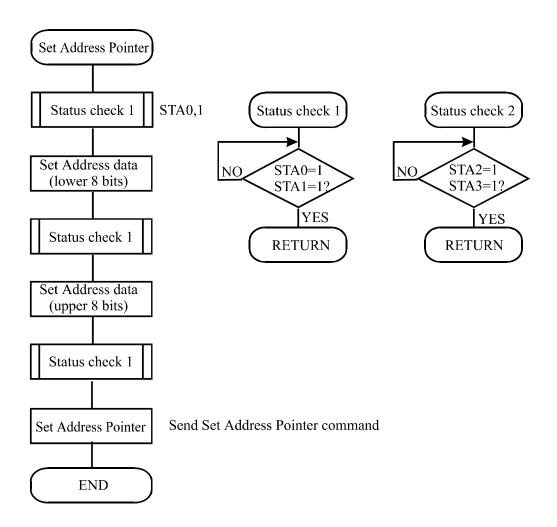
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The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode. (Note)A Status check for Auto mode

(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.



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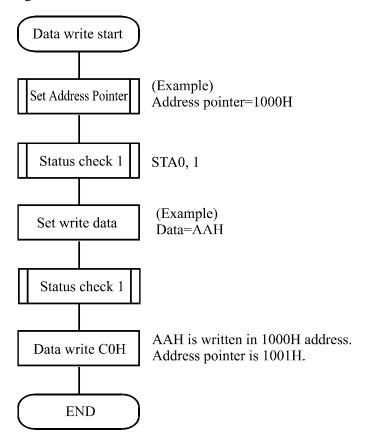


• Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	СОН	Data Write and Increment ADP	Data
11000001	С1Н	Data Read and Increment ADP	_
11000010	С2Н	Data Write and Decrement ADP	Data
11000011	СЗН	Data Read and Decrement ADP	1
11000100	С4Н	Data Write and Non-variable ADP	Data
11000101	С5Н	Data Read and Non-variable ADP	_

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note)This command is necessary for each 1-byte datum.



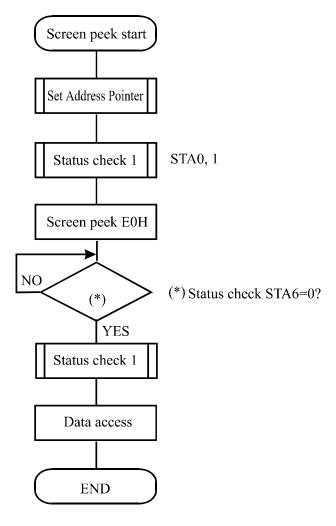
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· Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	Е0Н	Screen Peek	— e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this commands is ignored and a status flag (STA6) is set.



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· Screen Copy

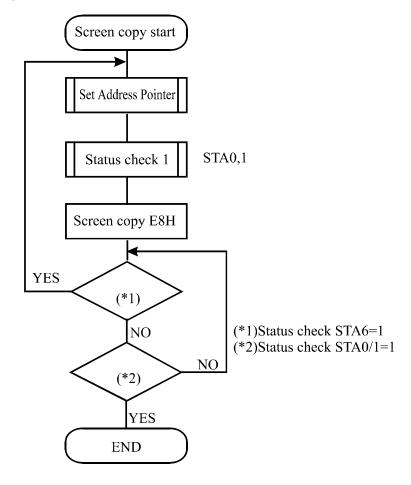
CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	_

This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1) If the attribute function is being used, this command is not available.

(With Attribute data is graphic area data.)



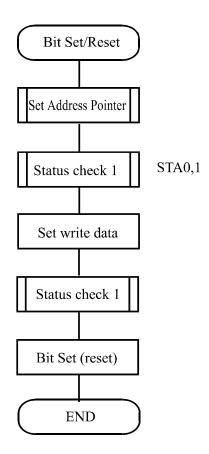
• Bit Set/Reset

CODE	FUNCTION	OPERAND
11110xxx	Bit Reset	_
11111xxx	Bit Set	
1111x000	Bit 0 (LSB)	l
1111x001	Bit 1	
1111x010	Bit 2	
1111x011	Bit 3	
1111x100	Bit 4	_
1111x101	Bit 5	_
1111x110	Bit 6	_
1111x111	Bit 7 (MSB)	_

X: invalid

This command use to set or reset a bit of the byte specified by the address pointer.

Only one bit can be set/reset at a time.



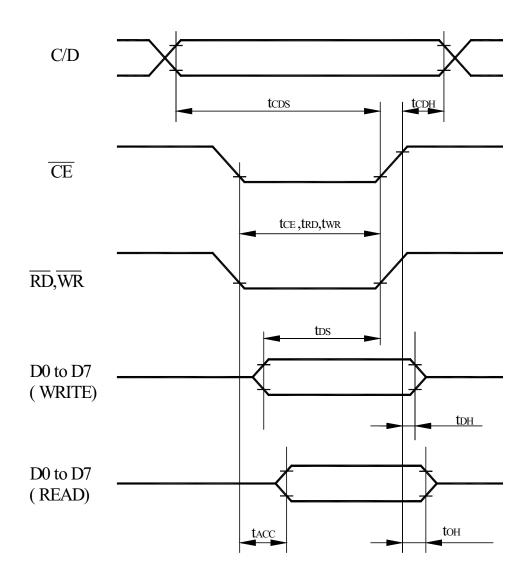
CHARACTER CODE MAP

Upper								
Lowbit						LHLH		l
			LLHL					
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11. Timing Characteristics

 $\label{eq:business} \begin{array}{l} \textbf{Bus Timing} \\ \text{(} Vss = 0 \text{ V , VDD} = 5 \text{ V)} \end{array}$

Item	Symbol	Min	Тур	Max	Unit
C/D Set-up Time	tcds	100	_	_	ns
C/D Hold Time	tcdh	10	_	_	ns
CE,RD,WR Pulse Width	tcds,trd,twr	80	_	_	ns
Data Set-up Time	tDS	80	_	_	ns
Data Hold Time	tDH	40	_	_	ns
Access Time	tacc	_	_	150	ns
Output Hold Time	tон	10	_	50	ns



12.RELIABILITY

Content of Reliability Test (wide temperature, -20°~~70°℃)

	Environmental Test								
Test Item	Content of Test	Test Condition	Note						
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2						
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	1,2						
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs							
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1						
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 °C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C ,90%RH 96hrs	1,2						
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C/70°C 10 cycles							
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3						
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time							

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

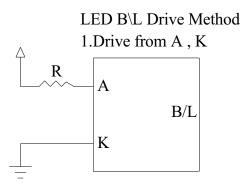
Note3: Vibration test will be conducted to the product itself without putting it in a container.

13. Backlight Information

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	96	160	240	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	_
Reverse Voltage	VR	_	_	5	V	_
Luminous Intensity	IV	260	300	_	CD/M ²	ILED=160mA
Life Time	_	_	50K	_	Hr.	ILED≤160mA
Color	White		1	•	•	

Note: The LED of B/L is drive by current only, drive voltage is for reference only. drive voltage can make driving current under safety area (current between minimum and maximum).



14. Inspection specification

NO	Item	Criterion				
01	Electrical Testing	 1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect. 				0.65
02	Black or white spots on LCD (display only)	 2.1 White and black spots on display ≤0.25mm, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm 				
03	LCD black spots, white spots, contamination	3.1 Round type: $\Phi = (x + y)/2$ X 3.2 Line type: (2)	Y Y	SIZE $\Phi \le 0.10$ $0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.25$ $0.25 < \Phi$	2	2.5
	(non-display)	→ L ₩	Length L≤3.0 L≤2.5	$\begin{tabular}{c c} Width & $W \le 0.02$ \\ \hline 0.02 < W \le 0.03 \\ \hline 0.03 < W \le 0.05 \\ \hline 0.05 < W \\ \hline \end{tabular}$	Acceptable Q TY Accept no dense 2 As round type	2.5
04	Polarizer bubbles	If bubbles are virjudge using blac specifications, not of find, must che specify direction	k spot ot easy eck in	Size Φ $\Phi \le 0.20$ $0.20 < \Phi \le 0.50$ $0.50 < \Phi \le 1.00$ $1.00 < \Phi$ Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5

NO	Item	Criterion	AQL
NO 06	Glass	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	AQL 2.5
		specifications.	
		y: width x: length	
		$y \le 1/3L \qquad x \le a$	
		y y	

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit.8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	 10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB 	2.5 2.5 0.65 2.5 2.5 0.65 2.5 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. 12.2 No cracks on interface pin (OLB) of TCP. 12.3 No contamination, solder residue or solder balls on product. 12.4 The IC on the TCP may not be damaged, circuits. 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever. 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. 12.7 Sealant on top of the ITO circuit has not hardened. 12.8 Pin type must match type in specification sheet. 12.9 LCD pin loose or missing pins. 12.10 Product packaging must the same as specified on packaging specification sheet. 12.11 Product dimension and structure must conform to product specification sheet. 	2.5 0.65 2.5 2.5 2.5 2.5 0.65 0.65 0.65

15 . Material List of Components for RoHs

1. Newhaven Display International hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2.Process for RoHS requirement:

- (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
- (2) Heat-resistance temp. :

Reflow: 250°C,30 seconds Max.;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5° ;

Newhaven LCM Samp	le Estimate							
Module Number: Page: 1								
1 · Panel Specification :								
1. Panel Type:	Pass	□ NG ,						
2. View Direction:	Pass	□ NG ,						
3. Numbers of Dots:	Pass	□ NG ,						
4. View Area:	Pass	□ NG ,						
5. Active Area:	Pass	□ NG ,						
6. Operating Temperature	: Pass	□ NG ,						
7. Storage Temperature:	Pass	□ NG ,						
8. Others:								
2 · Mechanical								
1. PCB Size:	Pass	□ NG ,						
2. Frame Size:	Pass	□ NG ,						
3. Materal of Frame:	Pass	□ NG ,						
4. Connector Position:	Pass	□ NG ,						
5. Fix Hole Position:	Pass	□ NG ,						
6. Backlight Position:	Pass	□ NG ,						
7. Thickness of PCB:	Pass	□ NG ,						
8. Height of Frame to	Pass	□ NG ,						
9. Height of Module:	Pass	□ NG ,						
Others:	Pass	□ NG ,						
${f 3}^0$ Relative Hole Size :								
1. Pitch of Connector:	Pass	□ NG ,						
2. Hole size of Connector:	Pass	□ NG ,						
3. Mounting Hole size:	Pass	□ NG ,						
4. Mounting Hole Type:	Pass	□ NG ,						
5. Others:	Pass	□ NG ,						
4 · Backlight Specification :								
1. B/L Type:	Pass	□ NG ,						
2. B/L Color:	Pass	□ NG ,						
3. B/L Driving Voltage (Ref	erence for LE							
4. B/L Priving Current:	Pass	□ NG ,						
5. Brightness of B/L:	Pass	□ NG ,						
6. B/L Solder Method:	Pass	□ NG ,						
7. Others:	Pass	□ NG ,						
	>> Go to	page 2 <<						

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Newhaven odule Number:		Page	e: 2
5 · Electronic Characteristics of		1 "5"	·• =
1. Input Voltage:	☐ Pass	□ NG ,	
2. Supply Current:	Pass	□ NG ,	
3. Driving Voltage for LCD:	Pass	□ NG ,	
4. Contrast for LCD:	☐ Pass	□ NG ,	
5. B/L Driving Method:	Pass	□ NG ,	
6. Negative Voltage Output:	Pass	□ NG ,	
7. Interface Function:	Pass	□ NG ,	
8. LCD Uniformity:	Pass	□ NG ,	
9. ESD test:	☐ Pass	□ NG ,	
10. Others:	Pass	□ NG ,	
Sales signature :		_	
Customer Signature:		Date: / /	