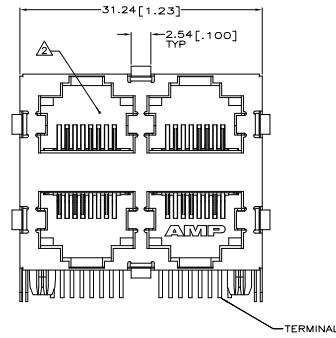
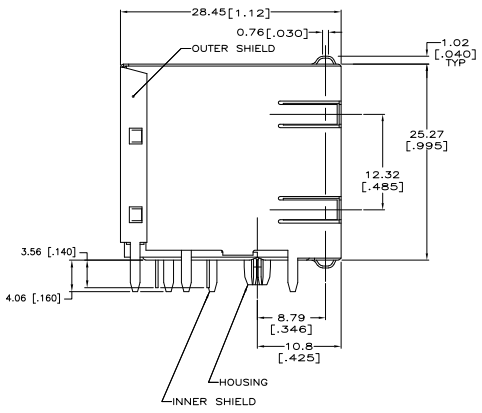
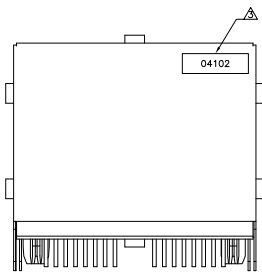


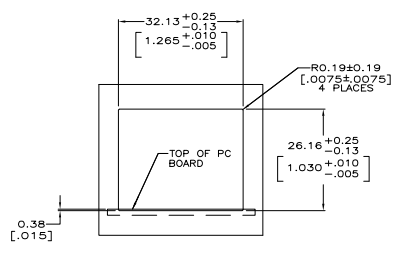
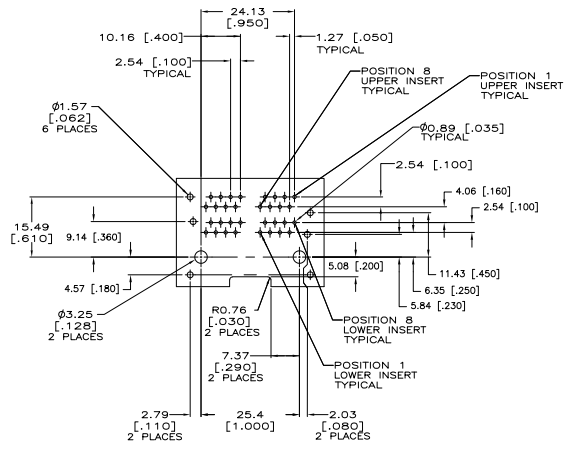
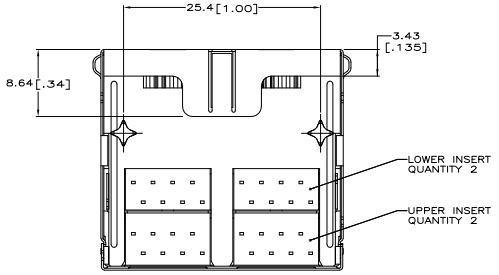
REV	DATE	DESCRIPTION	BY	CHK	APP
AA	22				
A		REV PER EC 0511-0201-04			



▲ MATERIAL:
 HOUSING - PCT POLYESTER, BLACK, UL94V-0.
 OVERMOLDED TERMINAL ARRAY - PBT, BROWN.
 TERMINALS - 0.33 [.013] THICK PHOS BRONZE
 PLATED WITH 1.27µm [.000050] MIN THICK
 HARD GOLD IN LOCALIZED AREA AND 3.81µm
 [.000150] MIN THICK MATTE TIN IN SOLDER
 AREA OVER 1.27µm [.000050] MIN THICK
 NICKEL UNDERPLATE.
 SHIELD - 0.25 [.010] THICK COPPER ALLOY
 PLATED WITH 2.03µm [.000080] MIN BRIGHT TIN
 OVER 1.27µm [.000050] MIN NICKEL.

▲ JACK CAVITY CONFORMS TO FCC RULES AND
 REGULATIONS, PART 68 SUBPART F.

▲ DATE CODE LOCATED ON REAR OF PART
 APPROXIMATELY AS SHOWN:
 FIRST 2 DIGITS ARE LAST 2 DIGITS OF YEAR,
 SECOND 2 DIGITS ARE MANUFACTURING
 WORK WEEK,
 LAST DIGIT IS DAY OF WEEK, WITH SUNDAY = 1.



PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE
 SCALE 2:1

SUGGESTED PANEL CUTOUT DIMENSIONS
 SCALE 2:1

THIS DRAWING IS A CONTROLLED DOCUMENT		5569260-1 PART NUMBER	
DRAWING NO. 108-1163-2 REV. 114-2048 DATE 05/11/04	DESIGNED BY WESTMAN DRAWN BY ELKINDGER CHECKED BY 108-1163-2 APPROVED BY 114-2048	TYPED BY INKED BY CHECKED BY APPROVED BY	Tye Electronics Corporation Harrisburg, PA 17105-3608 STACKED MODULAR JACK ASSEMBLY, 2x2 PORT, 8 POSITION, SHIELDED, PANEL GROUND, CATEGORY 5
CUSTOMER DRAWING		5569260 REV. 4:1 SHEET 1 OF 1	