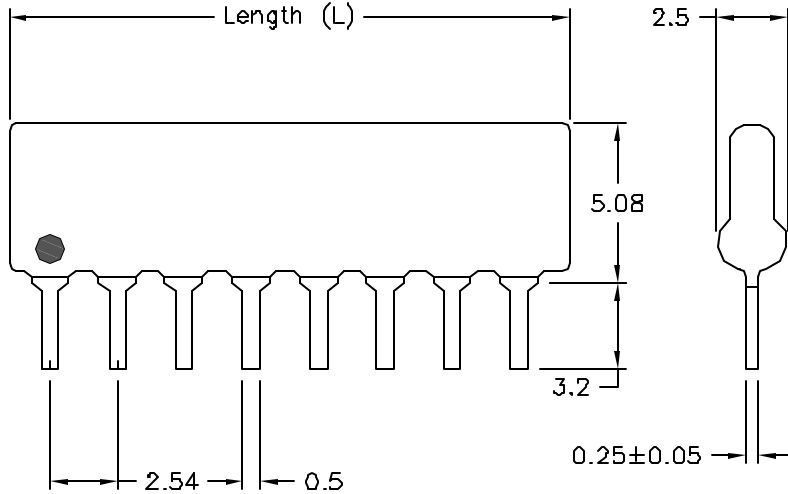


DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1991	A	RELEASED	JN	10/14/08	JWM	10/14/08	JWM	10/14/08



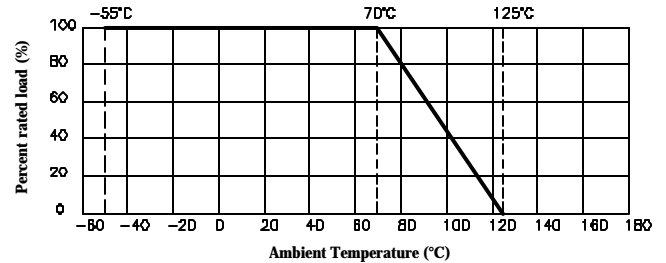
Features

- High Reliability with RUO2 paste
- Miniature, high density packaging
- Combinations of different ohmic values are available

Performance Specifications

- Temperature coefficient: 50W-1MW: ±200PPM/°C  
<50W & > 1MW: ±250PPM/°C
- Short-time Overload: DR/R ≤ ±(0.5 % + 0.1W)
- Insulation Resistance: Min. 10,000 Mega Ohm
- Dielectric withstanding Voltage: No Evidence of flashover, Mechanical damage, arcing or insulation breakdown.
- Terminal Strength: DR/R ≤ ±(0.5 % + 0.1W)
- Resistance to soldering heat: DR/R ≤ ±(0.5 % + 0.1W)
- Solderability: Min. 95 Coverage
- Thermal Shock: DR/R ≤ ±(0.5 % + 0.1W)
- Temperature cycling: DR/R ≤ ±(0.5 % + 0.1W)
- Load life in humidity: DR/R ≤ ±(3.0 % + 0.1W)
- Load Life: DR/R ≤ ±(3.0 % + 0.1W)

Mfg P/N	Resistance	Number of pins	Package/Case	L (Max)	H (Max)	T (Max)	C +0.3 -0.2	D± 0.1	F± 0.2
MCRNLA05G0103B0E	10kohm	5	5-SIP	12.7	5.08	2.5	3.2	0.5	2.54
MCRNLA06G0102B0E	1kohm	6	6-SIP	15.3					
MCRNLA06G0103B0E	10kohm	6	6-SIP						
MCRNLA06G0472B0E	4.7kohm	6	6-SIP	20.4					
MCRNLA08G0102B0E	1kohm	8	8-SIP						
MCRNLA08G0103B0E	10kohm	8	8-SIP						
MCRNLA08G0332B0E	3.3kohm	8	8-SIP						
MCRNLA08G0472B0E	4.7kohm	9	8-SIP	22.9					
MCRNLA09G0102B0E	1kohm	9	9-SIP						
MCRNLA09G0103B0E	10kohm	9	9-SIP						
MCRNLA09G0472B0E	4.7kohm	9	9-SIP	25.4					
MCRNLA10G0102B0E	1kohm	10	10-SIP						
MCRNLA10G0103B0E	10kohm	10	10-SIP						
MCRNLA10G0104B0E	100kohm	10	10-SIP						
MCRNLA10G0222B0E	2.2kohm	10	10-SIP						
MCRNLA10G0472B0E	4.7kohm	10	10-SIP						



Power Rating at 70 °C	Max. Working Voltage	Max. OverLoad Voltage	Dielectric With standing Voltage	Resistance Range	Resistance Tolerance	Operating Temp. Range
B Type: 0.2W	100V	150V	200V	R Type: 100■-10K■	±1%	-55°C--+125°C
Others: 0.125W				Others: 10■-1M■	±2% ±3%	

SPC-F004.DWG

<p>TOLERANCES: UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.</p>	<p>DRAWN BY: Jason Nash</p>	<p>DATE: 10/14/08</p>	<p>DRAWING TITLE: <b>Resistor Network</b></p>			
	<p>CHECKED BY: JEFF MCVICKER</p>	<p>DATE: 10/14/08</p>	<p>SIZE A</p>	<p>DWG. NO. TA-956</p>	<p>ELECTRONIC FILE TA-956.DWG</p>	<p>REV A</p>
	<p>APPROVED BY: JEFF MCVICKER</p>	<p>DATE: 10/14/08</p>	<p>SCALE: NTS</p>		<p>U.O.M.: MILLIMETERS</p>	<p>SHEET: 1 OF 1</p>

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