



EMULATION TECHNOLOGY, INC.

PROGRAMMING ADAPTER SPECIFICATION

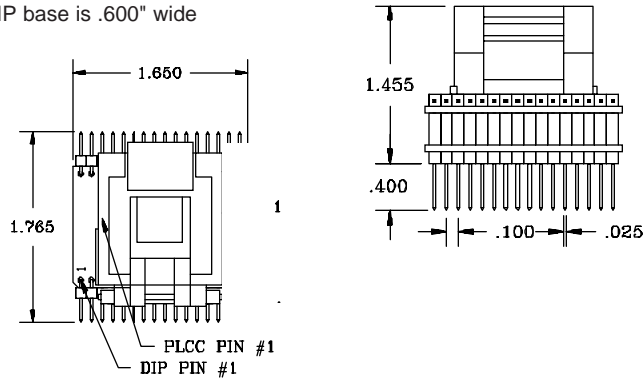
Item: 323201P600-YAM
Description: AS-32-32-01P-6YAM-LN

REPLACEMENT SOCKET: S-PCC-00-032-C

Call Emulation Technology for a replacement socket price and delivery quote.

TYPICAL DIMENSIONS:

Note: DIP base is .600" wide



All Dimensions in Inches.

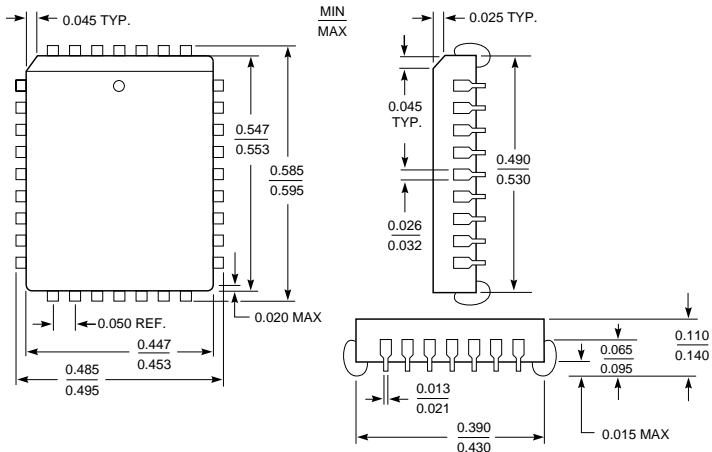
(Assembly Drawing: F1677)

CONNECTION TABLE:

32-Lead PLCC SOCKET	PIN Names*	32-Pin DIP Base
1	V _{PP}	1
2	NC/A16	2
3	NC/A15	3
4	A12	4
5	A7	5
6	A6	6
7	A5	7
8	A4	8
9	A3	9
10	A2	10
11	A1	11
12	A0	12
13	DQ0	13
14	DQ1	14
15	DQ2	15
16	V _{SS} (GND)	16
17	DQ3	17
18	DQ4	18
19	DQ5	19
20	DQ6	20
21	DQ7	21
22	CE#	22
23	A10	23
24	OE#	24
25	A11	25
26	A9	26
27	A8	27
28	A13	28
29	A14	29
30	NC/A17/RP#	30
31	WE#/PGM#	31
32	V _{CC}	32

Note: * Pin names are for reference only. This is a 1 to 1 adapter.

PACKAGE SPECIFICATIONS:



All Dimensions in Inches.

ET Code: 032-PCC7

Compatible PLCC devices:

- N28F256A AT29C57
- N28F512 27C010
- N28F010 27010
- N28F020 27C020
- N28F001BX 27C040
- N27C010 27C101
- N27C020

Manufacturers:

- Intel
- ATMEL and others package

Programmers:

- Advin Systems:** Pilot-U84/40/32, Pilot-145/4/3, Pilot-800(GM-832D), and Pilot-GE/GCE
- Advantest:** R4945, R4951, R4952
- Aval Data:** PKW-5100/3100/2100/1100
- B&C Microsystems:** Proteus
- BP Microsystems:** BP-1200, EP-1140, EP-1132
- Bytek:** 135HFT, MultiTRK-X000, EZ-FT, 832FT
- Data I/O:** ChipLab-32/48, 29B, 2900, 3900, UniSite(Site 40/48), AutoSite E, S1000(SR40), PSX1000/500(MEMORYRAIL, SR40),212, 288/A
- E.E. Tools:** AllMax, AllMax+, ProMax

- Elan:** 6000, 4000/5000 TURBO, 3000/4000/5000
 - Minato:** 1890A, 1891/2, 1910, 1930
 - Needham's Electronics:** EMP20
 - Logical Devices:** AllPro-88XR/88/40, Chipmaster 3000, GangPro SII/SE/8+, and 932(Digelec)
 - SMS:** Sprint PLUS48/Optima/Expert/MultiSyte/-Plus
 - Stag:** Eclipse, 3000, Quasar-1040/1084, Solar, PP39/40/41/42, Orbit, and Stratos 2
 - System General:** Multi-APRO, APRO, TURPRO-1/-1FX, SGUP-85A and TURPRO-832/840
- Additionally, any programmer supporting the DIP equivalent.



EMULATION TECHNOLOGY, INC.
 2344 WALSH AVENUE • BUILDING F
 SANTA CLARA, CA 95051-1301
 TEL 408 / 982.0660 • FAX 408 / 982.0664

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