

# IT3200C/E

#### Overview

Low cost SMD TCXO using analogue IC for compensation, with voltage control option. Frequencies ranging from 10MHz to 40MHz.

#### Description

The I(V)T3200 C/E series employs analogue IC technology for the oscillator and temperature compensation.  $3.2 \, \text{mm} \, \text{x}$   $2.5 \, \text{mm}$  in size, the RSX-8 crystal is surface mounted on top of the ceramic IC carrier. The segregation of the crystal from the oscillator further improves the reliability of the product.

#### **Recommended Applications**

Handset, GPS, PDA, PCMCIA CDPD cards, Automotive, Consumer Products, WiFi, WiMAX/WLAN, Communications, Other.

#### **Features**

Excellent phase noise performance

Standard temperature stability choices are ±0.5ppm, ±1.5ppm and ±2.5ppm over wide temperature ranges

# **IT3200C/E Specifications**

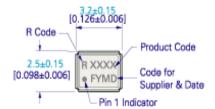
1.0	Specification References					
1.1	Model Description	I(V)T3200 C/E Series				
1.2	RoHS Compliant	Yes				
2.0	Frequency Characteristics					
	Parameter	Test Condition	Value	Units		
2.1	Frequency range	Frequency range available	10 to 40	MHz		
2.2	Frequency calibration	Offset from nominal frequency measured at 25°C±2°C	1 max	±ppm		
2.3	Reflow shift	Two consecutive reflows as per attached profile after 1 hour recovery	1 max	±ppm		
2.4	Frequency stability over temperature	Referenced to the midpoint between minimum and maximum frequency value over the specified temperature range. Control voltage is set to midpoint of control voltage (Note 1, 2)	0.5 to 5	±ppm		
2.5	Temperature range	The operating temperature range over which the frequency stability is measured for C series. (Note 3 & 10)	-40 to 85	°C		
2.6	Frequency slope	Minimum of 1 frequency reading every 2°C over the operating temperature range (Note 2, 4)	0.05 to 1	ppm/°C		
2.7	Static temperature hysteresis	Frequency change after reciprocal temperature ramped over the operating range. Frequency measured before and after at 25°C	0.6 max	ppm		
2.8	Supply voltage stability	Supply voltage varied ±5% at 25°C (Note 4)	0.2 max	±ppm		
2.9	Load sensitivity	±10% load change	0.2 max	±ppm		
2.10	Long term stability	Frequency drift over 1 year (Note 4)	2 max	±ppm		
3.0	Power Supply					
	Parameter	Test Condition	Value	Units		
3.1	Supply voltage	Nominal supply voltage range (Note 5 & 10)	2.3 to 3.7	V		
3.2	Current	At maximum supply voltage (Note 6)	2 max	mA		
4.0	Control Voltage (VCO) - Optional					
	Parameter	Test Condition	Value	Units		
4.1	Control voltage range	The nominal control voltage value is midway between the minimum and	0.5 to 2.8	V		
4.2	Frequency tuning	Frequency shift from minimum to maximum control voltages (Note 8 $\&$ 10)	6 to 50	ppm		
4.3	Port input impedance	Measured between control voltage and GND pin	500	kOhm		
4.4	Linearity	Deviation from the straight line curve fit	20 max	%		



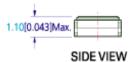
5.0	Oscillator Output			
	Parameter	Test Condition	Value	Units
5.1	Output waveform	DC coupled clipped sine wave (Note 9)		
5.2	Output voltage level	At minimum supply voltage (Note 6)	0.8	V
5.3	Output load resistance	Refer to test circuit	9 to 11	kOhm
5.4	Output load capacitance	Refer to test circuit	9 to 11	pF
				μ.
6.0	SSB Phase Noise			
	Parameter	Test Condition	Value	Units
6.1	SSB phase noise power density at	Typical values for a 16.369MHz oscillator at 25°C	-60 max	dBc/Hz
	1Hz offset			
6.2		Typical values for a 16.369MHz oscillator at 25°C	-89 max	dBc/Hz
	10Hz offset			
6.3		Typical values for a 16.369MHz oscillator at 25°C	-113 max	dBc/Hz
C 4	100Hz offset	Turisal values for a 10 200MUs cosillator at 25°C	122	dD = /1.1=
6.4		Typical values for a 16.369MHz oscillator at 25°C	-132 max	dBc/Hz
6.5	1kHz offset	Typical values for a 16 260MHz oscillator at 25°C	-145 max	dBc/Hz
0.5	10kHz offset	Typical values for a 16.369MHz oscillator at 25°C	-145 IIIdX	UBC/ FIZ
	TOKHZ OHSEL			
7.0	Environmental			
7.0	Parameter	Test Condition		
7.1	Shock	Half sine wave acceleration of 100G peak amplitude for 6mS duration, 3		
		cycles each plain		
7.2	Humidity	After 48 hours at 85°C±2°C 85% relative humidity non-condensing		
	,			
7.3	Thermal shock test	Exposed at -40°C for 30 minutes then to 85°C for 30 minutes repeatedly		
		for 120 hours		
7.4	Vibration	10G RMS from 30Hz to 1500Hz random in each of the 3 axis for 4 hours;		
		total 12 hours		
7.5	Storage temperature	-40 to 85°C		
8.0	Marking			
	Parameter	Test Condition		
8.1	Туре	Engraved		
8.2	Line 1	Rakon Logo and the last four characters of the Internal Part Number		
8.3	Line 2	Pin 1 mark and date code		
9.0	Manufacturing Information			
5.0				
9.1	Parameter Reflow	Test Condition Solder reflow processes		
9.2	Packaging description	Tape and reel		
J. <b>L</b>	r dekaging description	Tupe una reci		
10.0	Specification Notes			
	Parameter	Test Condition		
10.1	1	A maximum frequency stability over the temperature is required to be		
		specified. Standard options are ±0.5ppm, ±1ppm, ±2ppm		
10.2	2	Parts should be shielded from drafts causing unexpected thermal		
		gradients. Temperature changes due to ambient air currents on the		
		oscillator can lead to short term frequency drift		
10.3	3	The operating temperature range needs to be specified. The extremes for		
		these models are -40°C and 85°C		
10.4	4	The maximum value is the specification. A minimum value, if present		
		indicates the best specification available		
10.5				
		values		
10.6	6	Specified for the load stated in Oscillator Output section		
10.7	7	Voltage control cannot exceed Vcc -0.2V or below GDN +0.2V		
10.8	8	The maximum frequency tuning range depends on the design frequency		
		and the trimming sensitivity of the crystal. Linearity performance		
		degrades if maximum frequency tuning setting is selected		
40.0		Figure 140 Complete C		
10.9	9	External AC-Coupling capacitor required. 1nF or greater recommended		
10.10	10	Mot all variables are available on all readels Blazza accept Ball v. Color		
10.10	10	Not all variables are available on all models. Please consult Rakon Sales for further information		
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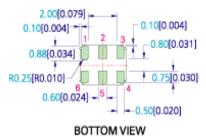


# MODEL DRAWING



TOP VIEW



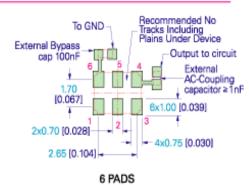


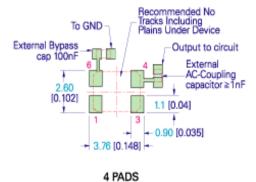
**END VIEW** 

#### PIN CONNECTIONS

Pin	IT	IVT
1	GND RECOMMENDED	vco
2	NC	NC
3	GND	GND
4	OUTPUT	OUTPUT
5	NC	NC
6	VCC	VCC

### RECOMMENDED PAD LAYOUT - TOP VIEW

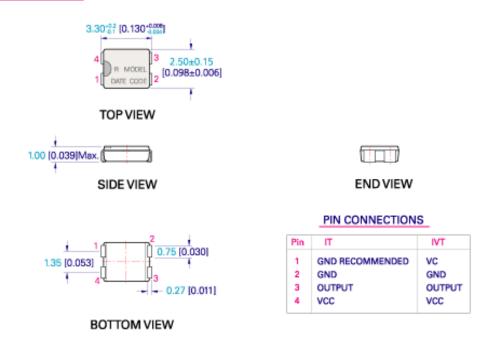




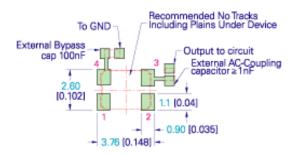
TITLE: I(V)T3200C MODEL	FILENAME: CAT434	REVISION: E	Tolerances: XX =±0.5
RELATED DRAWING:		DATE: 17-Feb-09	X.X. =±0.2
		SCALE: 5:1	X.XXX =±0.05
		Millimetres [inch]	X° -±1.0° Hole =±0.10 62009 Rakon Limited



# MODEL DRAWING

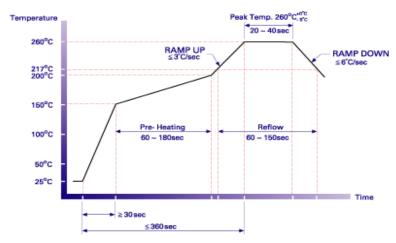


# RECOMMENDED PAD LAYOUT - TOP VIEW - FOR IVT3200 C & E PACKAGES



TITLE: I(V)T3200E MODEL	FILENAME: CAT389	REVISION: E	Tolerances: XX =±0.5
RELATED DRAWINGS:		DATE: 18-Feb-09	X.X. =±0.2
		SCALE: 5:1	— X.XX
		Millimetres [inch]	X° -±1.0° PRECISION QUARTE PRODUCTS  HOLE =±0.10 92009 Rakon Limited



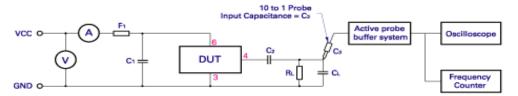


#### NOTE:

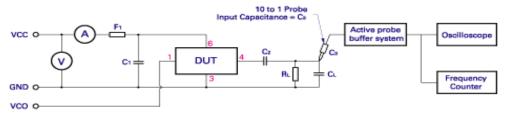
The product has been tested to withstand the Reflow Profile shown. The Reflow Profile used to solder Rakon TCXO is determined by the solder paste manufacturer's specification. It is recommended that the Reflow Profile used does not exceed the one shown above.

TITLE: 3200 SERIES Pb-FREE REFLOW	FILENAME: CAT324	REVISION: B	
RELATED DRAWINGS:		DATE: 14-Mar-08	
		SCALE: NTS	PRECISION QUARTZ PRODUCTS
		Millimetres [inch]	#2000 Palma I lavited

#### IT TEST CIRCUIT:



#### IVT TEST CIRCUIT:



C1: 100nF C2: ≥ 1nF RL: 10K

C<sub>1</sub> - C<sub>2</sub> + C<sub>3</sub> (C<sub>3</sub> - Oscilloscope probe capacitance) C<sub>7</sub> as stated in OSCILLATOR OUTPUT section

F<sub>1</sub>: A ferrite bead or a resistor between 22Ω~ 47Ω recommended.

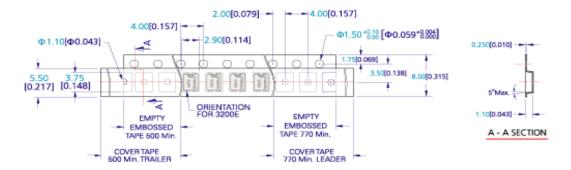
TITLE: I(V)T3200 SERIES TEST CIRCUIT FILENAME: CAT286 REVISION: F
RELATED DRAWINGS: DATE: 17-Feb-09

DATE: 17-Feb-09
SCALE: NTS
Millimetres [inch]

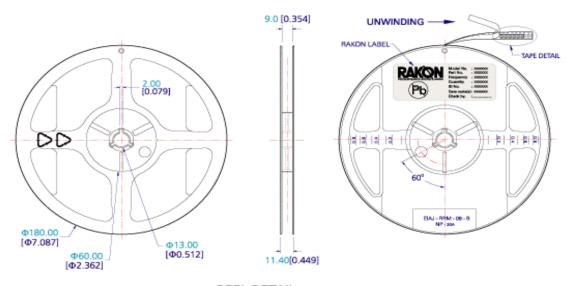


Overview Specification Sheet Version 1.0





TAPE DETAIL SCALE 2:1



REEL DETAIL SCALE 1:2.5

NOTE:

1. Φ180mm REEL's STANDARD PACKING QUANTITY IS 2000 OSCILLATORS PER REEL.