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FPX3 PRELIMINARY

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# High power Multichannel 869MHz band transceiver

FPX3 is a small, high power (half-watt) multi-channel wide band FM transceiver, operating in the Europewide 869.40?869.65MHz sub-band.



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Figure 1: FPX3-869-20

### **Features**

- Conforms to ETSI EN 300 220-3 and EN 301 489-3
- High performance PLL synthesizer
- +26dBm RF power output
- Data rates up to 20 kbps for standard module
- Usable range over 3km
- Fully screened. Low profile
- Feature-rich interface (RSSI, analogue and digital baseband)
- Re-programmable via RS232 interface

# **Applications**

- Asset tracking systems
- Industrial telemetry and telecommand
- High performance security system
- Fire alarms
- ROV control applications
- High speed data modems

# **Technical Summary**

- Operating frequency: 868 870MHz
- Maximum of 8 channels (depends on band allocation and data rate)
- Transmit power: 400mW (+26dBm) nominal
- Supply range: +5V regulated
- Current consumption: 500mA (at 400mW output) transmit, 50mA receive
- Data bit rate: 20kbps max. (standard module)
- Receiver sensitivity: -115dBm (for 1KHz/12 dB SINAD)
- Size: 67 x 30 x 12mm

**Evaluation Platform:** Narrow Band Evaluation Kit

Channel select pins +5U (tx) IN RSSI OUT S O tr anscei ver AF BOARD \$ 本 869MHz half-watt wideband multichannel AF LPF (BC) BOARD VOLTAGE REG AF LPF CPU BPF BPF Cer amic 10.7MHz BPF 12MHz variable phase RF SWITCH PA CONTROL LOOP PLL SYNTH <u></u> (RC) LC LPF RF SWITCH 429-435MHz (mod) FPX3 000

Figure 2: FPX3 block diagram

### FPX3 FOOTPRINT

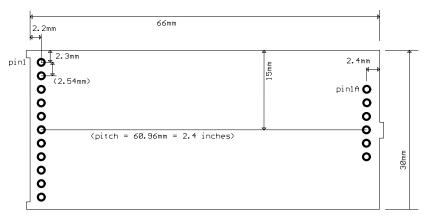


Figure 3: FPX3 footprint (Top) view

# **Pin Description**

Pins		Normal mode
RF		
1A	NO PIN	
2A	RF in/out	To the antenna
3A	RF Gnd	RF ground
4A	GND	0V
5A	GND	0V
6A	Vcc (TX)	+5V regulated supply for TX <sup>7</sup>
User		
1	TXD	DC coupled input for 5v pp CMOS logic waveforms
2	TXE	Transmit enable.
		Low = TX mode
3	GND	OV
4	Vcc	DC supply input (+5V regulated) <sup>8</sup>
5	P3	Parallel channel select
6	P2	Parallel channel select
7	P1	Parallel channel select
8	PGM in	Serial programming/control input (inverted logic level RS232)
9	RSSI	DC level between 0.5V and 2.5V. 60dB dynamic range
10	RXD	Open collector output of data slicer. 47K pullup to 4V
11	AF out	500mV p-p audio. DC coupled, approx 0.8V bias

### **NOTES:**

- 1. No inversion occurs between TXD and RXD.
- 2. P1 P3 (parallel channel selects) and N\_TXE are active LOW and have internal 47K pullups to 4v
- 3. RXD is an open collector output, with a weak pullup to 4v (approx 50K)
- 4. This radio has no 'internal modem' functions in this version.
- Programming port (P0) operates at 2400 baud. (Command set specific to this design). The unit is capable of operating in 'basic' parallel channel select only mode, or in serial controlled 'expanded' mode
- 6. The high current (500mA maximum) transmitter supply (5v regulated) is fed to a separate pin, on the RF out connector.
  - The V supply (pin 4 on the user connector) also requires a 5v regulated supply, and draws up to 50mA
  - Both pins must be supplied at all times (most users connect them to a common +5v supply)
- 7. 10% duty cycle: While the FPX3 is capable of transmitting continuously (the case temperature will rise noticeably) this will be in contravention of the sub-band restrictions. Units operating in the high power 869.4-869.65MHz sub-band are limited to 10% maximum transmit duty cycle. It is the responsibility of the user to ensure that this restriction is complied with.

Alternatively, the user may wish to implement a listen-before-talk protocol (using the RSSI output). See EN300-220 for further details

### Serial interface commands

Serial data is sent to the unit on the programming input (PGM). To successfully program the unit, it must be powered

Unlike earlier Radiometrix multichannel units, the FPX3 has separate N and R registers for each channel, on transmit and on receive. In other words, the frequency of each channel is individually programmed in every mode.

### Commands include:

Commands	Function
GOCHAN a	serial select of channel a (ch0 to 7)
SETPAR	channel selected by 3 bit parallel input
SETSER	channel selected by most recent 'gochan' operation
<cr></cr>	process entry
1	clear all buffers
#	disable command mode

Most users will not need to edit the table which relates channel frequency, channel number and synthesizer register programming values. The commands below are used to access the eeprom table containing this information.

Users should be warned that, in order to get the optimum switching speed from the module, the standard channel setup uses a different reference frequency for each channel, and the absolute calculated channel frequency is allowed to deviate by up to +/-2KHz from the nominal

LOAD aa nnnnn (aa = 0 - 7)	set value of N for channel a , for receive
LOAD aa nnnnn (aa = 8 - 15)	set value of N for channel a , for transmit
LOAD aa rrrrr (aa = 16 – 23)	set value of R for channel a , for receive
LOAD aa rrrrr (aa = 24 – 31)	set value of R for channel a , for transmit

Reference frequency (10MHz/R) must be in the range 210 to 300KHz (tx) or 175 to 300KHz (rx). As a rule of thumb, an R value greater than 45 (tx) or 55 (rx) should be viewed with suspicion.

a = single digit channel number from 0 to 7 aa = eprom location number from 0 to 31 nnnnn = a synthesizer N register value, (up to 65535) rrrrr = the synthesizer R register value, (up to 16383)

### ALL COMMANDS MUST BE IN UPPER CASE.

Notice that the synthesizer and VCO operate at half the output frequency, so for a frequency of 869.475MHz, the actual N/R values will be set for 434.7375MHz

Further information TBA

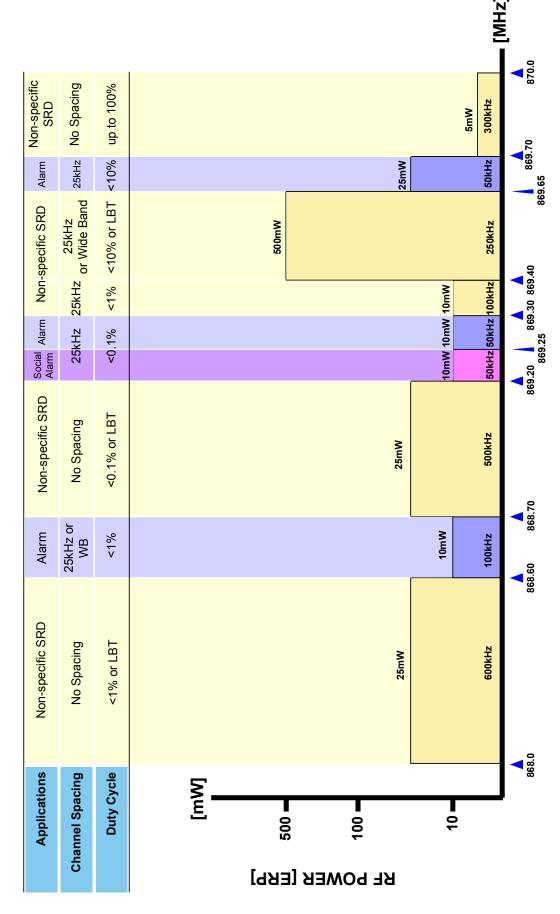
### NOTE:

A pause of at least 50mS must be allowed between command strings (eeprom programming time)

# **Condensed specifications** (All details are provisional)

Frequency stability +/-5ppm (better than ±5kHz)  Channel spacing 100kHz  Number of channels 8 maximum  This version 2 usable  Ch0 869.475MHz  Ch1 869.565MHz (also Ch2-3)  (Chan 4-7 are programmed for 869.50MHz, for backward compati	
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Ch1 869.565MHz (also Ch2-3)	
(Chan 4-7 are programmed for 869.50MHz, for backward compati	
	bility)
Supply Receive 5.0V regulated at 50mA	
Transmit: 5.0V regulated at 500mA for 400mW output	
Operating temperature -20 to +70 °C (Storage -30 to +70 °C)	
Spurious radiations Compliant with ETSI EN 300 220-3 and EN 301 489-3	
Interface	
User 11 pin 0.1" pitch molex	
RF and TX supply 5 pin 0.1" pitch molex	
Recommended PCB hole size 1.2mm (min.)	
Size 67 x 30 x 12mm	
Transmitter	
Output power 400mW (+26dBm)	
TX on switching time 5 ms	
Modulation type FM, FSK (F1D, F3D)	
TX modulation bandwidth DC – 10kHz	
Deviation ±16kHz nominal	
Adjacent channel TX power -37dBm	
TX spurii <-36dBm	
Inputs 5V pk-pk CMOS data	
Receiver	
Sensitivity -115dBm for 12dB SINAD at 1KHz	
-107dBm for <0.1% data errors (20Kbit/sec NRZ)	
image / spurious -55dB	
blocking -80dB	
adjacent channel -60dB (Tested per. ETSI 300-220-2.3.1)	
Outputs RSSI, Audio, Data	

# CEPT/ERC Rec 70-03, 868 MHz Band Plan



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The Intrastat commodity code for all our modules is: 8542 6000

# **R&TTE Directive**

After 7 April 2001 the manufacturer can only place finished product on the market under the provisions of the R&TTE Directive. Equipment within the scope of the R&TTE Directive may demonstrate compliance to the essential requirements specified in Article 3 of the Directive, as appropriate to the particular equipment.

Further details are available on The Office of Communications (Ofcom) web site: <a href="http://www.ofcom.org.uk/">http://www.ofcom.org.uk/</a>

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