

Optocoupler, Photodarlington Output, AC Input, High Gain (Single, Dual Channel)

Features

- AC or Polarity Insensitive Inputs
- · Built-in Reverse Polarity Input Protection
- · Industry Standard DIP Package
- · Lead-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

Agency Approvals

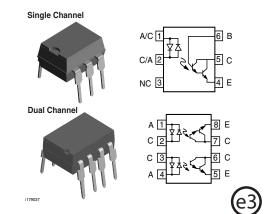
- UL1577, File No. E52744 System Code H or J, Double Protection
- CSA 93751
- BSI IEC60950 IEC60065
- DIN EN 60747-5-2 (VDE0884)
 DIN EN 60747-5-5 pending
 Available with Option 1

Applications

Designed for applications requiring detection or monitoring of AC signals.

Description

The IL755/ ILD755 are bidirectional input optically coupled isolators. They consist of two Gallium Arsenide infrared emitting diodes coupled to a silicon



NPN photodarlington per channel.

The IL755 is single channel Darlington optocouplers. The ILD755 has two isolated channels in a single DIP package.

Order Information

Part	Remarks
IL755-1	CTR > 750 %, DIP-6
IL755-2	CTR > 1000 %, DIP-6
ILD755-1	CTR > 750 %, DIP-8
ILD755-2	CTR > 1000 %, DIP-8
IL755-1X007	CTR > 750 %, SMD-6 (option 7)

For additional information on the available options refer to Option Information.

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

Input

Parameter	Test condition	Symbol	Value	Unit
Forward continuous current		I _F	60	mA
Power dissipation		P _{diss}	100	mW
Derate linearly from 25 °C			1.33	mW/°C

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IL755/ ILD755

Vishay Semiconductors



Output

Parameter	Test condition	Part	Symbol	Value	Unit
Collector-emitter breakdown voltage			BV _{CEO}	60	V
Collector-base breakdown voltage			BV _{CBO}	60	V
Power dissipation		IL755-1	P _{diss}	200	mW
		IL755-2	P _{diss}	200	mW
		ILD755-1	P _{diss}	150	mW
		ILD755-2	P _{diss}	150	mW
Derate linearly from 25 °C		IL755-1		2.6	mW/°C
		IL755-2		2.6	mW/°C
		ILD755-1		2.0	mW/°C
		ILD755-2		2.0	mW/°C

Coupler

Parameter	Test condition	Part	Symbol	Value	Unit
Isolation test voltage (PK)	t = 1.0 sec.		V _{ISO}	7500/5300	VAC _{PK} /V _{RMS}
Total power dissipation (LED plus detector)		IL755-1	P _{tot}	250	mW
		IL755-2	P _{tot}	250	mW
		ILD755-1	P _{tot}	400	mW
		ILD755-2	P _{tot}	400	mW
Derate linearly from 25 °C		IL755-1		3.0	mW/°C
		IL755-2		3.0	mW/°C
		ILD755-1		3.0	mW/°C
		ILD755-2		3.0	mW/°C
Creepage				≥ 7	mm
Clearance				≥ 7	mm
Storage temperature			T _{stg}	- 55 to + 150	°C
Operating temperature			T _{amb}	- 55 to + 100	°C
Lead soldering time at 260 °C				10	sec.

Electrical Characteristics

 T_{amb} = 25 °C, unless otherwise specified

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

Input

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Forward voltage	$I_F = \pm 10 \text{ mA}$	V_{F}		1.2	1.5	V

Output

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Collector-emitter breakdown voltage	I _C = 1.0 mA	BV _{CEO}	60	75		V
Collector-base breakdown voltage	I _C = 10 μA	BV _{CBO}	60	90		V
Collector-emitter leakage current	V _{CE} = 10 V	I _{CEO}		10	100	nA



Coupler

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Collector-emitter saturation voltage	$I_F = \pm 10 \text{ mA},$ $I_C = 10 \text{ mA}$	V _{CE(sat)}			1.0	V

Current Transfer Ratio

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Current Transfer Ratio	$V_{CE} = 5.0 \text{ V}, I_F = \pm 2.0 \text{ mA}$	IL755-1	CTR	750			%
		ILD755-1	CTR	750			%
	$V_{CE} = 5.0 \text{ V}, I_F = \pm 1.0 \text{ mA}$	IL755-2	CTR	1000			%
		ILD755-2	CTR	1000			%

Switching Characteristics

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Rise time	$V_{CC} = 10 \text{ V}, I_F = 2.0 \text{ mA}, R_L = 100 \Omega$	IL755-1	t _r		50		μS
		ILD755-1	t _r		50		μS
Fall time	V_{CC} = 10 V, I_F = 2.0 mA, R_L = 100 Ω	IL755-1	t _f		50		μS
		ILD755-1	t _f		50		μS
Rise time	V_{CC} = 10 V, I_F = 1.0 mA, R_L = 100 Ω	IL755-2	t _r		70		μS
		ILD755-2	t _r		70		μS
Fall time	V_{CC} = 10 V, I_F = 1.0 mA, R_L = 100 Ω	IL755-2	t _f		70		μS
		ILD755-2	t _f		70		μS

Typical Characteristics (Tamb = 25 °C unless otherwise specified)

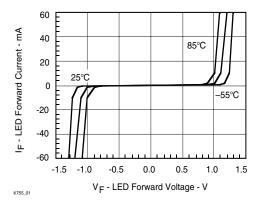


Figure 1. LED Forward Current vs.Forward Voltage

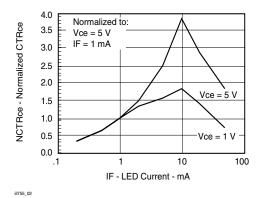


Figure 2. Normalized Non-saturated and Saturated CTR $_{\rm CE}$ vs. LED Current



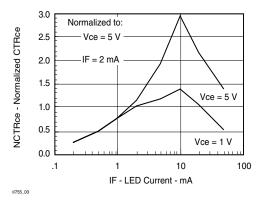


Figure 3. Normalized Non-saturated and Saturated CTR $_{\mbox{\scriptsize CE}}$ vs. LED Current

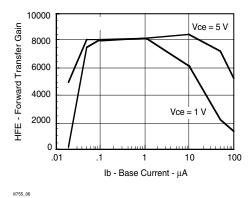


Figure 6. Non-Saturated and Saturated HFE vs. Base Current

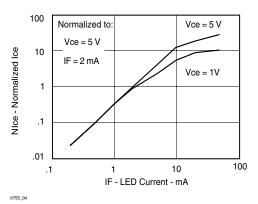


Figure 4. Normalized Non-Saturated and Saturated I_{CE} vs. LED Current

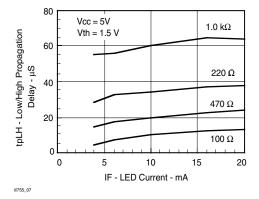


Figure 7. Low to High Propagation Delay vs. Collector Load Resistance and LED Current

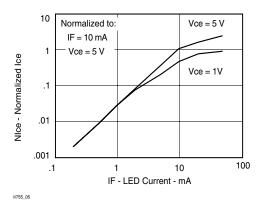


Figure 5. Normalized Non-Saturated and Saturated Collector-Emitter Current vs. LED Current

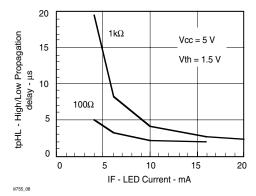


Figure 8. High to low Propagation Delay vs. Collector Load Resistance and LED Current

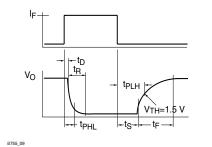
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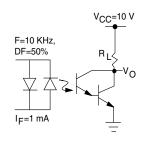
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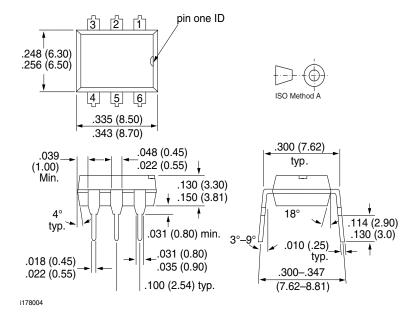


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Figure 9. Switching Waveform

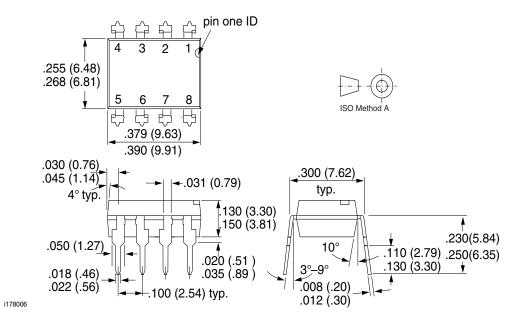
Figure 10. Normalized Non-Saturated and Saturated CTR $_{\rm CE}$ vs. LED Current

Package Dimensions in Inches (mm)



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Package Dimensions in Inches (mm)



Option 7
.300 (7.62)
TYP
.255 (6.5)
.248 (6.3)
.180 (4.6)
.160 (4.1)
.315 (8.0)
.MIN.
.331 (8.4)
.MIN.
.406 (10.3)
.408 (10.3)
.408 (10.3)

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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively. Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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