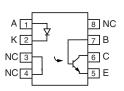


**Vishay Semiconductors** 

## Optocoupler, Phototransistor Output, with Base Connection in SOIC-8 Package





#### DESCRIPTION

The VO205AT, VO206AT, VO207AT, VO208AT are optically coupled pairs with a gallium arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. This family comes in a standard SOIC-8A small outline package for surface mounting which makes them ideally suited for high density application with limited space.

### FEATURES

- High BV<sub>CEO</sub>, 70 V
- Isolation test voltage, 4000 V<sub>RMS</sub>
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### AGENCY APPROVALS

- UL1577, file no. E52744 system code Y
- CUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5 (VDE 0884) available with option 1

ORDER INFORMATION	
PART	REMARKS
VO205AT	CTR 40 % to 80 %, SOIC-8
VO206AT	CTR 63 % to 125 %, SOIC-8
VO207AT	CTR 100 % to 200 %, SOIC-8
VO208AT	CTR 160 % to 320 %, SOIC-8

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Peak reverse voltage		V <sub>R</sub>	6	V
Peak forward current	1 µs, 300 pps	I <sub>FM</sub>	1	А
Forward continuous current		I <sub>F</sub>	60	mA
Power dissipation		P <sub>diss</sub>	90	mW
Derate linearly from 25 °C			1.2	mW/°C
OUTPUT				
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7	V
Collector-base breakdown voltage		BV <sub>CBO</sub>	70	V
I <sub>Cmax. DC</sub>		I <sub>Cmax. DC</sub>	50	mA
I <sub>Cmax.</sub>	t < 1 ms	I <sub>Cmax.</sub>	100	mA
Power dissipation		P <sub>diss</sub>	150	mW
Derate linearly from 25 °C			2	mW/°C
COUPLER				
Isolation test voltage		V <sub>ISO</sub>	4000	V <sub>RMS</sub>
Total package dissipation (LED and detector)		P <sub>tot</sub>	240	mW
Derate linearly from 25 °C			3.3	mW/°C
Operating temperature		T <sub>amb</sub>	- 40 to + 100	°C
Storage temperature		T <sub>stg</sub>	- 40 to + 150	°C
Soldering time	at 260 °C	T <sub>sld</sub>	10	S

Note

 $T_{amb} = 25$  °C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

Document Number: 81957 Rev. 1.0, 02-Dec-08

# VISHAY.

## Vishay Semiconductors Optocoupler, Phototransistor Output, with Base Connection in SOIC-8 Package

ELECTRICAL CHARACTERISTCS									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>		1.3	1.5	V		
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>		0.1	100	μΑ		
Capacitance	V <sub>R</sub> = 0 V		Co		13		pF		
OUTPUT									
Collector emitter breakdown voltage	I <sub>C</sub> = 100 μA		BV <sub>CEO</sub>	70			V		
Emitter collector breakdown voltage	I <sub>E</sub> = 10 μA		BV <sub>ECO</sub>	7	10		V		
Collector base breakdown voltage	I <sub>C</sub> = 100 μA		BV <sub>CBO</sub>	100			V		
Collector base current			I <sub>CBO</sub>			1	nA		
Emitter base current			I <sub>EBO</sub>			1	nA		
Collector emitter leakage current	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>		5	50	nA		
Saturation voltage, collector emitter	$I_{C} = 2 \text{ mA}, I_{F} = 10 \text{ mA}$		V <sub>CEsat</sub>			0.4	V		
COUPLER		•			•	•	•		
Capacitance, input to output			C <sub>IO</sub>		0.5		pF		

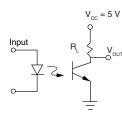
#### Note

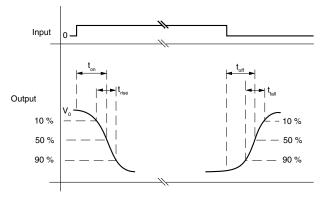
 $T_{amb}$  = 25 °C, unless otherwise specified.

Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Current transfer ratio		VO205AT	CTR	40		80	%	
		VO206AT	CTR	63		125	%	
	l <sub>F</sub> = 10 mA, V <sub>CE</sub> = 5 V	VO207AT	CTR	100		200	%	
		VO208AT	CTR	160		320	%	

SWITCHING CHARACTER	ISTICS						
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega, \text{ V}_{CC} = 10 \text{ V}$		t <sub>on</sub>		3		μs
Turn-off time	$I_C=2 \text{ mA},  \text{R}_L=100  \Omega,  \text{V}_{CC}=10  \text{V}$		t <sub>off</sub>		3		μs
Rise time	$I_C = 2 \text{ mA},  \text{R}_L = 100 \ \Omega,  \text{V}_{CC} = 10 \text{ V}$		t <sub>r</sub>		3		μs
Fall time	$I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , $V_{CC}$ = 10 V		t <sub>f</sub>		2		μs





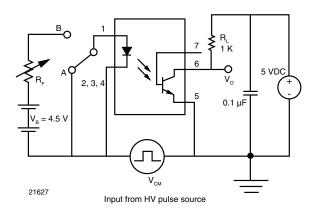
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#### Fig. 1 - Switching Test Circuit



Optocoupler, Phototransistor Output, with Vishay Semiconductors Base Connection in SOIC-8 Package

COMMON MODE TRANSIENT IMMUNITY							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Common mode transient immunity at logic high	$\label{eq:VCM} \begin{split} V_{CM} &= 1000 \; V_{P\text{-}P},  R_L = 1 \; k\Omega, \\ I_F &= 0 \; \text{mA} \end{split}$	С <sub>МН</sub>		5000		V/µs	
Common mode transient immunity at logic low	$V_{CM}$ = 1000 $V_{P-P}$ , $R_L$ = 1 k $\Omega$ , I <sub>F</sub> = 10 mA	C <sub>ML</sub>		5000		V/µs	



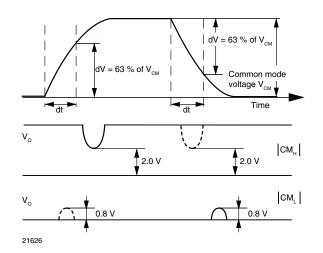


Fig. 2 - Test Circuit for Common Mode Transient Immunity

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				40/100/21		
Polution degree				2		
Comparative tracking index		CTI	175		399	
Isolation test voltage	1 s	V <sub>ISO</sub>	4000			V <sub>RMS</sub>
Peak transient overvoltage		V <sub>IOTM</sub>	6000			V
Peak insulation voltage		V <sub>IORM</sub>	560			V
Resistance (input to output)		R <sub>IO</sub>		100		GΩ
Safety rating - power output		P <sub>SO</sub>			350	mW
Safety rating - input current		I <sub>SI</sub>			150	mA
Safety rating - temperature		T <sub>SI</sub>			165	°C
External creepage distance			4			mm
External clearance distance			4			mm
Internal creepage distance			3.3			mm
Insulation thickness			0.2			mm

#### Note

As per IEC 60747-5-2, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

Vishay Semiconductors Optocoupler, Phototransistor Output, with Base Connection in SOIC-8 Package



## **TYPICAL CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

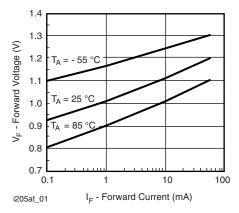


Fig. 3 - Forward Voltage vs. Forward Current

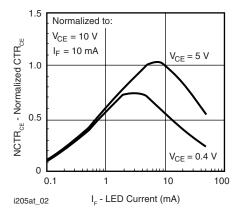
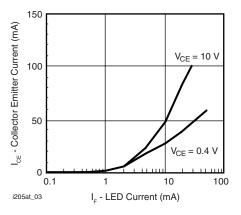
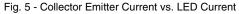


Fig. 4 - Normalized Non-Saturated and Saturated CTR<sub>CE</sub> vs. LED Current





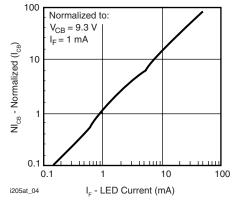


Fig. 6 - Normalized Collector-Base Photocurrent vs. LED Current

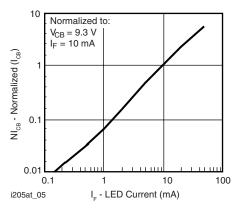


Fig. 7 - Normalized Collector-Base Photocurrent vs. LED Current

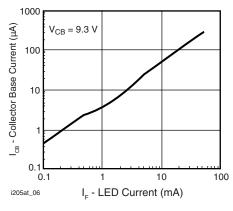
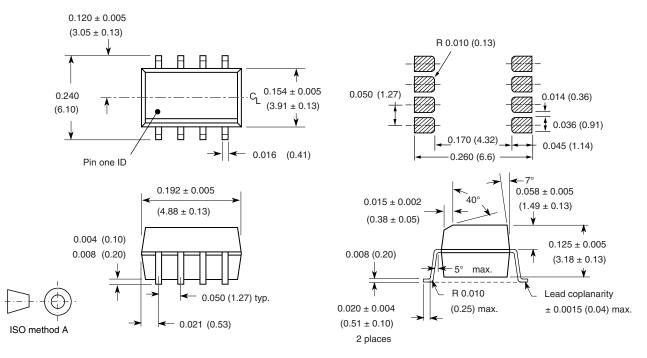


Fig. 8 - Collector Base Photocurrent vs. LED Current



## Optocoupler, Phototransistor Output, with Vishay Semiconductors Base Connection in SOIC-8 Package

## **PACKAGE DIMENSIONS** in inches (millimeters)



i178003

Vishay Semiconductors Optocoupler, Phototransistor Output, with Base Connection in SOIC-8 Package



#### **OZONE DEPLETING SUBSTANCES POLICY STATEMENT**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



Vishay

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