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# ILD32, ILQ32

**Vishay Semiconductors** 

# **Optocoupler, Photodarlington Output,** High Gain (Dual, Quad Channel)

## **FEATURES**

- Isolation test voltage, 5300 V<sub>RMS</sub>
- High isolation resistance, 10<sup>11</sup>Ω typical
- · Low coupling capacitance
- Standard plastic DIP package
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

## AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1
- BSI IEC 60950; IEC 60065

#### DESCRIPTION

i179017

The ILD32, ILQ32 are optically coupled isolators with a gallium arsenide infrared LED and a silicon photodarlington sensor. Switching can be achieved while maintaining a high degree of isolation between driving and load circuits.

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These optocouplers can be used to replace reed and mercury relays with advantages of long life, high speed switching and elimination of magnetic fields.

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ORDERING INFORMATION							
I   L   x   3   2   PART NUMBER	-     X     0     #     #       PACKAGE OPTION	TAPE AND REEL DIP-# Option 6					
x = D (Dual) or Q (Quad)		Option 7 Option 9					
AGENCY CERTIFIED/PACKAGE	DUAL CHANNEL	QUAD CHANNEL					
AGENCT CERTIFIED/PACKAGE	CTR (%)						
UL, BSI	≥ 500	≥ 500					
DIP-8	ILD32	-					
SMD-8, option 7	ILD32-X007T <sup>(1)</sup>	-					
SMD-8, option 9	ILD32-X009	-					
DIP-16	-	ILQ32					
SMD-16, option 7	-	ILQ32-X007T <sup>(1)</sup>					
SMD-16, option 9	-	ILQ32-X009T <sup>(1)</sup>					
VDE, BSI	≥ 500	≥ 500					
DIP-8, 400 mil, option 6	ILD32-X016	-					
DIP-16		ILQ32-X001					

#### Notes

Additional options may be possible, please contact sales office.

<sup>(1)</sup> Also available in tubes, do not put T on the end.

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For technical questions, contact: optocoupleranswers@vishay.com

www.vishay.com





COMPLIANT





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ABSOLUTE MAXIMUM F					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT			-	<u>.                                    </u>	
Peak reverse voltage			V <sub>R</sub>	3	V
Forward continuous current			I <sub>F</sub>	60	mA
Power dissipation			P <sub>diss</sub>	100	mW
Derate linearly from 25°C				1.33	mW/°C
OUTPUT					
Collector emitter breakdown voltage			BV <sub>CEO</sub>	30	V
Collector (load) current			Ι <sub>C</sub>	125	mA
Power dissipation			P <sub>diss</sub>	150	mW
Derate linearly from 25°C				2	mW/°C
COUPLER					
Isolation test voltage between emitter and detector	t = 1 s		V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Creepage distance				≥ 7	mm
Clearance distance				≥ 7	mm
Comparative tracking index per DIN IEC 112/VDE 0303, part 1			СТІ	≥ 175	
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$		R <sub>IO</sub>	10 <sup>12</sup>	Ω
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$		R <sub>IO</sub>	10 <sup>11</sup>	Ω
Total discipation		ILD32	P <sub>tot</sub>	400	mW
Total dissipation		ILQ32	P <sub>tot</sub>	500	mW
Devete linearly from 25 %		ILD32		5.33	mW/°C
Derate linearly from 25 °C		ILQ32		6.67	mW/°C
Storage temperature			T <sub>stg</sub>	- 55 to + 150	°C
Operating temperature			T <sub>amb</sub>	- 55 to + 100	°C
Lead soldering time at 260 °C				10	S

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>		1.25	1.5	V
Reverse current	V <sub>R</sub> = 3 V	I <sub>R</sub>		0.1	100	μA
Capacitance	$V_{R} = 0 V$	Co		25		pF
OUTPUT						
Collector emitter breakdown voltage	$I_{C}$ = 100 $\mu$ A, $I_{F}$ = 0 A	BV <sub>CEO</sub>	30			V
Breakdown voltage emitter collector	I <sub>E</sub> = 100 μA	BC <sub>ECO</sub>	5	10		V
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>		1	100	nA
COUPLER						
Collector emitter	$I_{\rm C} = 2$ mA, $I_{\rm F} = 8$ mA	V <sub>CEsat</sub>			1	V
Capacitance (input to output)		C <sub>IO</sub>		0.5		pF

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



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CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_{\rm F}$ = 10 mA, $V_{\rm CE}$ = 10 V	CTR	500			%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 10 \; V, \; I_{F} = 5 \; mA, \\ R_{L} = 100 \; \; \Omega \end{array}$	t <sub>on</sub>		15		μs
Turn-off time	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 10 \; V, \; I_{F} = 5 \; mA, \\ R_{L} = 100 \; \; \Omega \end{array}$	t <sub>off</sub>		30		μs

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
V <sub>IOTM</sub>			10 000			V
V <sub>IORM</sub>			890			V
P <sub>SO</sub>					400	mW
I <sub>SI</sub>					275	mA
T <sub>SI</sub>					175	°C
Creepage distance	standard DIP-8		7			mm
Clearance distance	standard DIP-8		7			mm
Creepage distance	400 mil DIP-8		8			mm
Clearance distance	400 mil DIP-8		8			mm
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.4			mm

Note

• As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

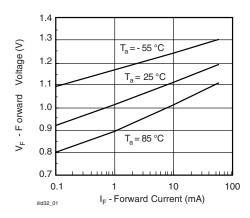
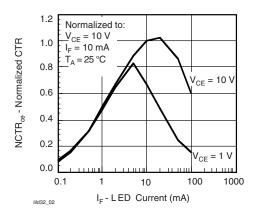
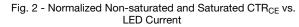


Fig. 1 - Forward Voltage vs. Forward Current



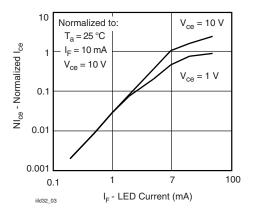


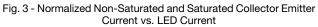
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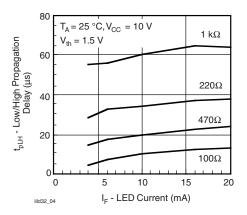


Fig. 4 - Low to High Propagation Delay vs. Collector Load Resistance and LED Current

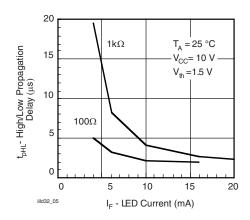
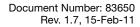
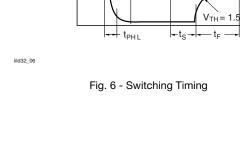


Fig. 5 - High to low Propagation Delay vs. Collector Load Resistance and LED Current





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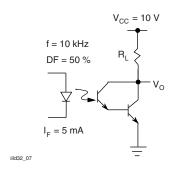
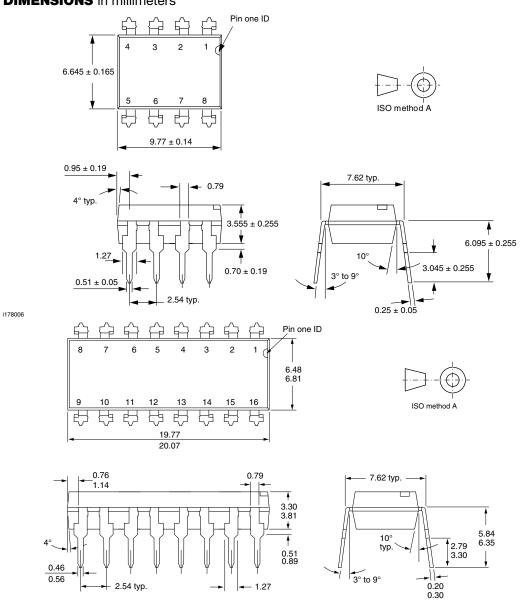


Fig. 7 - Switching Schematic



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PACKAGE DIMENSIONS in millimeters



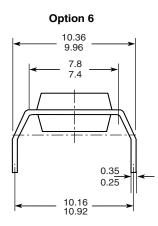
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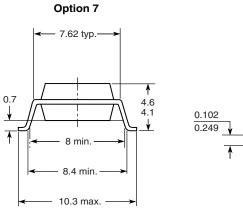
# ILD32, ILQ32

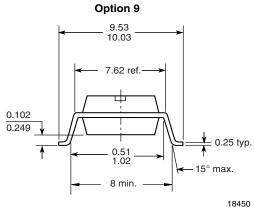
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## **Vishay Semiconductors**

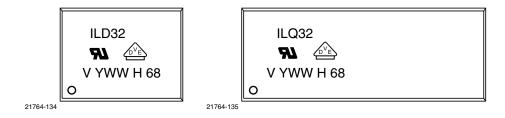
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#### PACKAGE MARKING (example)



#### Notes

- Only options 1, and 7 reflected in the package marking.
- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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