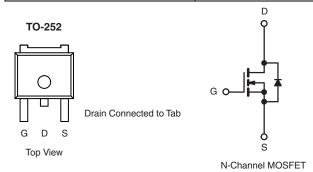


Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	40		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.010		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.014		
I _D (A)	42		
Configuration	Single		



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- AEC-Q101 Qualified^d
- Compliant to RoHS Directive 2002/95/EC
- Find out more about Vishay's Automotive Grade Product Requirements at: www.vishay.com/applications



FREE

ORDERING INFORMATION	
Package	TO-252
Lead (Pb)-free and Halogen-free	SQD40N04-10A-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	40	V	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current ^a	T _C = 25 °C	1	42		
	T _C = 125 °C	l _D	35	Δ.	
Continuous Source Current (Diode Conduction) ^a		I _S	42	A	
Pulsed Drain Current ^b		I _{DM}	168		
Single Pulse Avalanche Energy	1 0.1 ml l	E _{AS}	45	mJ	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	30	А	
Maximum Power Dissipation ^b	T _C = 25 °C	D	71	W	
	T _C = 125 °C	P_{D}	24	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	50	°C/W	
unction-to-Case (Drain)		R _{thJC}	2.1	C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

SQD40N04-10A

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SPECIFICATIONS ($T_C = 25 ^{\circ}C_{\odot}$				1	1	1	1
PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$		1.5	2.0	2.5	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 40 V	-	-	1.0	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = 40 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	50	μΑ
		$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 175 °C	-	-	150]
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α
		V _{GS} = 10 V	I _D = 20 A	-	0.006	0.010	Ω
Dunin Course On State Resistance?		V _{GS} = 10 V	I _D = 20 A, T _J = 125 °C	-	-	0.016	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 20 A, T _J = 175 °C	-	-	0.019	
		V _{GS} = 4.5 V	I _D = 20 A	-	0.011	0.014	
Forward Transconductance ^b	9 _{fs}	V_{DS}	= 15 V, I _D = 40 A	-	58	-	S
Dynamic ^b							
Input Capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = 25 V, f = 1 MHz	-	1755	2190	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	385	480	
Reverse Transfer Capacitance	C _{rss}			-	250	315	
Total Gate Charge ^c	Qg	V _{GS} = 10 V	V _{GS} = 10 V V _{DS} = 20 V, I _D = 40 A	-	46	70	nC
Gate-Source Charge ^c	Q _{gs}			-	6.2	-	
Gate-Drain Charge ^c	Q _{gd}			-	13.5	-	
Turn-On Delay Time ^c	t _{d(on)}	$V_{DD} = 20 \text{ V}, \text{ R}_L = 0.5 \Omega$ $I_D \cong 40 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		-	7	10	
Rise Time ^c	t _r			-	10	15	- ns
Turn-Off Delay Time ^c	t _{d(off)}			-	25	37	
Fall Time ^c	t _f			-	9	14	
Source-Drain Diode Ratings and Char	acteristics ^b	•					
Pulsed Current ^a	I _{SM}			-	-	168	Α
Forward Voltage	V _{SD}	I _F = 40 A, V _{GS} = 0 V		-	0.8	1.2	V

Notes

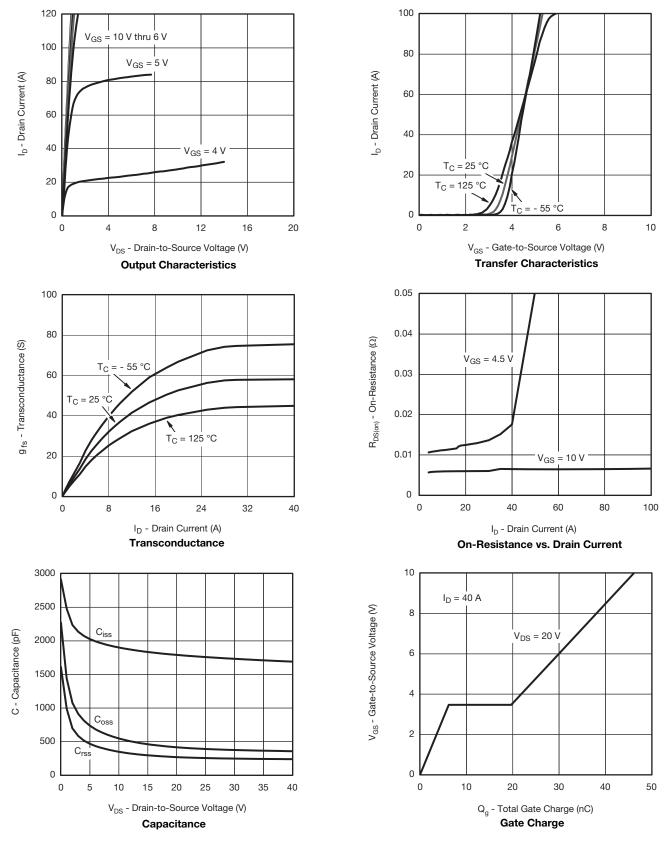
- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





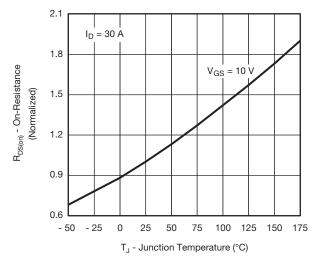
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



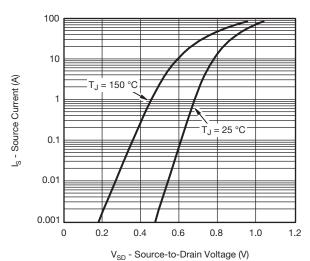
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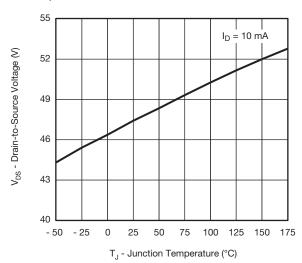
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



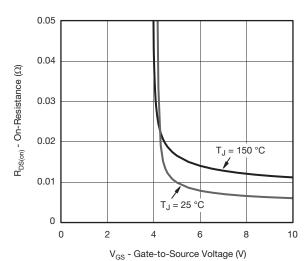
On-Resistance vs. Junction Temperature



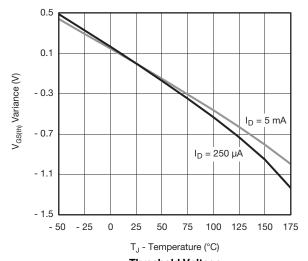
Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



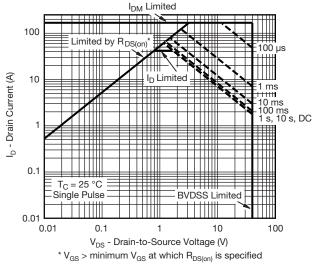
On-Resistance vs. Gate-to-Source Voltage



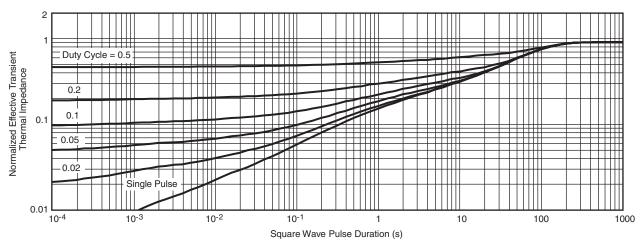
Threshold Voltage



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Safe Operating Area

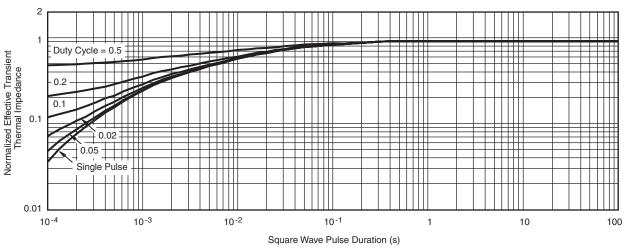


Normalized Thermal Transient Impedance, Junction-to-Ambient

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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg268847.

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