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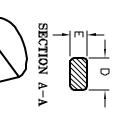
SPC-FODS.DWB

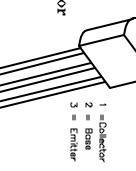
모 무 * 1820 쯴 > REVISIONS DESCRIPTION RELEASED H

DRAWN DD: No. 3/24/05 DATE SPC-FD05 * Effective: 7/8/02 * DCP No. 1398 SESS Ē 04/25/08 DATE APPRVD ź 04/25/08 DATE

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K	Ħ	G	F	I	D	С	Ħ	Δ	DIM.
12.70	1.14	1.14	5	0.35	0.41	3.18	4,45	4.32	MIN.
ı	1.53	1.40	DEG	0.50	0.55	4.19	5.20	5.33	MAX.





Features:

approach.

No External Components Required

current level is needed. This device offers a substantial advantage over the common resistor/zener diode

A useful for on-card regulation or any other application where a regulated negative voltage at a modest

requiring up to 100mA. This device features thermal shutdown and current limiting making the device

A negative 3-terminal voltage regulator in a TO92 type package suitable for numerous applications

remarkably rugged. In most applications, no external components are required for operation.

- Internal Short-Circuit Current Limiting
- Internal Thermal Overload Protection

Absolute Maximum Ratings:

Range, Tstg-55°C to +150°CLead Temperature (During Soldering, 10sec., TL+300°C Input Voltage, VIN-40VInternal Power Dissipation (Note 1), PDInternally LimitedOperating Junction Temperature Range, Topr0°C to +70°CMaximum Junction Temperature, TJ+125°CStorage Temperature

Parameter	Symbol	Symbol Test Conditions	Min	ı Typ I	Max Units	Units
Output Voltage	8	$T_J = +25^{\circ}C$	23.0	23.0 24.0 25.0	25.0	٧
		1mA O I_{0} O 100mA, 27V O V_{IN} O 38V	22.8	22.8 24.0 25.2	25.2	٧
Line Regulation	Regline	$T_{\rm J} = +25^{\circ}{\rm C}$, 27V o V _{IN} o 38V	ı	ı	350	m۷
Load Regulation		$T_{\rm J}$ = +25°C, 1mA O $I_{\rm O}$ O 100mA	Ι	-	200	m۷
Quiescent Current	Ŧ	$T_{\rm J} = +125^{\circ}{\rm C}$	I	I	6	mA
Quiescent Current Change	¥.	With line, 28V O V _{IN} O 38V	I	I	1,5	mА
		With load, 1mA O I_0 O 40mA	I	I	0.1	mA
Output Noise Voltage	ź	$T_{u} = +25^{\circ}C_{s}$ f = 10Hz to 10kHz	I	200	I	٧ų
Ripple Rejection	72 72	29V O V_{IN} O 35V, f = 120Hz	31	47	ı	dВ
Drop Out Voltage	V _{DO}	$T_{i} = +25^{\circ}C, I_{0} = 40 \text{mA}$	ı	1.7	I	<

Notes:

Emitter

- 1— Thermal resistance, junction—to—ambient is +180°C/W when mounted with 0.40 inch leads on a P.C. board, and +160°C/W when mounted with 0.25 inch leads on a P.C. board.
 2— To ensure constant junction temperature, low duty cycle pulse testing is used.

	<u>점</u> [FIR REFERENCE ARE	SPECIFIED,	ALL STATEMENTS AND TECHNICAL INFORMATION CONTAINED UNLESS	TOLERANCES:
	PURPOSES ONLY.	FIR REFERENCE ARE	[] 	UNLESS OTHERWISE	ËS:
Jason Nash	APPROVED BY:	Jason Nash	CHECKED BY:	HSIDO WAHSIH	DRAWN BY:
04/25/08	DATE:	04/25/08	DATE:	3/24/05	DATE:
SCALE:		>	SIZE	Voltag	DRAWIN
SCALE: NTS U.O.M.: MI		2N5401	DWG. NO.	je Regulator,	,
J.O.M.: MILLIMETERS				ar, Plastic	! :
SHEET: 1 OF		35C0724.DWG	ELECTRONIC FILE	Bipolar, Plastic, TO-92, Positive	
		>	REV	ve	