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SPC-F005.DWG

REVISIONS

DOC. NO. SPC-F005 * Effective: 7/8/02 * DCP No: 1398

DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1885	A	RELEASED	BYF	02/08/06	HO	2/6/06	JWM	2/6/06

Description:

A general purpose, medium power silicon PNP transistor in a TO-220 type package designed for switching and amplifier applications. This device is especially designed for series and shunt regulators and as a driver and output stage of high-fidelity amplifiers.



Features:

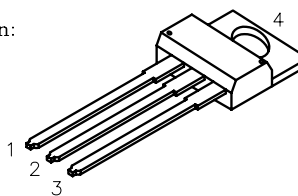
- Low Saturation Voltage.

Absolute Maximum Ratings:

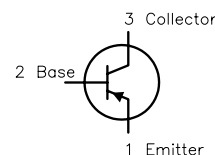
- Collector-Base Voltage, $V_{CBO} = 100V$
- Collector-Emitter Voltage, $V_{CEO} = 100V$
- Emitter-Base Voltage, $V_{EBO} = 5V$
- Continuous Collector Current = 3A
- Continuous Base Current = 1A
- Total Device Dissipation ($T_C = +25^\circ C$), $P_D = 40W$
Derate Linearly Above $25^\circ C = 0.23W/^\circ C$
- Total Device Dissipation ($T_A = +25^\circ C$), $P_D = 2W$
Derate Linearly Above $25^\circ C = 0.016W/^\circ C$
- Operating Junction Temperature Range, $T_{opr} = -65^\circ$ to $+150^\circ C$
- Storage Temperature Range, $T_{stg} = -65^\circ$ to $+150^\circ C$
- Lead Temperature (During Soldering, $1/8"$ (3.17mm) from case, 10sec max), $T_L = +235^\circ C$
- Thermal Resistance Junction-to-Case, $R_{thjc} = 3.125^\circ C/W$
- Thermal Resistance Junction-to-Ambient, $R_{thja} = 62.5^\circ C/W$

Pin Configuration:

1. Base
2. Collector
3. Emitter
4. Collector



PNP

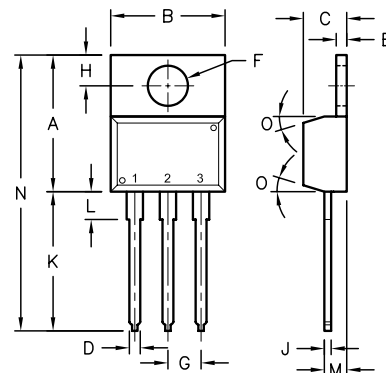


Dimensions	A	B	C	D	E	F	G	H	J	K	L	M	N	O
Min.	14.42	9.63	3.56	-	1.15	3.75	2.29	2.54	-	12.70	2.80	2.03	-	7
Max.	16.51	10.67	4.83	0.90	1.40	3.88	2.79	3.43	0.56	14.73	4.07	2.92	31.24	

Electrical Characteristics: ($T_C = +25^\circ C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Max	Unit
Collector Cutoff Current	I_{CEO}	$V_{CE} = 60V, I_B = 0$	-	0.3	mA
Emitter Cutoff Current	I_{EBO}	$V_{EB} = -5V, I_C = 0$	-	1	mA
Collector-Emitter Sustaining Voltage	$V_{CEO(sus)}$	$I_C = 30mA, I_B = 0, (Note 1)$	100	-	V
DC Current Gain	h_{FE}	$I_C = 1A, V_{CE} = 4V, (Note 1)$	25	-	-
		$I_C = 3A, V_{CE} = 4V, (Note 1)$	10	50	-
Base-Emitter Voltage	$V_{BE(on)}$	$I_C = 3A, V_{CE} = 4V, (Note 1)$	-	1.8	V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 3A, I_B = 375mA, (Note 1)$	-	1.2	V
Gain Bandwidth Product	f_T	$V_{CE} = 10V, I_C = 500mA, f = 1MHz$	3	-	MHz
Small Signal Forward Current Transfer Ratio	h_{fe}	$V_{CE} = 10V, I_C = 500mA, f = 1kHz$	3	-	-

Note 1. Pulsed: Pulse Duration = 300µs, Duty Factor = 0.018.



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TOLERANCES: UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

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APPROVED BY:	DATE:
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DRAWING TITLE:			
Medium Power Transistor, Silicon, TO-220, PNP			
SIZE	DWG. NO.	ELECTRONIC FILE	REV
A	TIP32C	35C0642.DWG	A
SCALE:	U.O.M.:	SHEET:	
NTS	MILLIMETERS	1 OF 1	