

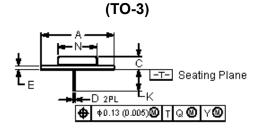


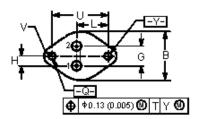
Silicon power transistors.

The MJ15025 powerBase<sup>TM</sup> power transistors designed for high power audio, disk head positioners and other linear applications.

#### Features:

- High safe operating area (100% tested) 2A at 80V.
- High DC current gain =  $h_{FE}$  = 15 (minimum) at  $I_C$  = 8A dc.
- · Pb-free packages.





Style 1: Pin 1. Base 2. Emitter Collector (Case)

Dimensions	Minimum	Maximum	
А	1.550 (39.37) Reference		
В	-	1.050 (26.67)	
С	0.250 (6.35)	0.335 (8.51)	
D	0.038 (0.97)	0.043 (1.09)	
E	0.055 (1.40)	0.070 (1.77)	
G	0.430 (10.92) BSC		
Н	0.215 (5.46) BSC		
К	0.440 (11.18)	0.480 (12.19)	
L	0.665 (16.89) BSC		
N	-	0.830 (21.08)	
Q	0.151 (3.84) 0.165 (4.3		
U	1.187 (30.15) BSC		
V	0.131 (3.33) 0.188 (4.77		

Dimensions : Inches (Millimetres)

16 Amperes Silicon Power Transistors 200 - 250 Volts, 250 Watts



(TO-3) Case 1-07 Style 1



# **Maximum Ratings**

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage MJ15025	V <sub>CEO</sub>	250	V dc	
Collector-Base Voltage MJ15025	V <sub>CBO</sub>	400		
Emitter-Base Voltage	V <sub>EBO</sub>	5	v uc	
Collector-Emitter Voltage	V <sub>CEX</sub>	400		
Collector Current-Continuous -Peak (Note 1)	I <sub>C</sub>	16 30	A dc	
Base Current-Continuous	I <sub>B</sub>	5		
Total Power Dissipation at T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	250 1.43	W W/°C	
Operating and Storage Junction Temperature Range	T <sub>J,</sub> T <sub>stg</sub>	-65 to +200	°C	

### **Thermal Characteristics**

Characteristics	Symbol	Maximum	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.70	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

# Electrical Characteristics (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Minimum	Maximum	Unit
Off Characteristics					
Collector-Emitter Sustaining Voltage (Note 2) $(I_C = 100 \text{mA dc}, I_B = 0)$	MJ15025	V <sub>CEO (sus)</sub>	250	-	-
Collector Cut off Current (V <sub>CE</sub> = 250V dc, V <sub>BE (off)</sub> = 1.5V dc)	MJ15025	I <sub>CEX</sub>	-	250	
Collector Cut off Current (V <sub>CE</sub> = 200V dc, I <sub>B</sub> = 0)	MJ15025	I <sub>CEO</sub>	-	500	μA dc
Emitter Cut off Current (V <sub>CE</sub> = 5V dc, I <sub>B</sub> = 0)		I <sub>EBO</sub>	-	500	
Second Breakdown					
Second Breakdown Collector Current with Base F ( $V_{CE}$ = 50V dc, t = 0.5s (Non-repetitive) ( $V_{CE}$ = 80V dc, t = 0.5s (Non-repetitive)	orward Biased	I <sub>S/b</sub>	5 2	-	A dc

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<sup>1.</sup> Pulse Test: Pulse Width = 5ms, Duty Cycle ≤10%.

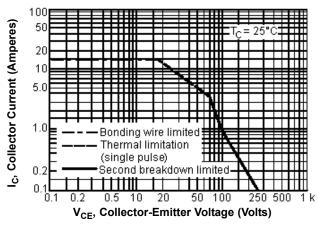


## Electrical Characteristics (T<sub>C</sub> = 25°C unless otherwise noted)

On Characteristic				
DC Current Gain ( $I_C$ = 8A dc, $V_{CE}$ = 4V dc) ( $I_C$ = 16A dc, $V_{CE}$ = 4V dc)	h <sub>FE</sub>	15 5	60 -	-
Collector-Emitter Saturation Voltage ( $I_C$ = 8A dc, $I_B$ = 0.8A dc) ( $I_C$ = 16A dc, $I_B$ = 3.2A dc)	V <sub>CE (sat)</sub>	-	1.4 4.0	V dc
Base-Emitter On Voltage ( $I_C = 8A \text{ dc}, V_{CE} = 4V \text{ dc}$ )	V <sub>BE (on)</sub>	-	2.2	
Dynamic Characteristics	·			
Current-Gain Bandwidth Product ( $I_C = 1A dc$ , $V_{CE} = 10V dc$ , $f_{test} = 1MHz$ )	f <sub>T</sub>	4	-	MHz
Output Capacitance $(V_{CB} = 10V \text{ dc}, I_E = 0, f_{test} = 1MHz)$	C <sub>ob</sub>	-	600	pF

1. Pulse Test: Pulse Width =  $300\mu s$ , Duty Cycle  $\leq 2\%$ .

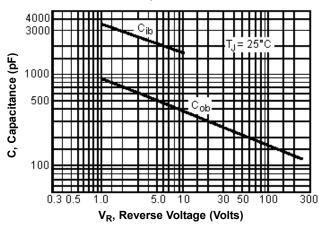
#### **Active Region DC Safe Operating Area**



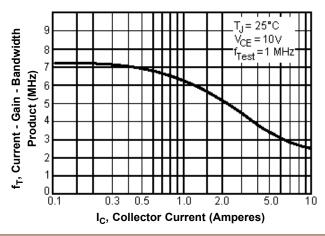
There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_{C}$  -  $V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data is based on  $T_{J (pk)} = 200^{\circ}C$ ;  $T_{C}$  is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

Capacitances



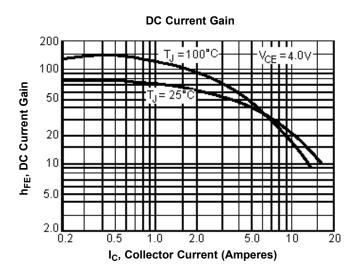
**Current - Gain - Bandwidth Product** 

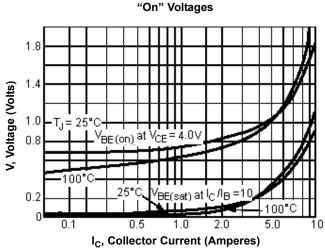


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#### **Part Number Table**

Description	Part Number	
Transistor, PNP, TO-3	MJ15025	

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