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SPC-F005.DWG

۷.	REVISIONS			DOC. NO. SPC-F005 * Effective: 7/8/02 * DCP No: 1398					
۷,	DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	1447	Α	RELEASED	но	6/27/03	JWM	6/27/03	DJC	6/27/03
	1885	В	UPDATED TO ROHS COMPLIANCE	EO	02/03/06	Ю	2/6/06	НО	2/6/06

Description: SWITCHMODE SERIES TO-3 NPN SILICON POWER DARLINGTON TRANSISTOR

WITH BASE-EMITTER SPEEDUP DIODE. The MJ10005 darlington transistor is designed for

high voltage, high speed, power switching in inductive circuits where fall time is critical.

Features:

SWITCHING REGULATORS

CONTINUOUS COLLECTOR CURRENT— $I_{\rm C}=20{\rm Ma}$

SOLENOID AND RELAY DRIVERS

MOTOR CONTROLS

Absolute Maximum Ratings:

- Collector-Emitter Voltage, $V_{CEV} = 500V$ - Collector-Emitter Voltage, $V_{CEO} = 400V$ - Emitter-Base Voltage, $V_{CEO} = 8V$ - Continuous Collector Current, $I_{C} = 20A$ - Base Current, $I_{B} = 2.5 A$ - Total Device Dissipation ($T_{C} = +25^{\circ}C$), $P_{D} = 175W$ Derate above $25^{\circ}C = 1W/^{\circ}C$ - Operating Junction Temperature Range, $T_{J} = -65^{\circ}$ to $+200^{\circ}C$ - Storage Temperature Range, $T_{stg} = -65^{\circ}$ to $+200^{\circ}C$

Electrical Characteristics:

R J TYP.

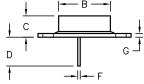
Collector (Case)

Pin 1 = Base

Pin 2 = Emitter

 $(T_A = +25^{\circ}C \text{ unless otherwise specified})$

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	Α	38.75	39.96
	В	19.28	22.23
	С	7.96	9.23
	D	11.18	12.19
İ	Ε	25.20	26.67
E	F	0.92	1.09
	G	1.38	1.62
	Н	29.90	30.40
	_	16.64	17.30
	J	3.88	4.36
	K	10.67	11.18

MIN

MAX





-		I	T	I	T				
Parameter	Symbol	Test Conditions	Min	Max	Unit				
OFF Characteristics									
Collector—Emitter Breakdown Voltage	V _{(BR)CEO}	$I_C = 250$ mA, $I_B = 0$		-	V				
Collector Cut-Off Current	I _{CEV}	$V_{CE} = 500V, V_{EB(off)} = 1.5V$	_	0.25	mA				
	I _{CER}	$V_{CE} = 500V$, $R_{BE}=50$ ohm, $T_{C} = +100$ °C	-	5	mA				
Emitter Cut-Off Current	I _{EBO}	$V_{EB} = 2V$, $I_{C} = 0$	-	175	mA				
ON Characteristics (Note 1)									
DC Current Gain	h _{FE}	$V_{CE} = 5V$, $I_{C} = 5A$	50	600	-				
		$V_{CE} = 5V$, $I_{C} = 10A$	40	400	-				
Collector—Emitter Saturation Voltage	V _{CE(sat)}	I_C = 10A, I_B = 400mA	-	1.9	٧				
		$I_C = 20A$, $I_B = 2A$	-	3	V				
Base-Emitter Saturation Voltage	V _{BE(sat)}	I_C = 10A, I_B = 400mA	-	2.5	٧				
Diode Foward Voltage	V _F	$I_{\mathbf{F}} = 10A$	-	5	٧				
Small-Signal Characteristics									
Output Capacitance	Cobo	$V_{CB} = 10V$, $I_{E} = 0$, $f = 100MHz$	100	-	рF				
Small—Signal Current Gain (Note 2)	h _{fe}	$V_{CE} = 10V$, $I_{C} = 1A$, $f = 1MHz$	10	-	-				
Switching Characteristics									
Delay Time	t _d	$V_{CC} = 250V$, $I_{C} = 10A$, $V_{BE(off)} = 5V$, $I_{B1} = 400$ mA		0.2	μs				
Rise Time	tr	VCC = 2557, 10 = 1575, *BE(off) = 57, 181 = 4501117	-	0.6	μs				
Storage Time	ts	V_{CC} = 250V, I_{C} = 10A, $V_{BE(off)}$ = 5V, I_{BI} = 400mA		1.5	μs				
Fall Time	t _f			0.5	μs				
Note 1 Pulse Test Pulse	Width	$ Duty Cycle $							

Note 1. Pulse Test: Pulse Width </= 300\mus, Duty Cycle </= 2\%. Note 2. f_T is defined as the frequency at which |hfe| extrapolates to unity

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TOLERANCES:

DRAWN BY: DATE: HISHAM ODISH 6/27/03 CHECKED BY: DATE: JEFF MCVICKER 6/27/03 DATE: APPROVED BY: DANIEL CAREY 6/27/03

DRAWING TITLE: Transistor, Power Swtching, High Voltage, Bipolar, TO-3, NPN DWG. NO. ELECTRONIC FILE REV 01H0845.DWG В MJ10005 SCALE: NTS U.O.M.: Millimeters SHEET: 1 OF 1