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SPC-F005.DWG

REVISIONS

DOC. NO. SPC-F005 \* Effective: 7/8/02 \* DCP No: 1398

DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1447	A	RELEASED	HYO	6/11/02	JWM	2/20/04	JC	2/20/04
1885	B	UPDATED TO ROHS COMPLIANCE	EO	02/03/06	HO	2/6/06	HO	2/6/06

Dimensions	A	B	C	D	E	F	G	H	J	K	L
Min.	8.50	7.74	6.09	0.40	-	2.41	4.82	0.71	0.73	12.70	42°
Max.	9.39	8.50	6.60	0.53	0.88	2.66	5.33	0.86	1.02	-	48°



This is a silicon PNP transistor in a TO-39 type case designed primarily for amplifier and switching applications. This device features high breakdown voltage, low leakage current, low capacity, and beta useful over an extremely wide current range.

Electrical Characteristics: (T<sub>A</sub> = +25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>OFF Characteristics</b>						
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 100mA, I <sub>B</sub> = 0	65	-	-	V
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 10μA, I <sub>C</sub> = 0	7	-	-	V
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> = 90V, I <sub>E</sub> = 0	-	-	100	μA
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>BE</sub> = 7V, I <sub>C</sub> = 0	-	-	10	μA

ON Characteristics, Note 1

DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> = 10V, I <sub>C</sub> = 100μA	20	-	-	-
		V <sub>CE</sub> = 2V, I <sub>C</sub> = 150mA	20	-	200	-
		V <sub>CE</sub> = 10V, I <sub>C</sub> = 500mA	20	-	-	-
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA	-	-	0.65	V
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA	-	-	1.4	V

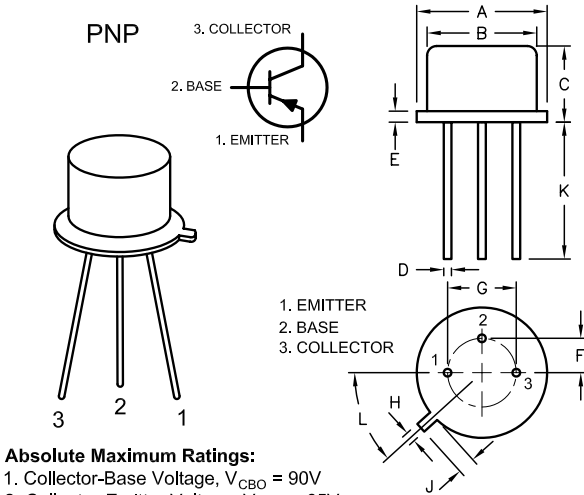
Small-Signal Characteristics

Small-Signal Current Gain	h <sub>fe</sub>	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA, f = 20MHz	1	-	-	-
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Switching Characteristics

Storage Time	t <sub>s</sub>	I <sub>B2</sub> = 15mA	-	-	600	nS
Turn-On Time	t <sub>on</sub>	I <sub>B1</sub> = I <sub>B2</sub>	-	-	110	nS
Fall Time	t <sub>f</sub>	I <sub>B2</sub> = 15mA	-	-	100	nS

Note 1. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 1%.



Absolute Maximum Ratings:

- Collector-Base Voltage, V<sub>CBO</sub> = 90V
- Collector-Emitter Voltage, V<sub>CEO</sub> = 65V
- Emitter-Base Voltage, V<sub>EBO</sub> = 7V
- Continuous Collector Current, I<sub>C</sub> = 1A
- Total Device Dissipation (T<sub>A</sub> = +25°C), P<sub>D</sub> = 1W  
Derate above 25°C = 5.72mW/°C
- Total Device Dissipation (T<sub>C</sub> = +25°C), P<sub>D</sub> = 5W  
Derate above 25°C = 28.6mW/°C
- Operating Junction Temperature Range, T<sub>J</sub> = -65° to +200°C
- Storage Temperature Range, T<sub>stg</sub> = -65° to +200°C
- Thermal Resistance, Junction-to-Case, R<sub>thJC</sub>: 35°C
- Lead temperature (During Soldering, 1/16" from case, 60sec max), T<sub>L</sub>: 300°C

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TOLERANCES:

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

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DRAWING TITLE:			
Transistor, Bipolar, Metal, TO-39, PNP			
SIZE	DWG. NO.	ELECTRONIC FILE	REV
A	2N4036	35C0711.DWG	B
SCALE:	NTS	U.O.M.: Millimeters	SHEET: 1 OF 1