# General Purpose Transistor (Isolated Dual Transistors) EMT1/UMT1N/IMT1A

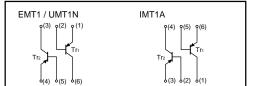
# Features

- 1) Two 2SA1037AK chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.

### Structure

Epitaxial planar type PNP silicon transistor

# Equivalent circuit

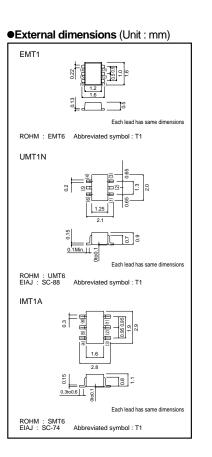


The following characteristics apply to both  $Tr_1 \,and \, Tr_2.$ 

### •Absolute maximum ratings (Ta = 25°C)

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Parameter		Symbol	Limits	Unit			
Collector-base voltage		Vсво	-60	V			
Collector-emitter voltage		Vceo	-50	V			
Emitter-base voltage		Vebo	-6	V			
Collector current		lc	-150	mA			
Collector power dissipation	EMT1, UMT1N	Pc	150 (TOTAL)	mW *1 *2			
	IMT1A	FC	300 (TOTAL)				
Junction temperature		Tj	150	°C			
Storage temperature		Tstg	-55 to +150	°C			

\*1 120mW per element must not be exceeded.
 \*2 200mW per element must not be exceeded.



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# EMT1 / UMT1N / IMT1A

# Transistors

## •Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	-60	-	-	V	$Ic = -50\mu A$	
Collector-emitter breakdown voltage	BVCEO	-50	-	-	V	Ic = -1mA	
Emitter-base breakdown voltage	ВVево	-6	-	-	V	Ιε = -50μΑ	
Collector cutoff current	Ісво	-	-	-0.1	μΑ	Vcb = -60V	
Emitter cutoff current	Іево	-	-	-0.1	μΑ	Veb = -6V	
Collector-emitter saturation voltage	VCE(sat)	-	-	-0.5	V	$Ic/I_B = -50mA/-5mA$	
DC current transfer ratio	hfe	120	-	560	-	$V_{CE} = -6V$ , $I_C = -1mA$	
Transition frequency	f⊤	-	140	-	MHz	Vce = -12V, Ie = 2mA, f = 100MHz	
Output capacitance	Cob	-	4	5	pF	$V_{CB} = -12V$ , $I_E = 0A$ , $f = 1MHz$	

#### Packaging specifications

	Package	Taping		
	Code	T2R	TR	T108
Туре	Basic ordering unit (pieces)	8000	3000	3000
EMT1		0	-	-
UMT1N		-	0	-
IMT1A		-	-	0

### Electrical characteristic curves

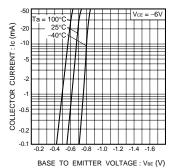
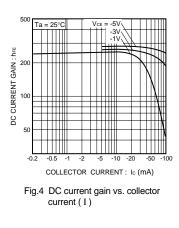
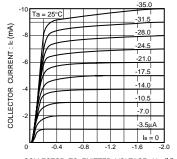


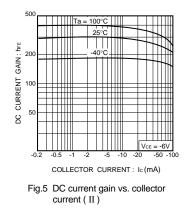
Fig.1 Grounded emitter propagation characteristics

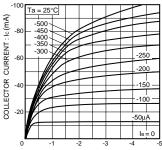




COLLECTOR TO EMITTER VOLTAGE :  $V_{CE}(V)$ 

Fig.2 Grounded emitter output characteristics (I)





COLLECTOR TO EMITTER VOLTAGE : VCE (V)

Fig.3 Grounded emitter output characteristics (II)

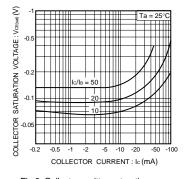


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

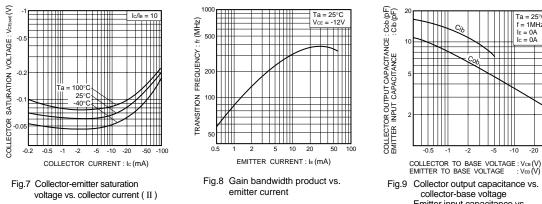
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Rev.A 2/3

# Transistors

Ta = 25°C f = 1MHz I<sub>E</sub> = 0A I<sub>C</sub> = 0A

-10 -20



collector-base voltage Emitter input capacitance vs. emitter-base voltage



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