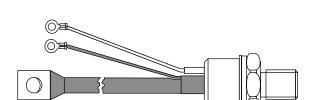


## Vishay High Power Products

## **Phase Control Thyristors** (Stud Version), 280 A

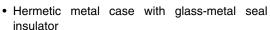


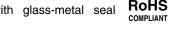
TO-209AB (TO-93)

PRODUCT SUMMARY		
I <sub>T(AV)</sub>	280 A	

#### **FEATURES**

- · Center amplifying gate
- International standard case TO-209AB (TO-93)





- Compression bonded encapsulation for heavy duty operations such as severe thermal cycling
- Lead (Pb)-free
- · Designed and qualified for industrial level

#### **TYPICAL APPLICATIONS**

- DC motor controls
- Controlled DC power supplies
- · AC controllers

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
1		280	A	
I <sub>T(AV)</sub>	T <sub>C</sub>	85	°C	
I <sub>T(RMS)</sub>		440		
I <sub>TSM</sub>	50 Hz	7850	Α	
	60 Hz	8220		
10.	50 Hz	308	kA <sup>2</sup> s	
I <sup>2</sup> t	60 Hz	281	KA-S	
V <sub>DRM</sub> /V <sub>RRM</sub>		400/600	V	
t <sub>q</sub>	Typical	100	μs	
T <sub>J</sub>		- 40 to 125	°C	

### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS								
TYPE NUMBER	VOLTAGE CODE	V <sub>DRM</sub> /V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	$\begin{aligned} I_{DRM}/I_{RRM} & \text{MAXIMUM} \\ \text{AT T}_{J} &= T_{J} & \text{MAXIMUM} \\ & \text{mA} \end{aligned}$				
ST280S	04	400	500	30				
012000	06	600	700	30				

## **ST280SPbF Series**

# Vishay High Power Products Phase Control Thyristors (Stud Version), 280 A



ABSOLUTE MAXIMUM RATIN	GS					
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average on-state current		180° condu	180° conduction, half sine wave		280	Α
at case temperature	$I_{T(AV)}$				85	°C
Maximum RMS on-state current	I <sub>T(RMS)</sub>	DC at 75 °C	case temperati	ure	440	
		t = 10 ms	No voltage		7850	
Maximum peak, one-cycle		t = 8.3 ms	reapplied		8220	А
non-repetitive surge current	I <sub>TSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		6600	
		t = 8.3 ms	reapplied	Sinusoidal half wave,	6900	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 10 ms	No voltage	initial $T_J = T_J$ maximum	310	- kA <sup>2</sup> s
		t = 8.3 ms	reapplied		220	
		t = 10 ms	100 % V <sub>RRM</sub>		218	
		t = 8.3 ms	reapplied		200	
Maximum I $^2\sqrt{t}$ for fusing	I <sup>2</sup> √t	t = 0.1 to 10	ms, no voltage	reapplied	3100	kA²√s
Low level value of threshold voltage	V <sub>T(TO)1</sub>	(16.7 % x π	$x I_{T(AV)} < I < \pi x$	$I_{T(AV)}$ , $T_J = T_J$ maximum	0.84	V
High level value of threshold voltage	V <sub>T(TO)2</sub>	$(I > \pi \times I_{T(A)})$	$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ maximum}$		0.88	ľ
Low level value of on-state slope resistance	r <sub>t1</sub>	(16.7 % x $\pi$ x $I_{T(AV)} < I < \pi$ x $I_{T(AV)}$ ), $T_J = T_J$ maximum		0.50	mΩ	
High level value of on-state slope resistance	r <sub>t2</sub>	$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ maximum}$		0.47	1115.2	
Maximum on-state voltage	$V_{TM}$	$I_{pk} = 880 \text{ A}, T_J = T_J \text{ maximum}, t_p = 10 \text{ ms sine pulse}$		1.28	V	
Maximum holding current	I <sub>H</sub>	T 05 00 and to send to 10 V mai ii to 1	T 05 00 anada ayyahı 10 V yazidiye kad	T <sub>J</sub> = 25 °C, anode supply 12 V resistive load	600	mA
Maximum (typical) latching current	IL	1]=25 °C,	anoue supply In	v resistive ludu	1000 (300)	IIIA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum non-repetitive rate of rise of turned-on current	dl/dt	Gate drive 20 V, 20 $\Omega$ , $t_r \le 1~\mu s$ $T_J = T_J$ maximum, anode voltage $\le 80~\%~V_{DRM}$	1000	A/µs
Typical delay time	t <sub>d</sub>	Gate current 1 A, $dl_g/dt = 1$ A/ $\mu$ s $V_d = 0.67 \% V_{DRM}$ , $T_J = 25 \ ^{\circ}C$	1.0	110
Typical turn-off time	tq	$I_{TM} = 300 \text{ A, } T_J = T_J \text{ maximum, dl/dt} = 20 \text{ A/}\mu\text{s,}$ $V_R = 50 \text{ V, dV/dt} = 20 \text{ V/}\mu\text{s, gate 0 V } 100 \Omega, t_p = 500 \mu\text{s}$	100	μs

BLOCKING						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum critical rate of rise of off-state voltage	dV/dt	$T_J = T_J$ maximum linear to 80 % rated $V_{DRM}$	500	V/µs		
Maximum peak reverse and off-state leakage current	I <sub>RRM,</sub> I <sub>DRM</sub>	$T_J = T_J$ maximum, rated $V_{DRM}/V_{RRM}$ applied	30	mA		



### Phase Control Thyristors (Stud Version), 280 A

## Vishay High Power Products

TRIGGERING						
PARAMETER	SYMBOL	TEGT COMPLETIONS		VALUES		UNITS
PARAMETER	SYMBOL	·	EST CONDITIONS	TYP.	MAX.	
Maximum peak gate power	P <sub>GM</sub>	$T_J = T_J$ maximum,	, t <sub>p</sub> ≤ 5 ms	10	0.0	w
Maximum average gate power	P <sub>G(AV)</sub>	$T_J = T_J$ maximum,	, f = 50 Hz, d% = 50	2	.0	l VV
Maximum peak positive gate current	I <sub>GM</sub>	$T_J = T_J$ maximum,	, t <sub>p</sub> ≤ 5 ms	3	.0	Α
Maximum peak positive gate voltage	+ V <sub>GM</sub>	T T mayimum	+ < F ma	20		.,
Maximum peak negative gate voltage	- V <sub>GM</sub>	$T_J = T_J$ maximum, $t_p \le 5$ ms		5.0		V
				180	-	
DC gate current required to trigger	I <sub>GT</sub>	T <sub>J</sub> = 25 °C	Maximum required gate trigger/ current/voltage are the lowest value which will trigger all units 12 V anode to cathode applied	90	150	mA
		T <sub>J</sub> = 125 °C		40	-	
		T <sub>J</sub> = - 40 °C		2.9	-	
DC gate voltage required to trigger	$V_{GT}$	T <sub>J</sub> = 25 °C		1.8	3.0	V
		T <sub>J</sub> = 125 °C		1.2	-	]
DC gate current not to trigger	I <sub>GD</sub>	T. – T. mavimum	Maximum gate current/voltage not to trigger is the maximum value	10		mA
DC gate voltage not to trigger	V <sub>GD</sub>	$T_J = T_J \text{ maximum}$	which will not trigger any unit with rated V <sub>DRM</sub> anode to cathode applied	0.25		V

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum operating junction temperature range	T <sub>J</sub>		- 40 to 125	°C	
Maximum storage temperature range	T <sub>Stg</sub>		- 40 to 150		
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	0.105	K/W	
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>	hCS Mounting surface, smooth, flat and greased		17/44	
Mounting torque, ± 10 %		Non-lubricated threads	31 (275)	N · m	
Woulding torque, ± 10 %		Lubricated threads	24.5 (210)	(lbf · in)	
Approximate weight			280	g	
Case style		See dimensions - link at the end of datasheet	eet TO-209AB (TO-93)		

△R <sub>th</sub> JC CONDUCTION					
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.016	0.012			
120°	0.019	0.020			
90°	0.025	0.027	$T_J = T_J \text{ maximum}$	K/W	
60°	0.036	0.037			
30°	0.060	0.060			

#### Note

 $\bullet \ \ \, \text{The table above shows the increment of thermal resistance } \, R_{thJC} \, \text{when devices operate at different conduction angles than DC} \,$ 

# Vishay High Power Products Phase Control Thyristors (Stud Version), 280 A



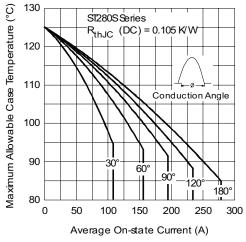


Fig. 1 - Current Ratings Characteristics

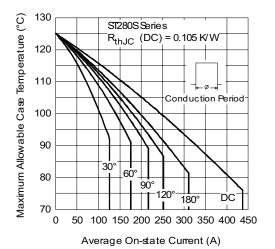


Fig. 2 - Current Ratings Characteristics

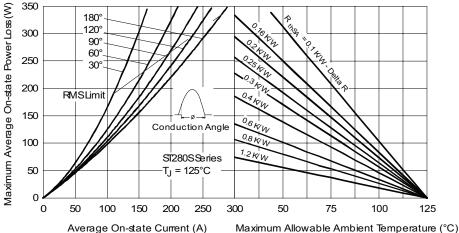


Fig. 3 - On-State Power Loss Characteristics

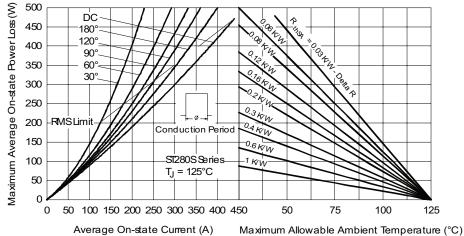
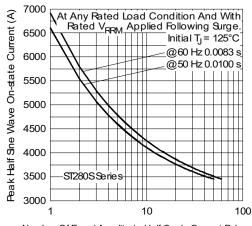


Fig. 4 - On-State Power Loss Characteristics



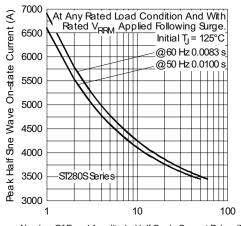
## Phase Control Thyristors (Stud Version), 280 A

## Vishay High Power Products



 $Number\,Of\,Equa\,I\,Amplitud\,e\,\,Ha\,lf\,\,Cyc\,le\,\,Current\,\,Pulses\,(N)$ 

Fig. 5 - Maximum Non-Repetitive Surge Current



 $Number\,Of\,Equa\,I\,Amp\,Iitude\,Half\,Cycle\,Current\,Pulses\,(N)$ 

Fig. 6 - Maximum Non-Repetitive Surge Current

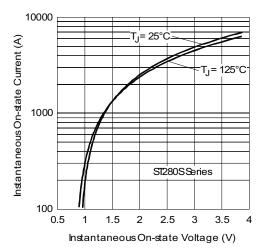


Fig. 7 - On-State Voltage Drop Characteristics

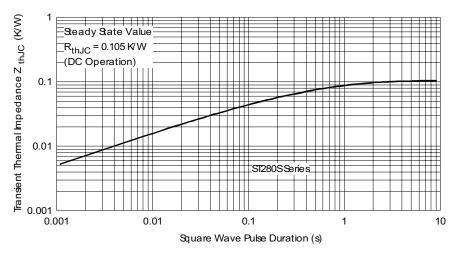


Fig. 8 - Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

# Vishay High Power Products Phase Control Thyristors (Stud Version), 280 A



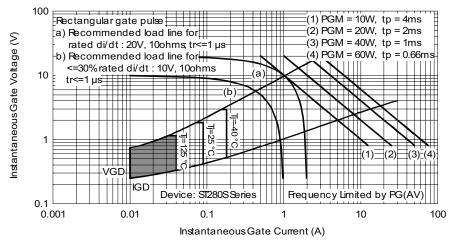
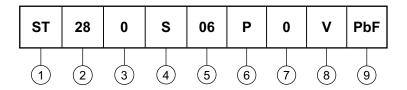


Fig. 9 - Gate Characteristics

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Thyristor
- 2 Essential part number
- 3 0 = Converter grade
- 4 S = Compression bonding stud
- 5 Voltage code x 100 = V<sub>RRM</sub> (see Voltage Ratings table)
- 6 P = Stud base 3/4"-16UNF-2A threads
- 7 0 = Eyelet terminals (gate and auxiliary cathode leads)
  - 1 = Fast-on terminals (gate and auxiliary cathode leads)
- 8 V = Glass-metal seal
- 9 Lead (Pb)-free

Note: For metric device M16 x 1.5 contact factory

LINKS TO RELATED DOCUMENTS		
Dimensions	http://www.vishay.com/doc?95077	

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Document Number: 91000 www.vishay.com
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