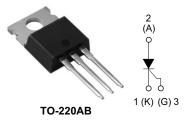


Vishay High Power Products

#### Phase Control SCR, 10 A



PRODUCT SUMMARY			
V <sub>T</sub> at 10 A < 1.4 V			
I <sub>TSM</sub>	200 A		
V <sub>RRM</sub>	800/1200 V		

#### **DESCRIPTION/FEATURES**

The 16TTS..PbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology



RoHS COMPLIANT

used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	TIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS			
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	13.5	17	A	

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I <sub>T(AV)</sub>	Sinusoidal waveform	10	٨		
I <sub>RMS</sub>		16	A		
V <sub>DRM</sub> /V <sub>RRM</sub>	Range <sup>(1)</sup>	800/1200	V		
I <sub>TSM</sub>		200	А		
V <sub>T</sub>	10 A, T <sub>J</sub> = 25 °C	1.4	V		
dV/dt		500	V/µs		
dl/dt		150	A/µs		
TJ	Range	- 40 to 125	۵°		

#### Note

<sup>(1)</sup> For higher voltage up to 1600 V contact factory

VOLTAGE RATINGS						
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>DRM</sub> , MAXIMUM PEAK DIRECT VOLTAGE V	I <sub>RRM</sub> /I <sub>DRM</sub> AT 125 °C mA			
16TTS08PbF	800	800	10			
16TTS12PbF	1200	1200	10			

\* Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL			VALUES		
PARAMETER	STMBUL		TEST CONDITIONS		MAX.	UNITS
Maximum average on-state current	I <sub>T(AV)</sub>	T <sub>C</sub> = 98 °C, 1	= 98 °C, 180° conduction, half sine wave 10		0	
Maximum RMS on-state current	I <sub>RMS</sub>			1	6	
Maximum peak, one-cycle,	1	10 ms sine p	ulse, rated V <sub>RRM</sub> applied	170		A
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine p	ulse, no voltage reapplied	200		
Maximum 12t for fusing	l <sup>2</sup> t	10 ms sine p	ulse, rated V <sub>RRM</sub> applied	14	14	A20
Maximum I <sup>2</sup> t for fusing	I-t	10 ms sine pulse, no voltage reapplied		200		A <sup>2</sup> s
Maximum I <sup>2</sup> √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		20	00	A²√s
Maximum on-state voltage drop	V <sub>TM</sub>	10 A, T <sub>J</sub> = 25 °C		1.4		V
On-state slope resistance	r <sub>t</sub>	T <sub>1</sub> = 125 °C		24	l.0	mΩ
Threshold voltage	V <sub>T(TO)</sub>			1	.1	V
	1 /1	T <sub>J</sub> = 25 °C	T <sub>J</sub> = 25 °C		.5	
Maximum reverse and direct leakage current	I <sub>RM</sub> /I <sub>DM</sub>	$T_J = 125 \text{ °C}$ $V_R = \text{Rated } V_{RRM}/V_{DRM}$		1	0	
Holding current	I <sub>H</sub>	Anode supply = 6 V, resistive load, initial $I_T = 1 A$ 16TTS08PbF, 16TTS12PbF		-	100	mA
Maximum latching current	١L	Anode supply = 6 V, resistive load		20	00	
Maximum rate of rise of off-state voltage	dV/dt			50	00	V/µs
Maximum rate of rise of turned-on current	dl/dt			1	50	A/μs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P <sub>GM</sub>		8.0	14/	
Maximum average gate power	P <sub>G(AV)</sub>		2.0	W	
Maximum peak positive gate current	+ I <sub>GM</sub>		1.5	А	
Maximum peak negative gate voltage	- V <sub>GM</sub>		10	V	
	I <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J = -65 \ ^{\circ}C$	90		
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	60	mA	
		Anode supply = 6 V, resistive load, $T_J = 125 \ ^{\circ}C$	35		
	V <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J = -65 \ ^{\circ}C$	3.0		
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	2.0	V	
		Anode supply = 6 V, resistive load, $T_J = 125 \ ^{\circ}C$	1.0	v	
Maximum DC gate voltage not to trigger	$V_{GD}$	T 105 °C V Batad value	0.2		
Maximum DC gate current not to trigger	I <sub>GD</sub>	T <sub>J</sub> = 125 °C, V <sub>DRM</sub> = Rated value $2.0$		mA	

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t <sub>gt</sub>	T <sub>J</sub> = 25 °C	0.9	
Typical reverse recovery time	t <sub>rr</sub>	T <sub>1</sub> = 125 °C	4	μs
Typical turn-off time	tq	1J = 125 C	110	



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THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 125	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	1.3		
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		62	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.5		
				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
	maximum			12 (10)	(lbf · in)	
Marking davias				16T	rso8	
Marking device			Case style TO-220AB	16T	16TTS12	

90°

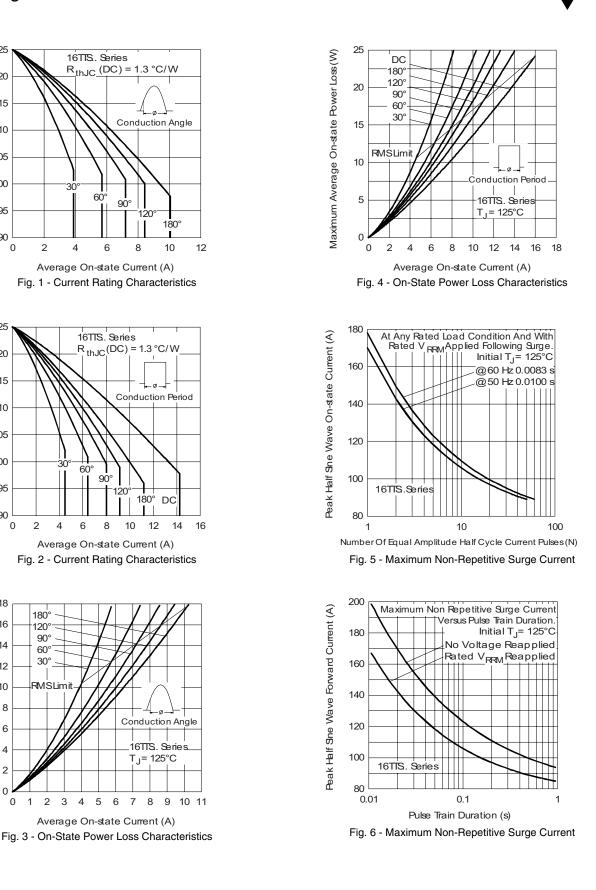
60°

Maximum Average On-state Power Loss(W)

Maximum Allowable Case Temperature (°C)

Maximum Allowable Case Temperature (°C)

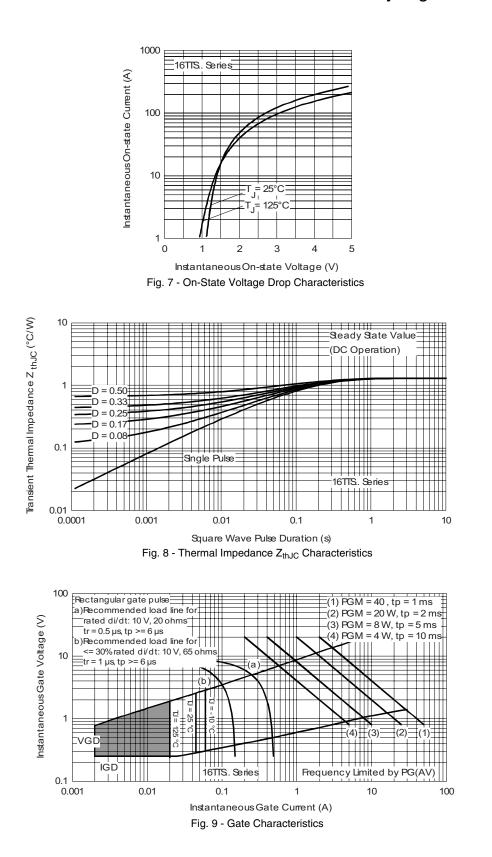
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Phase Control SCR, 10 A Vishay High Power Products

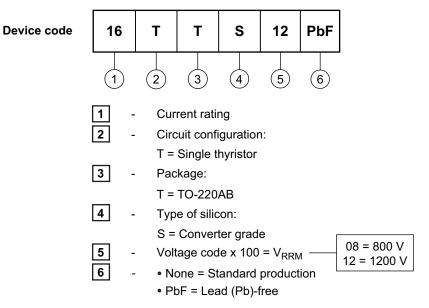


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#### ORDERING INFORMATION TABLE



Note: For higher voltage up to 1600 V contact factory

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



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