

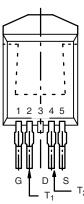
Vishay Siliconix

ROHS COMPLIANT

N-Channel 40-V (D-S) MOSFET with Sensing Diode

PRODUCT SUMMARY			
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)	
40	0.0055 at V _{GS} = 10 V	60 ^a	



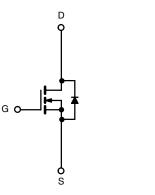


FEATURES

- TrenchFET[®] Power MOSFETS Plus Temperature Sensing Diode
- 175 °C Junction Temperature
- New Low Thermal Resistance Package

APPLICATIONS

Industrial





Ordering Information: SUM60N04-06T-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_{C} = 25 \ ^{\circ}C$, unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	40	v	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current $(T_J = 175 \ ^{\circ}C)^d$	T _C = 25 °C	I _D	60 ^a		
	T _C = 100 °C	'D	60 ^a		
Pulsed Drain Current		I _{DM}	250	А	
Continuous Diode Current (Diode Conduction) ^d		I _S	60 ^a		
Avalanche Current		I _{AR}	60 ^a		
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	180	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	P-	200 ^c	w	
	T _A = 25 °C	– P _D –	3.75 ^d	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS	5				
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient ^d	PCB Mount ^d	R _{thJA}	40	°C/W	
Junction-to-Case		R _{thJC}	0.75		

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. See SOA curve for voltage derating.

d. When mounted on 1" square PCB (FR-4 material).

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	40			- V
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{DS} = 250 \ \mu A$	2			
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 32 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ
	I _{DSS}	V_{DS} = 32 V, V_{GS} = 0 V, T_{J} = 125 °C			50	
		$V_{DS} = 32 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			500	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	120			Α
Drain-Source On-State Resistance ^a		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 25 \text{ A}$		0.0044	0.0055	Ω
	r _{DS(on)}	V_{GS} = 10 V, I_D = 25 A, T_J = 125 °C			0.0088	
		V_{GS} = 10 V, I _D = 25 A, T _J = 175 °C			0.011	
	V _{FD1}	I _F = 50 μA	655		715	
Sense Diode Forward Voltage	V _{FD2}	I _F = 25 μA	600		660	mV
Sense Diode Forward Voltage Increase	ΔV_F	From $I_F = 25 \ \mu A$ to $I_F = 50 \ \mu A$	30		80	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		35		S
Dynamic ^b				+	• •	
Input Capacitance	C _{iss}			6400		pF
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		1100		
Reverse Transfer Capacitance	C _{rss}			630		
Total Gate Charge ^c	Qg			115	150	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 25 \text{ A}$		35		
Gate-Drain Charge ^c	Q _{gd}			35		
Turn-On Delay Time ^c	t _{d(on)}			15	20	ns
Rise Time ^c	t _r	V_{DD} = 20 V, R _L = 0.8 Ω I _D \cong 25 A, V _{GEN} = 10 V, R _G = 2.5 Ω		150	210	
Turn-Off Delay Time ^c	t _{d(off)}			60	85	
Fall Time ^c	t _f			80	110	
Source-Drain Diode Ratings and Char	racteristics T	_C = 25 °C ^b		•		
Continuous Current	۱ _S				60	۸
Pulsed Current	I _{SM}			1	200	A
Forward Voltage ^a	V _{SD}	$I_{F} = 60 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		1.0	1.5	V
Reverse Recovery Time	t _{rr}			45	70	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 60 A, di/dt = 100 A/μs		2.5	5	А
Reverse Recovery Charge	Q _{rr}			0.06	0.18	μC

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

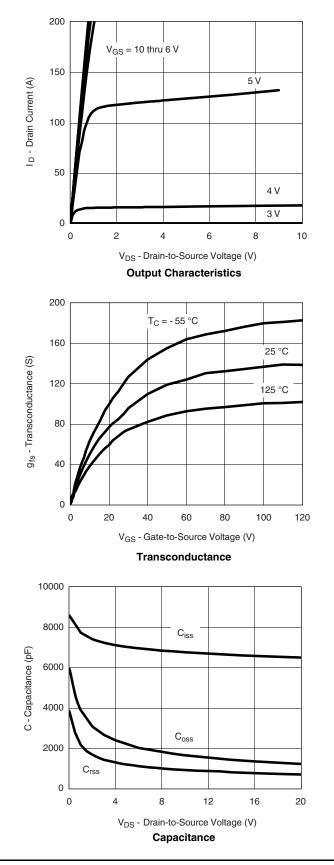
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

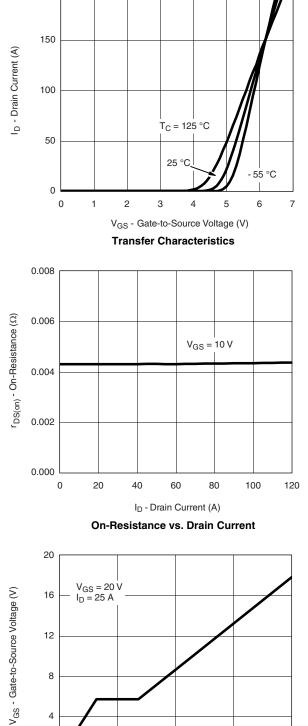


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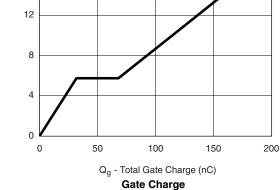
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





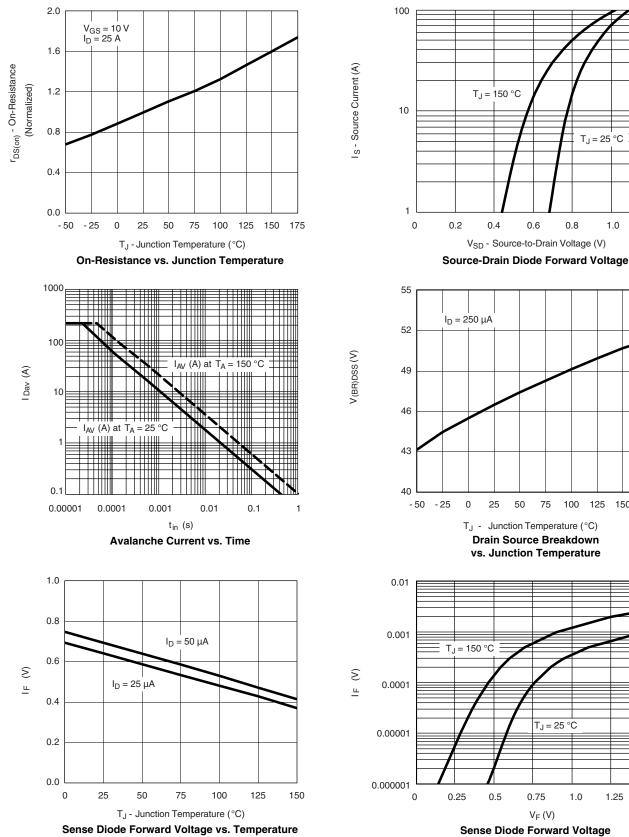
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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1.0

1.25

1.5



 $T_J = 25^{\circ}C$

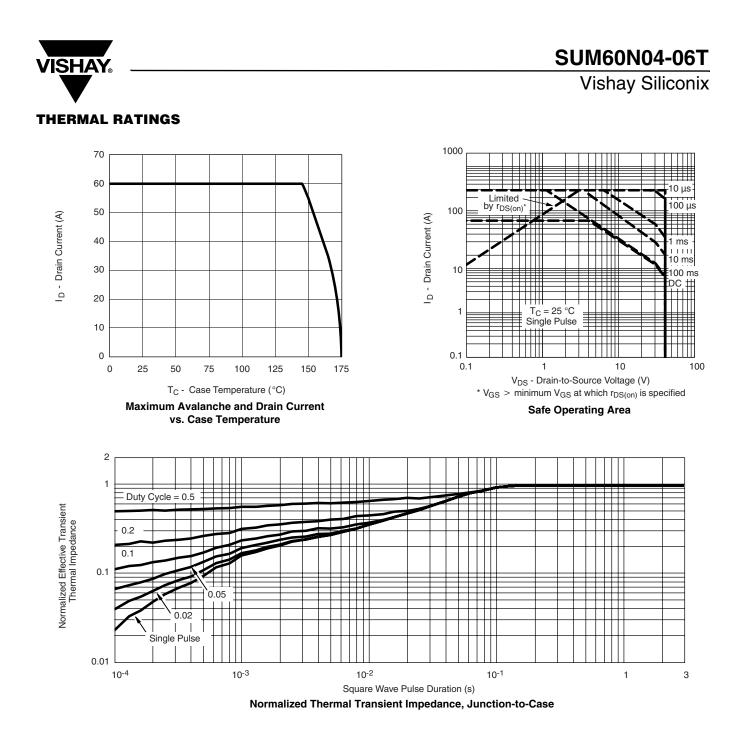
1.0

1.2

0.8

100 125

150 175



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71746.

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