



# N-Channel 200-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
200	0.130 at V <sub>GS</sub> = 10 V	3		
	0.142 at V <sub>GS</sub> = 6.0 V	2.8		

#### **FEATURES**

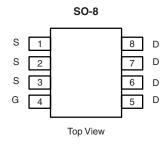
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested

# Pb-free RoHS COMPLIANT HALOGEN

FREE

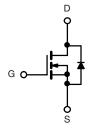
#### **APPLICATIONS**

Primary Side Switch



Ordering Information: Si4418DY-T1-E3 (Lead (Pb)-free)

Si4418DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	200		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Dunin Courset /T 150 °C\2	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	3	2.3		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		2.1	1.6	^	
Pulsed Drain Current		I <sub>DM</sub>	12		Α	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	6			
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = U. I MH	E <sub>AS</sub>	1.8		mJ	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.1	1.25	Α	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.5	1.5	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		1.3	0.8	Į vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifestory Languign to Application	t ≤ 10 s	- R <sub>thJA</sub>	36	50	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		71	85	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	15	20	

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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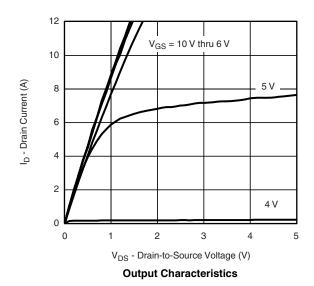
<b>SPECIFICATIONS</b> $T_J = 25$ °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zava Cata Valtaga Dvain Curvent		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V			1			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			20	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	12			Α		
	D	$V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		0.110				
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 6.0 \text{ V}, I_D = 2.8 \text{ A}$		0.120	0.142	Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 3 A		13		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.1 A, V <sub>GS</sub> = 0 V		0.8	1.2	V		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			20	30			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		4.5		nC		
Gate-Drain Charge	$Q_{gd}$			6.5				
Gate Resistance	$R_{g}$	f = 1 MHz	1	2	3.4	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			15	25			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 100 V, $R_L$ = 100 $\Omega$		15	25	ns		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 1 A, $V_{GEN}$ = 10 V, $R_g$ = 6 $\Omega$		40	60			
Fall Time	t <sub>f</sub>			20	30			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 2.1 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		70	110			

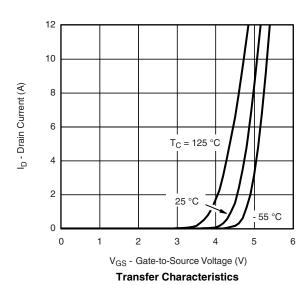
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

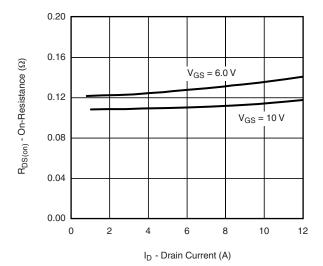




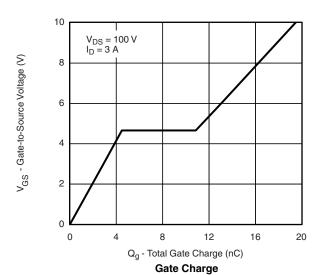


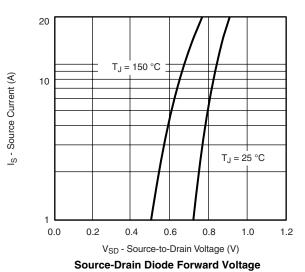


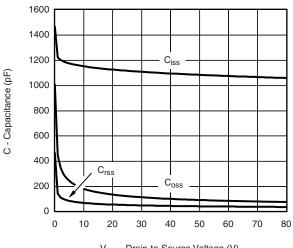
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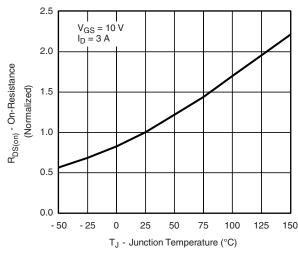


#### On-Resistance vs. Drain Current

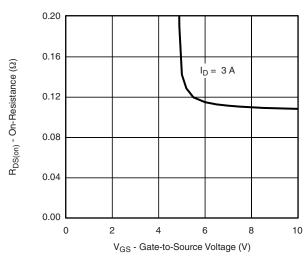








On-Resistance vs. Junction Temperature

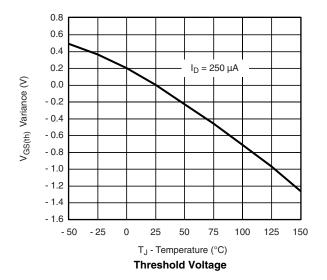


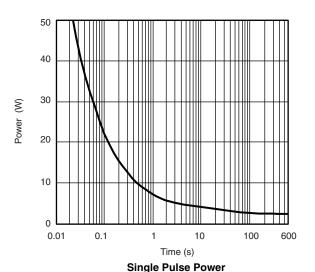
On-Resistance vs. Gate-to-Source Voltage

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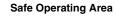
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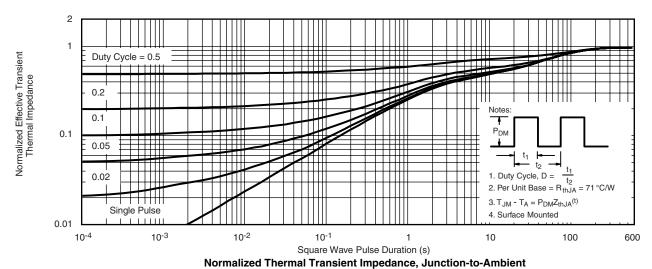
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





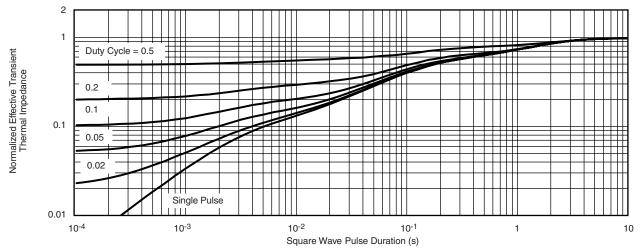
100 I<sub>DM</sub> Limited 10 100 μs I<sub>D</sub> - Drain Current (A) 1ms 10 ms 0.1 1 s T<sub>A</sub> = 25 °C Single Pulse 10 s 0.01 DC BV<sub>DSS</sub> Limited 0.001 10 100 1000 0.1 V<sub>DS</sub> - Drain-to-Source Voltage (V)  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified







#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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