

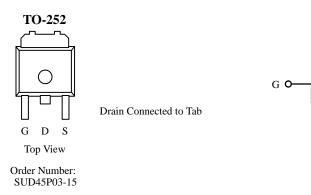
Siliconix

P-Channel 30-V (D-S), 150°C MOSFET

Product Summary

V _{DS} (V)	$\mathbf{r}_{\mathbf{DS(on)}}(\Omega)$	$I_D(A)^a$
-30	$0.015 @ V_{GS} = -10 V$	±13
	$0.024 @ V_{GS} = -4.5 V$	±8





P-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^{\circ}C$ Unless Otherwise Noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V_{GS} ± 20		- v	
Continuous Drain Current ^b	$T_A = 25^{\circ}C$	ID	±13	А	
Continuous Dram Current ^o	$T_A = 100^{\circ}C$		± 8		
Pulsed Drain Current		I _{DM}	± 100	А	
Continuous Source Current (Diode Conduction)		I _S	-13		
Maximum Power Dissipation ^b	$T_C = 25^{\circ}C$	PD	70	w	
	$T_A=25^\circ C$	тD	4 ^a		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

Thermal Resistance Ratings

Parameter		Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	R _{thJA} 30		°C/NI		
Maximum Junction-to-Case	R _{thJC}		1.8	°C/W	

Notes

a. Calculated Rating for $T_A = 25^{\circ}C$, for comparison purposes only. This cannot be used as continuous rating (see Absolute Maximum Ratings and Typical Characteristics).

b. Surface Mounted on FR4 Board, $t \le 10$ sec.

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70267.

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Specifications ($T_J = 25^{\circ}C$ Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typa	Max	Unit	
Static				•			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = –250 μA	-30			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \; \mu A$	-1.0				
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			-1		
	I _{DSS}	$V_{DS} = -30$ V, $V_{GS} = 0$ V, $T_J = 125^{\circ}C$			-50	μA	
On-State Drain Current ^b		$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-50				
	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-20			A	
		$V_{GS} = -10$ V, $I_D = -13$ A		0.012	0.015	Ω	
Drain-Source On-State Resistance ^b	r _{DS(on)}	V_{GS} = -10 V, I_D = -13 A, T_J = 125 °C		0.018	0.026		
		$V_{GS} = -4.5 \text{ V}, I_D = -13 \text{ A}$		0.020	0.024		
Forward Transconductance ^b	g _{fs}	$V_{DS} = -15 \text{ V}, I_D = -13 \text{ A}$	20			S	
Dynamic ^a							
Input Capacitance	C _{iss}			3200			
Output Capacitance	Coss	V_{GS} = 0 V, V_{DS} = –25 V, F = 1 MHz		800		pF	
Reverse Transfer Capacitance	C _{rss}			280			
Total Gate Charge ^c	Qg			50	125	nC	
Gate-Source Charge ^c	Qgs	$V_{DS} = -15 \text{ V}, \ V_{GS} = -10 \text{ V}, \ I_D = -45 \text{ A}$		14			
Gate-Drain Charge ^c	Q _{gd}			6.2			
Turn-On Delay Time ^c	t _{d(on)}			13	20		
Rise Time ^c	t _r	$\begin{split} V_{DD} = -15 \ V, R_L = 0.33 \ \Omega \\ I_D &\cong -45 \ A, \ V_{GEN} = -10 \ V, R_G = 2.4 \ \Omega \end{split}$		10	20	ns	
Turn-Off Delay Time ^c	t _{d(off)}			50	100		
Fall Time ^c	t _f	1 1		20	40	1	
Source-Drain Diode Ratings and	Characterist	ic ($T_C = 25^{\circ}C$)		-			
Pulsed Current	I _{SM}				100	A	
Diode Forward Voltage ^b	V _{SD}	$I_F = -45 \text{ A}, V_{GS} = 0 \text{ V}$		1.0	1.5	v	
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = -45$ A, di/dt = 100 A/µs		55	100	ns	

Notes

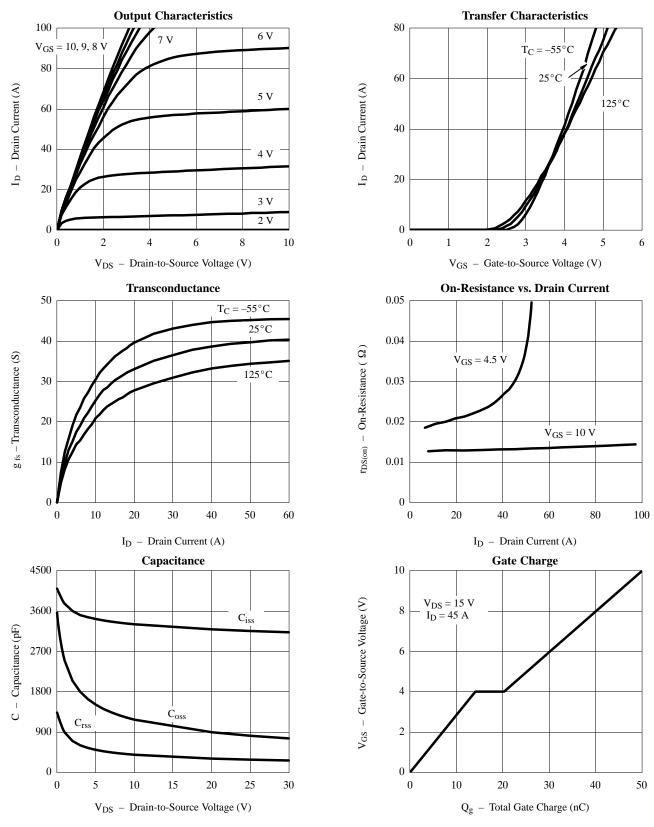
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300 \,\mu$ s, duty cycle $\leq 2\%$.

c. Independent of operating temperature.

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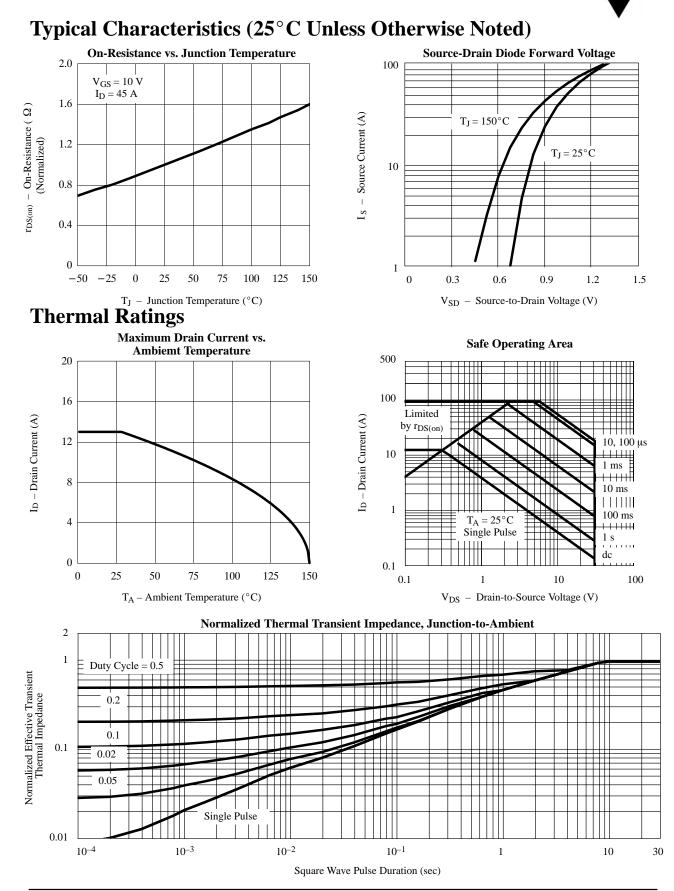
Typical Characteristics (25°C Unless Otherwise Noted)

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