



Dual N-Channel 20-V (D-S) MOSFET, Common Drain

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
20	0.026 at V _{GS} = 4.5 V	8.5		
	0.030 at V _{GS} = 2.5 V	8		
	0.036 at V _{GS} = 1.8 V	7		

FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFET: 1.8 V Rated



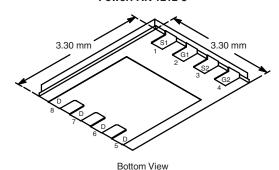
- Low Thermal Resistance, RthJC
- Low 1.07 mm Profile
- 3000 V ESD Protection

APPLICATIONS



RoHS

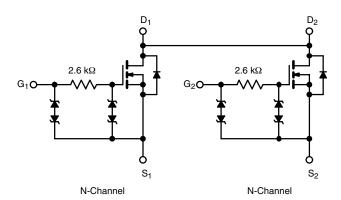
PowerPAK 1212-8



Ordering Information: Si7900AEDN-T1-E3 (Lead (Pb)-free)

Si7900AEDN-T1-GE3 (Lead (Pb)-free and Halogen-free)

Protection Switch for 1-2 Li-ion Batteries



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	20		V	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Current /T 150 °C\a	T _A = 25 °C	- I _D	8.5	6		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		6.4	4.3	^	
Pulsed Drain Current		I _{DM}	30		Α	
Continuous Source Current (Diode Conduction) ^a		I _S	2.9	1.4		
Mariana Barra Biratian I	T _A = 25 °C	- P _D	3.1	1.5	W	
Maximum Power Dissipation ^a	T _A = 85 °C		1.6	0.79		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifestura Instanta Assaultinată	t ≤ 10 s	R _{thJA}	32	40	°C/W
Maximum Junction-to-Ambient ^a	Steady State		65	82	
Maximum Junction-to-Case	Steady State	R_{thJC}	2.2	2.8	

Notes

a. Surface Mounted on 1" x 1" FR4 board.

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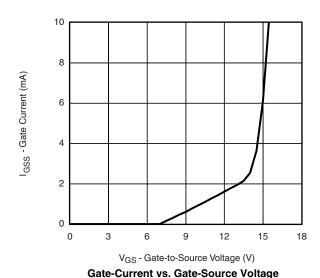
SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	0.40		0.9	V		
Gate-Body Leakage	I _{GSS} -	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1	μΑ		
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10	mA		
Zero Gate Voltage Drain Current	I _{DSS} -	V _{DS} = 20 V, V _{GS} = 0 V			1			
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			20	μΑ		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 4.5 V	20			Α		
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 8.5 \text{ A}$		0.020	0.026	Ω		
Drain-Source On-State Resistance ^a		$V_{GS} = 2.5 \text{ V}, I_D = 8 \text{ A}$		0.022	0.030			
		V _{GS} = 1.8 V, I _D = 7 A		0.026	0.036			
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 8.5 \text{ A}$		25		S		
Diode Forward Voltage ^a	V_{SD}	I _S = 2.9 A, V _{GS} = 0 V		0.65	1.1	V		
Dynamic ^b								
Total Gate Charge	Qg			10.5	16	nC		
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$		1.9				
Gate-Drain Charge	Q _{gd}			1.8				
Turn-On Delay Time	t _{d(on)}			0.85	1.25	- ns		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		1.3	2.0			
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 1$ A, $V_{GEN}=4.5$ V, $R_G=6~\Omega$		8.6	13			
Fall Time	t _f			4.2	6.5			

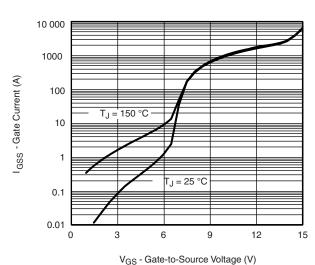
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

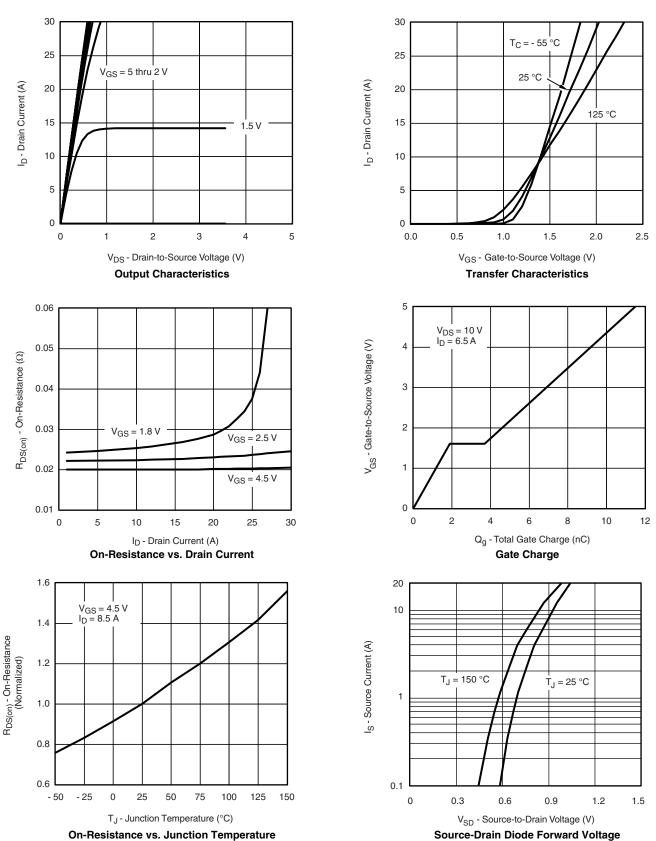




Gate Current vs. Gate-Source Voltage



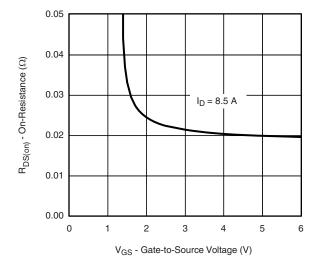
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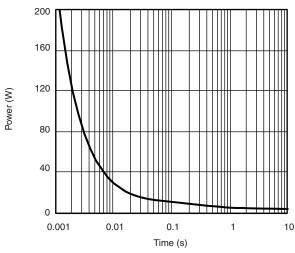
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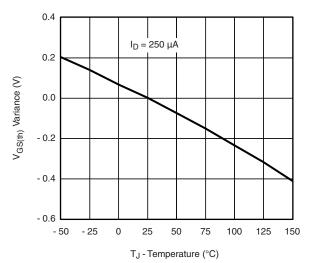
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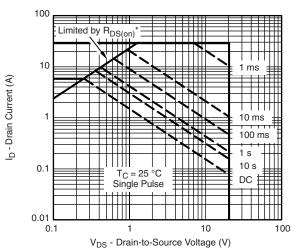
On-Resistance vs. Gate-to-Source Voltage



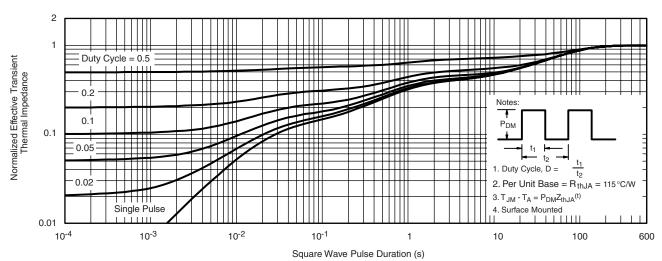
Single Pulse Power, Junction-to-Ambient







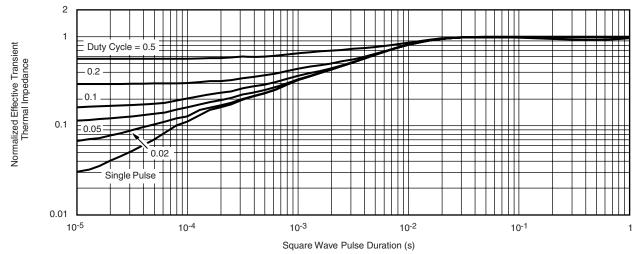
* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified **Safe Operating Area, Junction-to-Case**



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Case

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