

## 30V DUAL N AND P-CHANNEL ENHANCEMENT MODE MOSFET

**N-channel** 

**G1** 

**D1** 

**S1** 

### SUMMARY

### DESCRIPTION

This new generation of high density MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



**MSOP8** 

**P-channel** 

**G2** 

**D2** 

### **FEATURES**

- Low on-resistance
- Fast switching speed
- Low threshold
- · Low gate drive
- Low profile SOIC package

### **APPLICATIONS**

- DC DC converters
- Power management functions
- Disconnect switches
- Motor control

### **ORDERING INFORMATION**

DEVICE	REEL SIZE (inches)	TAPE WIDTH (mm)	QUANTITY PER REEL
ZXMD63C03XTA	7	12 embossed	1,000
ZXMD63C03XTC	13	12 embossed	4,000

### **DEVICE MARKING**

**ISSUE 2 - SEPTEMBER 2007** 

ZXM63C03

Pin-out

**S2** 

			_
S1	Ð	80	D1
G1	7	~	D1
S2	т	9	D2
G2	4	2	D2

**Top view** 



## **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	N-CHANNEL	P-CHANNEL	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	30	-30	V
Gate- Source Voltage	V <sub>GS</sub>	±20		V
Continuous Drain Current $(V_{GS}=4.5V; T_A=25^{\circ}C)(b)(d)$ $(V_{GS}=4.5V; T_A=70^{\circ}C)(b)(d)$	ID	2.3 1.8	-2.0 -1.6	AA
Pulsed Drain Current (c)(d)	I <sub>DM</sub>	14	-9.6	А
Continuous Source Current (Body Diode)(b)(d)	I <sub>S</sub>	1.5	-1.4	A
Pulsed Source Current (Body Diode)(c)(d)	I <sub>SM</sub>	14	-9.6	А
Power Dissipation at T <sub>A</sub> =25°C (a)(d) Linear Derating Factor	P <sub>D</sub>	0.87 6.9		W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (a)(e) Linear Derating Factor	P <sub>D</sub>	1.04 8.3		W mW/°C
Power Dissipation at T <sub>A</sub> =25°C (b)(d) Linear Derating Factor	P <sub>D</sub>	1.25 10		W mW/°C
Operating and Storage Temperature Range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150		°C

## THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(d)	R <sub>0JA</sub>	143	°C/W
Junction to Ambient (b)(d)	R <sub>0JA</sub>	100	°C/W
Junction to Ambient (a)(e)	$R_{\theta JA}$	120	°C/W

NOTES:

(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

(b) For a device surface mounted on FR4 PCB measured at t≤10 secs.

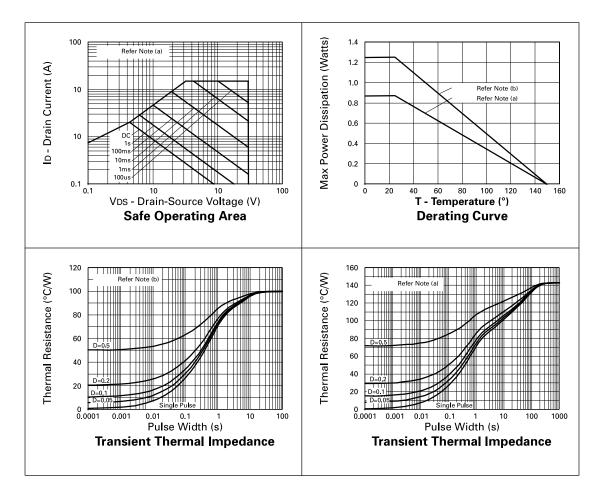
(c) Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

(d) For device with one active die.

(e) For device with two active die running at equal power.

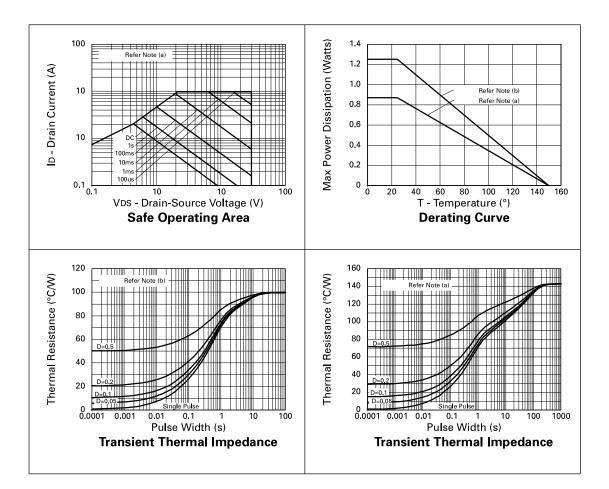






## **N-CHANNEL CHARACTERISTICS**

EMICONDUCTORS



### **P-CHANNEL CHARACTERISTICS**



ELECTRICAL CHARACTERISTICS (at T					I		
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	30			V	$I_D=250\mu A, V_{GS}=0V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μA	$V_{DS}$ =30V, $V_{GS}$ =0V	
Gate-Body Leakage	I <sub>GSS</sub>			100	nA	$V_{GS}$ = $\pm$ 20V, $V_{DS}$ =0V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	1.0			V	$I_{D}$ =250µA, $V_{DS}$ = $V_{GS}$	
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.135 0.200	Ω Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =1.7A V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.85A	
Forward Transconductance (3)	g <sub>fs</sub>	1.9			s	V <sub>DS</sub> =10V,I <sub>D</sub> =0.85A	
DYNAMIC (3)						•	
Input Capacitance	C <sub>iss</sub>		290		pF	V <sub>DS</sub> =25 V, V <sub>GS</sub> =0V, f=1MHz	
Output Capacitance	C <sub>oss</sub>		70		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>		20		pF		
SWITCHING(2) (3)			•			•	
Turn-On Delay Time	t <sub>d(on)</sub>		2.5		ns		
Rise Time	t <sub>r</sub>		4.1		ns	V <sub>DD</sub> =15V, I <sub>D</sub> =1.7A	
Turn-Off Delay Time	t <sub>d(off)</sub>		9.6		ns	$R_{G}=6.1\Omega$ , $R_{D}=8.7\Omega$ (Refer to test circuit)	
Fall Time	t <sub>f</sub>		4.4		ns		
Total Gate Charge Q <sub>g</sub>				8	nC		
Gate-Source Charge	0 <sub>gs</sub>			1.2	nC	V <sub>DS</sub> =24V,V <sub>GS</sub> =10V, I <sub>D</sub> =1.7A	
Gate Drain Charge	Q <sub>gd</sub>			2	nC	(Refer to test circuit	
SOURCE-DRAIN DIODE							
Diode Forward Voltage (1)	V <sub>SD</sub>			0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> =1.7A, V <sub>GS</sub> =0V	
Reverse Recovery Time (3)	t <sub>rr</sub>		16.9		ns	T <sub>j</sub> =25°C, I <sub>F</sub> =1.7A,	
Reverse Recovery Charge(3)	Q <sub>rr</sub>		9.5		nC	di/dt= 100A/µs	

### N-CHANNEL ELECTRICAL CHARACTERISTICS (at Tamb = 25°C unless otherwise stated).

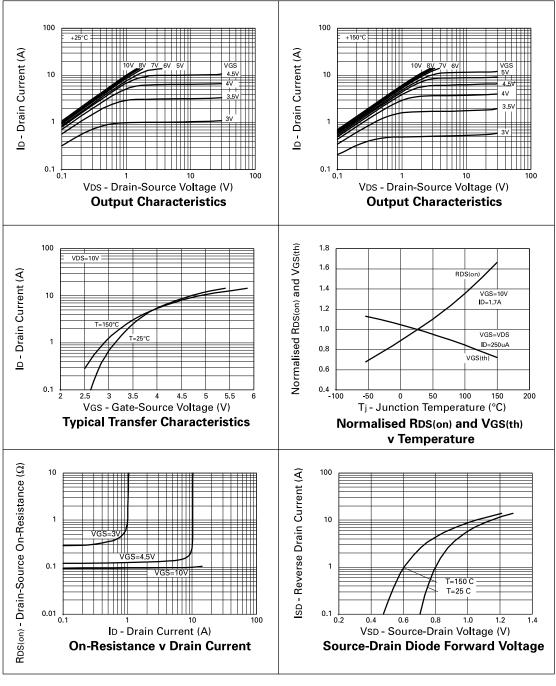
NOTES:

(1) Measured under pulsed conditions. Width=300 $\mu$ s. Duty cycle  $\leq$ 2%.

(2) Switching characteristics are independent of operating junction temperature.

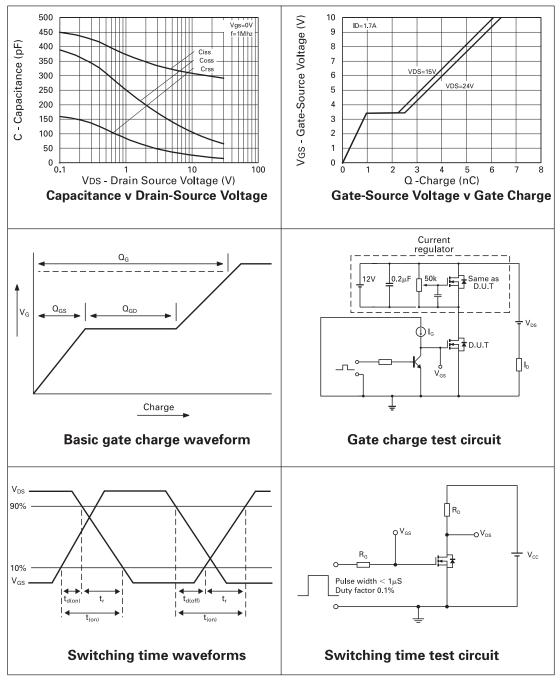
(3) For design aid only, not subject to production testing.





## **N-CHANNEL TYPICAL CHARACTERISTICS**





## **N-CHANNEL CHARACTERISTICS**



PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC	•					•	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	-30			V	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-1	μA	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	
Gate-Body Leakage	I <sub>GSS</sub>			±100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	-1.0			V	I <sub>D</sub> =-250μA, V <sub>DS</sub> =V <sub>GS</sub>	
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.185 0.27	Ω Ω	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.2A V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.6A	
Forward Transconductance (3)	9 <sub>fs</sub>	0.92			S	V <sub>DS</sub> =-10V,I <sub>D</sub> =-0.6A	
DYNAMIC (3)				-		-	
Input Capacitance	C <sub>iss</sub>		270		pF		
Output Capacitance	Coss		80		рF	V <sub>DS</sub> =-25 V, V <sub>GS</sub> =0V, f=1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		30		рF	1	
SWITCHING(2) (3)				-	-	-	
Turn-On Delay Time	t <sub>d(on)</sub>		2.6		ns		
Rise Time	t <sub>r</sub>		4.8		ns	V <sub>DD</sub> =-15V, I <sub>D</sub> =-1.2A	
Turn-Off Delay Time	t <sub>d(off)</sub>		13.1		ns	$R_{G}=6.2\Omega$ , $R_{D}=6.2\Omega$ (Refer to test circuit)	
Fall Time	t <sub>f</sub>		9.3		ns		
Total Gate Charge	Qg			7	nC		
Gate-Source Charge	Q <sub>gs</sub>			1.2	nC	V <sub>DS</sub> =-24V,V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.2A	
Gate Drain Charge	Q <sub>gd</sub>			2	nC	(Refer to test circuit	
SOURCE-DRAIN DIODE				-	-		
Diode Forward Voltage (1)	V <sub>SD</sub>			-0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V	
Reverse Recovery Time (3)	t <sub>rr</sub>		21.4		ns	T <sub>j</sub> =25°C, I <sub>F</sub> =-1.2A,	
Reverse Recovery Charge(3)	0 <sub>rr</sub>		15.7		nC	di/dt= 100A/µs	

### P-CHANNEL ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ}C$ unless otherwise stated).

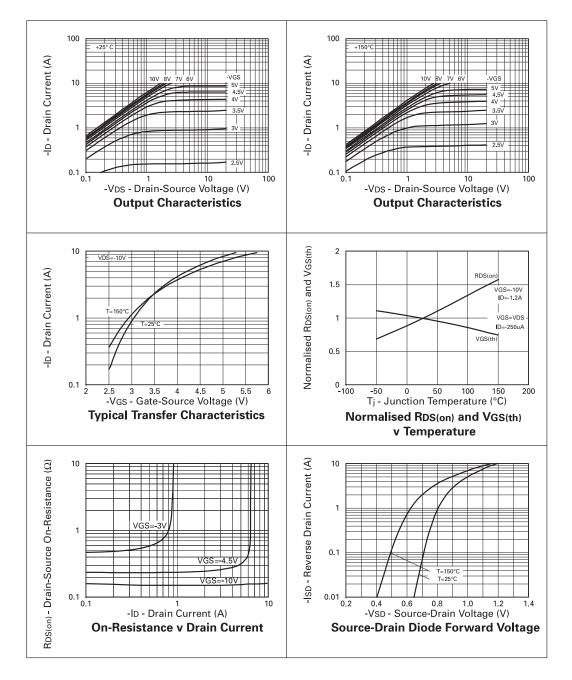
NOTES:

(1) Measured under pulsed conditions. Width=300 $\mu s.$  Duty cycle  ${\leq}2\%$  .

(2) Switching characteristics are independent of operating junction temperature.

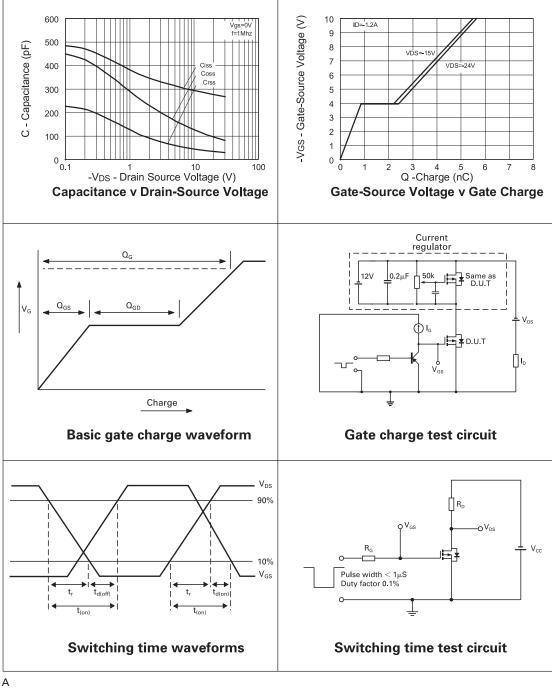
(3) For design aid only, not subject to production testing.





## **P-CHANNEL CHARACTERISTICS**





## P-CHANNEL TYPICAL CHARACTERISTICS



#### Definitions

#### Product change

Zetex Semiconductors reserves the right to alter, without notice, specifications, design, price or conditions of supply of any product or service. Customers are solely responsible for obtaining the latest relevant information before placing orders.

#### Applications disclaimer

The circuits in this design/application note are offered as design ideas. It is the responsibility of the user to ensure that the circuit is fit for the user's application and meets with the user's requirements. No representation or warranty is given and no liability whatsoever is assumed by Zetex with respect to the accuracy or use of such information, or infringement of patents or other intellectual property rights arising from such use or otherwise. Zetex does not assume any legal responsibility or will not be held legally liable (whether in contract, tort (including negligence), breach of statutory duty, restriction or otherwise) for any damages, loss of profit, business, contract, opportunity or consequential loss in the use of these circuit applications, under any circumstances.

#### Life support

Zetex products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Zetex Semiconductors plc. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body

or

2. support or sustain life and whose failure to perform when properly used in accordance with instructions

for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

#### Reproduction

The product specifications contained in this publication are issued to provide outline information only which (unless agreed by the company in writing) may not be used, applied or reproduced for any purpose or form part of any order or contract or be regarded as a representation relating to the products or services concerned.

#### **Terms and Conditions**

All products are sold subjects to Zetex' terms and conditions of sale, and this disclaimer (save in the event of a conflict between the two when the terms of the contract shall prevail) according to region, supplied at the time of order acknowledgement.

For the latest information on technology, delivery terms and conditions and prices, please contact your nearest Zetex sales office.

#### Quality of product

Zetex is an ISO 9001 and TS16949 certified semiconductor manufacturer.

To ensure quality of service and products we strongly advise the purchase of parts directly from Zetex Semiconductors or one of our regionally authorized distributors. For a complete listing of authorized distributors please visit: www.zetex.com/salesnetwork

Zetex Semiconductors does not warrant or accept any liability whatsoever in respect of any parts purchased through unauthorized sales channels.

#### ESD (Electrostatic discharge)

Semiconductor devices are susceptible to damage by ESD. Suitable precautions should be taken when handling and transporting devices. The possible damage to devices depends on the circumstances of the handling and transporting, and the nature of the device. The extent of damage can vary from immediate functional or parametric malfunction to degradation of function or performance in use over time. Devices suspected of being affected should be replaced.

#### Green compliance

Zetex Semiconductors is committed to environmental excellence in all aspects of its operations which includes meeting or exceeding regulatory requirements with respect to the use of hazardous substances. Numerous successful programs have been implemented to reduce the use of hazardous substances and/or emissions.

All Zetex components are compliant with the RoHS directive, and through this it is supporting its customers in their compliance with WEEE and ELV directives.

#### Product status key:

"Preview"Future device intended for production at some point. Samples may be available

"Active"Product status recommended for new designs

"Last time buy (LTB)"Device will be discontinued and last time buy period and delivery is in effect

"Not recommended for new designs"Device is still in production to support existing designs and production

"Obsolete"Production has been discontinued

Datasheet status key:

"Draft version"This term denotes a very early datasheet version and contains highly provisional

information, which may change in any manner without notice.

"Provisional version"This term denotes a pre-release datasheet. It provides a clear indication of anticipated performance. However, changes to the test conditions and specifications may occur, at any time and without notice.

"Issue" This term denotes an issued datasheet containing finalized specifications. However, changes to specifications may occur, at any time and without notice.



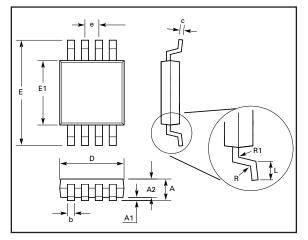
<u>4.8</u> 0.189

0.65

0.023

<u>mm</u> inches

## PACKAGE DIMENSIONS



DIM	Millimeters		Inc	hes	
	Min.	Max.	Min.	Max.	
А	-	1.10	-	0.0433	
A1	0.05	0.15	0.002	0.006	
A2	0.75	0.95	0.0295	0.0374	
b	0.25	0.40	0.010	0.0157	
С	0.13	0.23	0.005	0.009	
D	2.90	3.10	0.114	0.122	
Е	4.90	BSC	0.193	BSC	
E1	2.90	3.10	0.114	0.122	
е	0.65	BSC	0.025	BSC	
L	0.40	0.70	0.0157	0.0192	
R	0.07	-	0.0027	-	
R1	0.07	-	0.0027	-	

### © Zetex Semiconductors plc 2007

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH Kustermannpark Balanstraße 59 D-81541 München Germanv	Zetex Inc 700 Veterans Memorial Hwy Hauppauge, NY 11788 USA	Zetex (Asia) Ltd 3701-04 Metroplaza Tower 1 Hing Fong Road, Kwai Fong Hong Kong	Zetex Semiconductors plc Zetex Technology Park Chadderton, Oldham, OL9 9LL United Kingdom
Telefon: (49) 89 45 49 49 0 Fax: (49) 89 45 49 49 49 europe.sales@zetex.com	Telephone: (1) 631 360 2222 Fax: (1) 631 360 8222 usa.sales@zetex.com	Telephone: (852) 26100 611 Fax: (852) 24250 494 <u>asia.sales@zetex.com</u>	Telephone (44) 161 622 4444 Fax: (44) 161 622 4446 hq@zetex.com

**ISSUE 2 - SEPTEMBER 2007** 

## PAD LAYOUT DETAILS

 $\frac{1.02}{0.040}$ 

0.41

0.016