



Dual N-Channel 2.5-V (G-S) MOSFET, ESD Protected

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
20	0.030 at V _{GS} = 4.5 V	± 5.2		
	0.040 at V _{GS} = 2.5 V	± 4.5		

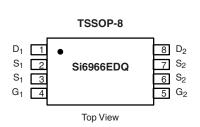
FEATURES

· Halogen-free

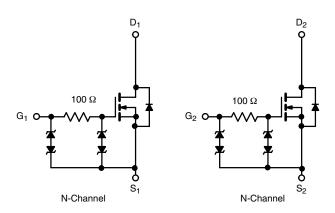
• ESD Protected: 4000 V



RoHS COMPLIANT



Ordering Information: Si6966EDG-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	± 12		
Continuous Dunin Comment /T 150 °C\A b	T _A = 25 °C	I _D	± 5.2		
Continuous Drain Current $(T_J = 150 {}^{\circ}\text{C})^{a, b}$	T _A = 70 °C		± 4.0	Δ.	
Pulsed Drain Current		I _{DM}	± 30	A	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	1.25		
Mariana Barra Biratani ah	T _A = 25 °C	P _D	1.25	W	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C] 'D	0.72		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian de la Ambienta	t ≤ 10 s	- R _{thJA}		110	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		115		C/VV	

Notes:

a. Surface Mounted on FR4 board.

b. $t \le 10 \text{ s}$.

Si6966EDQ

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.6			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = + 20 V, V _{GS} = 0 V			1	μΑ		
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C			25			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	30			Α		
D : 0	D	$V_{GS} = 4.5 \text{ V}, I_D = 5.2 \text{ A}$		0.021	0.030	0		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 4.5 A		0.028	0.040	Ω		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 5.2 A		20		S		
Diode Forward Voltage ^a	V_{SD}	I _S = 1.25 A, V _{GS} = 0 V		0.65	1.2	V		
Dynamic ^b								
Total Gate Charge	Q_g			15	25			
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5.2 \text{ A}$		2.5		nC		
Gate-Drain Charge	Q_{gd}			4.5				
Turn-On Delay Time	t _{d(on)}			100	200			
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		130	250			
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 4.5 V, R_G = 6 Ω		420	800	ns		
Fall Time	t _f			220	450			
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 1.25 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}$		210	500			

Notes:

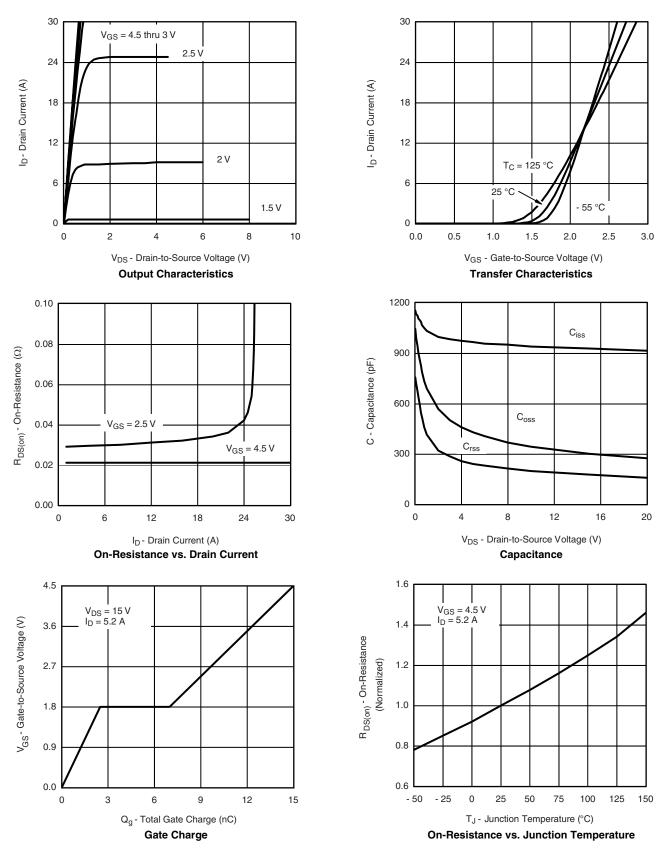
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

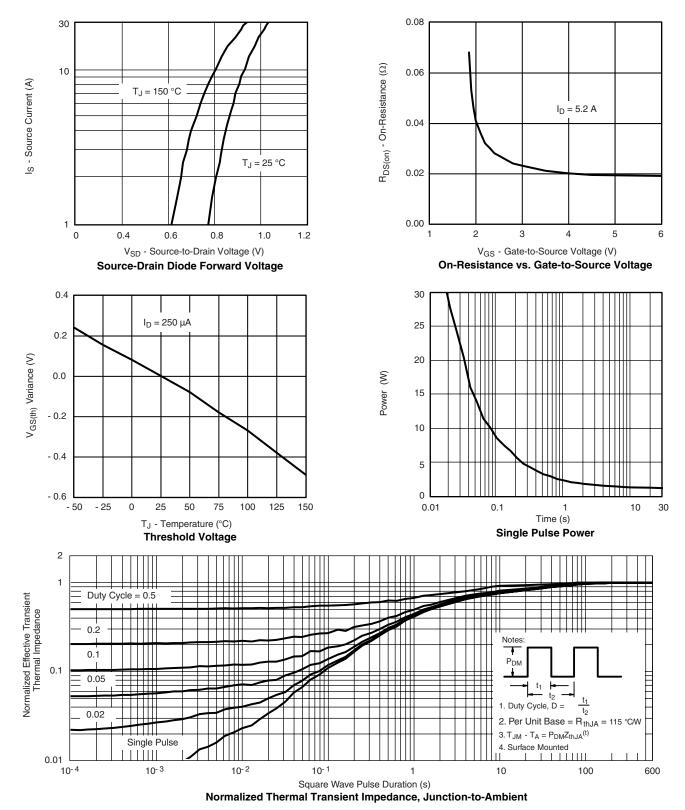


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