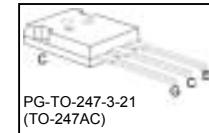
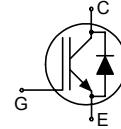


Fast IGBT in NPT-technology with soft, fast recovery anti-parallel EmCon diode

- 40lower E_{off} compared to previous generation
- Short circuit withstand time – 10 μ s
- Designed for:
 - Motor controls
 - Inverter
 - SMPS
- NPT-Technology offers:
 - very tight parameter distribution
 - high ruggedness, temperature stable behaviour
 - parallel switching capability
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹ for target applications
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>


PG-T0-247-3-21
(TO-247AC)

Type	V_{CE}	I_c	E_{off}	T_j	Marking	Package
SKW25N120	1200V	25A	2.9mJ	150°C	K25N120	PG-T0-247-3-21

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CE}	1200	V
DC collector current	I_c		A
$T_C = 25^\circ\text{C}$		46	
$T_C = 100^\circ\text{C}$		25	
Pulsed collector current, t_p limited by T_{jmax}	I_{Cpuls}	84	
Turn off safe operating area	-	84	
$V_{CE} \leq 1200\text{V}, T_j \leq 150^\circ\text{C}$			
Diode forward current	I_F		
$T_C = 25^\circ\text{C}$		42	
$T_C = 100^\circ\text{C}$		25	
Diode pulsed current, t_p limited by T_{jmax}	I_{Fpuls}	80	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ²	t_{SC}	10	μs
$V_{GE} = 15\text{V}, 100\text{V} \leq V_{CC} \leq 1200\text{V}, T_j \leq 150^\circ\text{C}$			
Power dissipation	P_{tot}	313	W
$T_C = 25^\circ\text{C}$			
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature, wavesoldering, 1.6mm (0.063 in.) from case for 10s	T_s	260	

¹ J-STD-020 and JESD-022

² Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		0.4	K/W
Diode thermal resistance, junction – case	R_{thJCD}		1.15	
Thermal resistance, junction – ambient	R_{thJA}		40	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}$, $I_C=1500\mu\text{A}$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}$, $I_C=25\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	2.5 -	3.1 3.7	3.6 4.3	
Diode forward voltage	V_F	$V_{GE}=0\text{V}$, $I_F=25\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		2.0 1.75	2.5	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=1000\mu\text{A}$, $V_{CE}=V_{GE}$	3	4	5	
Zero gate voltage collector current	I_{CES}	$V_{CE}=1200\text{V}$, $V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			350 1400	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}$, $V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20\text{V}$, $I_C=25\text{A}$		20	-	S

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V}$, $V_{GE}=0\text{V}$, $f=1\text{MHz}$	-	2150	2600	pF
Output capacitance	C_{oss}		-	260	310	
Reverse transfer capacitance	C_{rss}		-	110	130	
Gate charge	Q_{Gate}	$V_{CC}=960\text{V}$, $I_C=25\text{A}$ $V_{GE}=15\text{V}$	-	225	300	nC
Internal emitter inductance	L_E		-	13	-	nH
Measured 5mm (0.197 in.) from case						
Short circuit collector current ¹⁾	$I_{C(\text{SC})}$	$V_{GE}=15\text{V}$, $t_{SC}\leq 10\mu\text{s}$ $100\text{V}\leq V_{CC}\leq 1200\text{V}$, $T_j \leq 150^\circ\text{C}$	-	240	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s

Switching Characteristic, Inductive Load, at $T_j=25\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			Min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25\text{ }^\circ\text{C}$, $V_{CC}=800\text{V}$, $I_C=25\text{A}$, $V_{GE}=15/0\text{V}$, $R_G=22\Omega$, $L_\sigma^{(1)}=180\text{nH}$, $C_\sigma^{(1)}=40\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	45	60	ns
Rise time	t_r		-	40	52	
Turn-off delay time	$t_{d(off)}$		-	730	950	
Fall time	t_f		-	30	39	
Turn-on energy	E_{on}		-	2.2	2.9	mJ
Turn-off energy	E_{off}		-	1.5	2.0	
Total switching energy	E_{ts}		-	3.7	4.9	

Anti-Parallel Diode Characteristic

Diode reverse recovery time	t_{rr}	$T_j=25\text{ }^\circ\text{C}$, $V_R=800\text{V}$, $I_F=25\text{A}$, $di_F/dt=650\text{A}/\mu\text{s}$	-	90		ns
	t_s		-			
	t_F		-			
Diode reverse recovery charge	Q_{rr}		-	1.0		μC
Diode peak reverse recovery current	I_{rrm}		-	20		A
Diode peak rate of fall of reverse recovery current during t_F	di_{rr}/dt		-	470		$\text{A}/\mu\text{s}$

Switching Characteristic, Inductive Load, at $T_j=150\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			Min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=150\text{ }^\circ\text{C}$, $V_{CC}=800\text{V}$, $I_C=25\text{A}$, $V_{GE}=15/0\text{V}$, $R_G=22\Omega$, $L_\sigma^{(1)}=180\text{nH}$, $C_\sigma^{(1)}=40\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	50	60	ns
Rise time	t_r		-	36	43	
Turn-off delay time	$t_{d(off)}$		-	820	990	
Fall time	t_f		-	42	50	
Turn-on energy	E_{on}		-	3.8	4.6	mJ
Turn-off energy	E_{off}		-	2.9	3.8	
Total switching energy	E_{ts}		-	6.7	8.4	

Anti-Parallel Diode Characteristic

Diode reverse recovery time	t_{rr}	$T_j=150\text{ }^\circ\text{C}$, $V_R=800\text{V}$, $I_F=25\text{A}$, $di_F/dt=750\text{A}/\mu\text{s}$	-	280		ns
	t_s		-			
	t_F		-			
Diode reverse recovery charge	Q_{rr}		-	4.3		μC
Diode peak reverse recovery current	I_{rrm}		-	32		A
Diode peak rate of fall of reverse recovery current during t_F	di_{rr}/dt		-	130		$\text{A}/\mu\text{s}$

¹⁾ Leakage inductance L_σ and stray capacity C_σ due to dynamic test circuit in figure E.

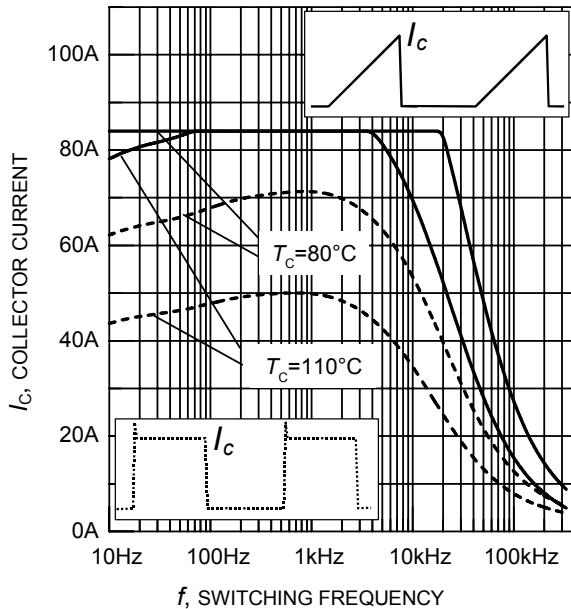


Figure 1. Collector current as a function of switching frequency

($T_j \leq 150^\circ\text{C}$, $D = 0.5$, $V_{CE} = 800\text{V}$,
 $V_{GE} = +15\text{V}/0\text{V}$, $R_G = 22\Omega$)

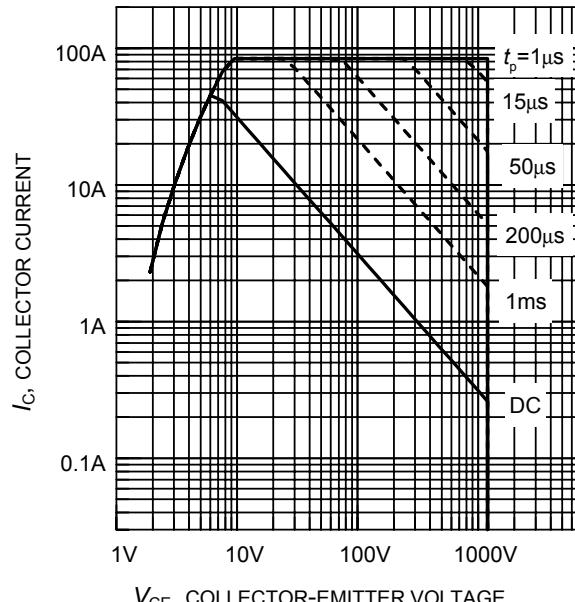


Figure 2. Safe operating area

($D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 150^\circ\text{C}$)

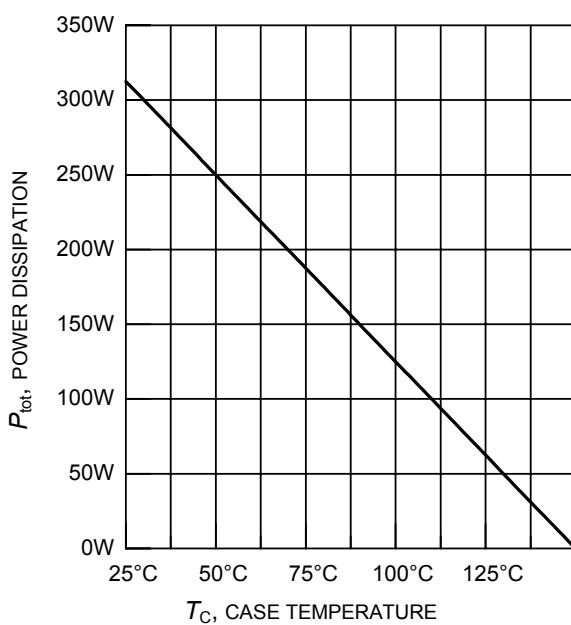


Figure 3. Power dissipation as a function of case temperature

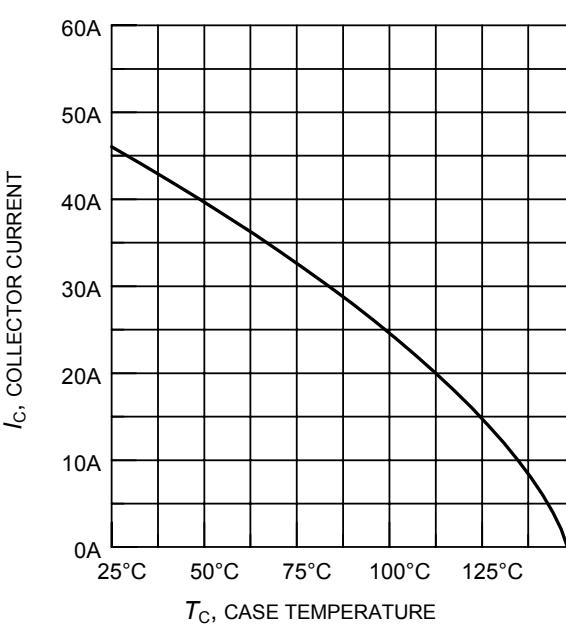


Figure 4. Collector current as a function of case temperature

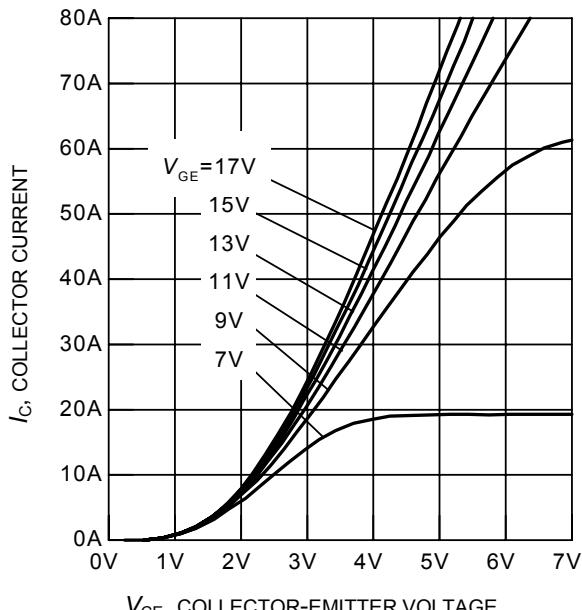


Figure 5. Typical output characteristics
($T_j = 25^\circ\text{C}$)

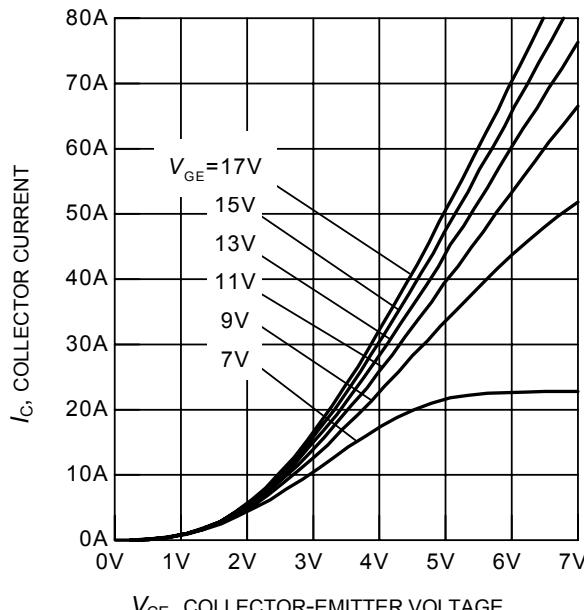


Figure 6. Typical output characteristics
($T_j = 150^\circ\text{C}$)

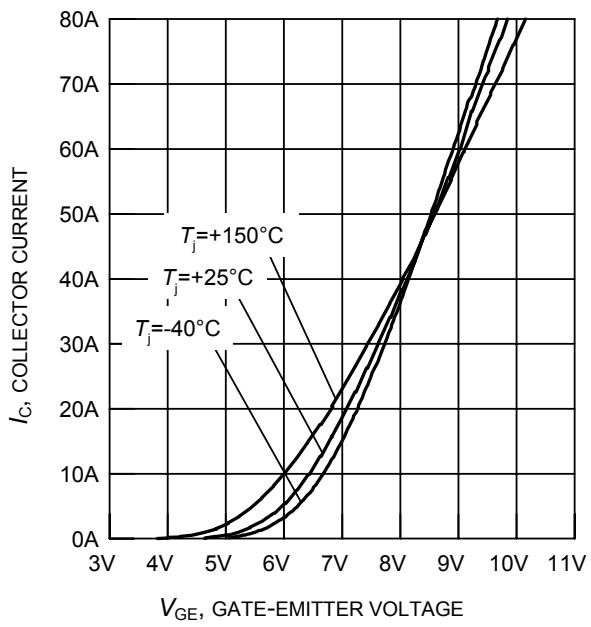


Figure 7. Typical transfer characteristics
($V_{CE} = 20\text{V}$)

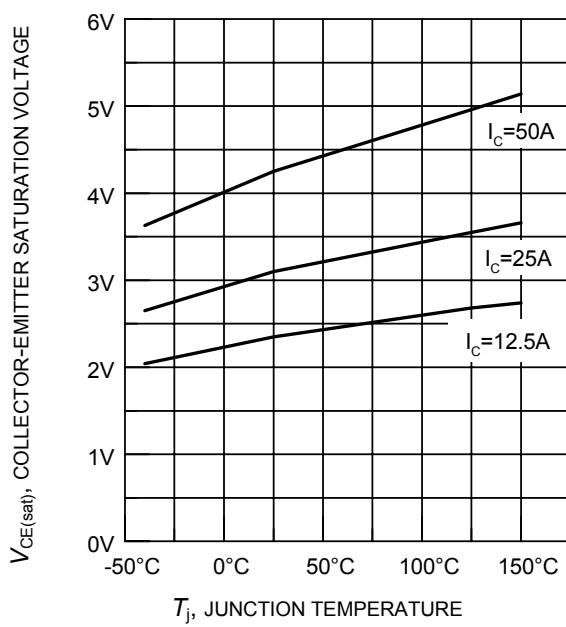
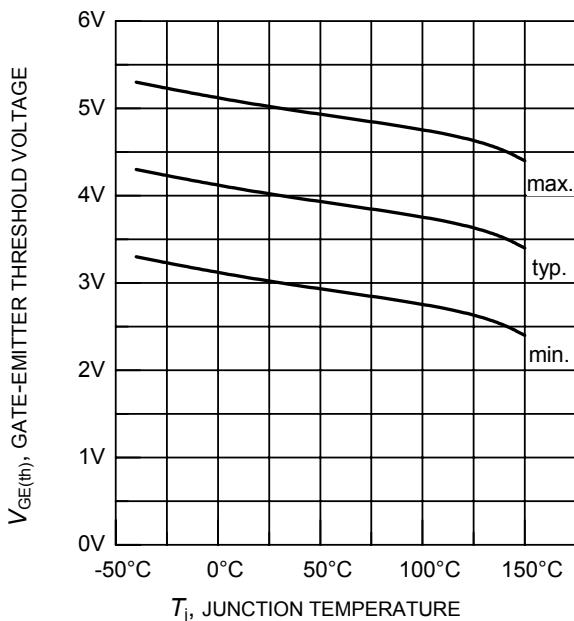
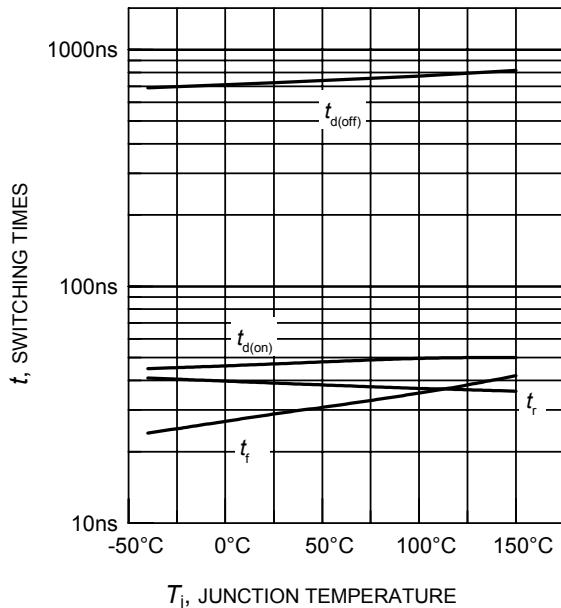
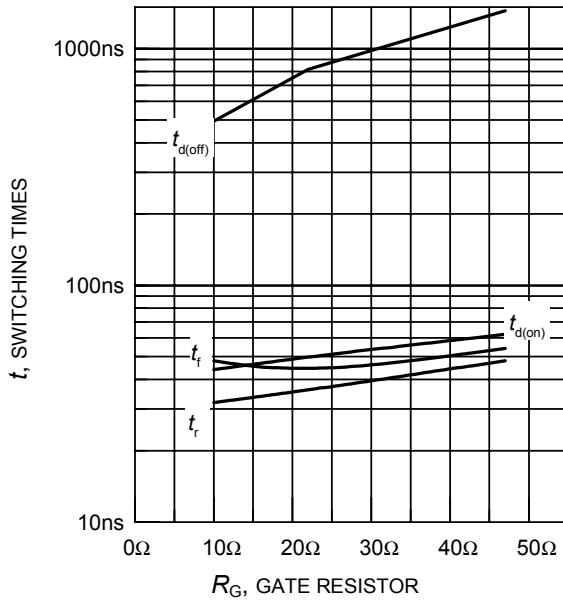
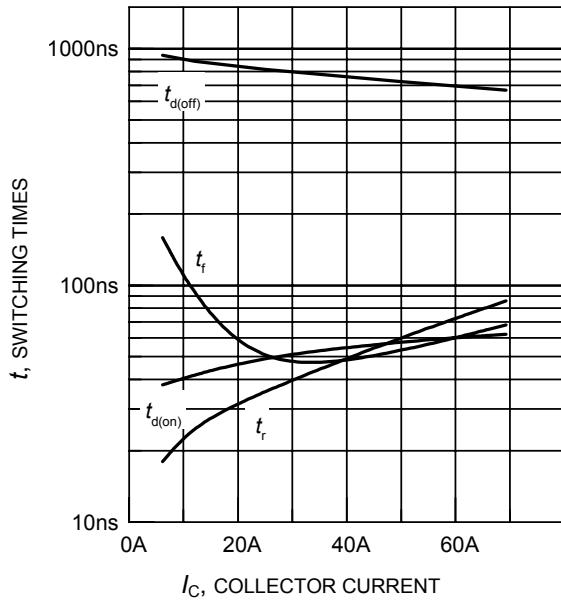
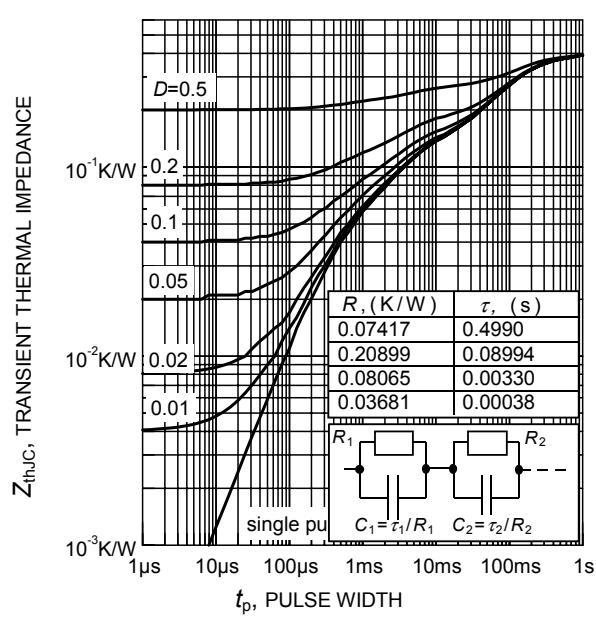
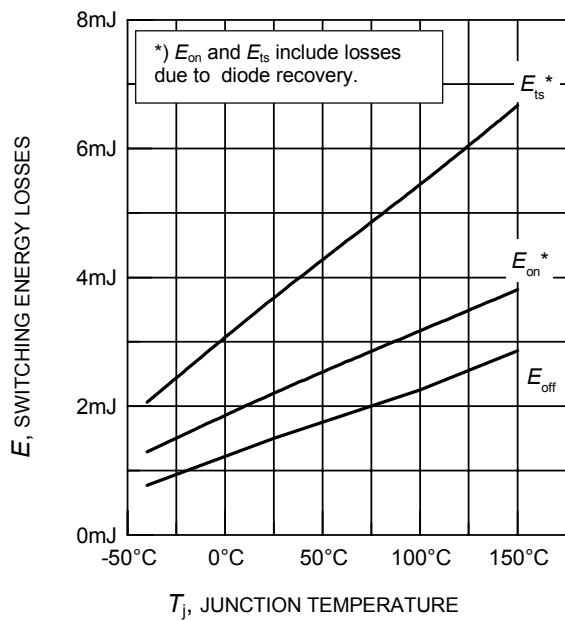
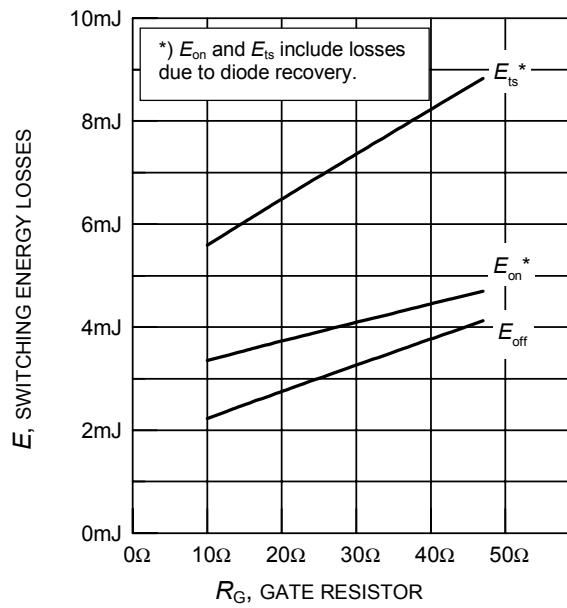
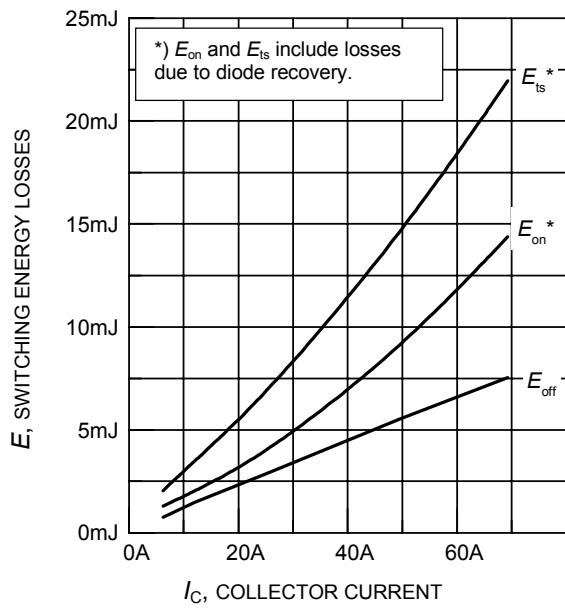


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)





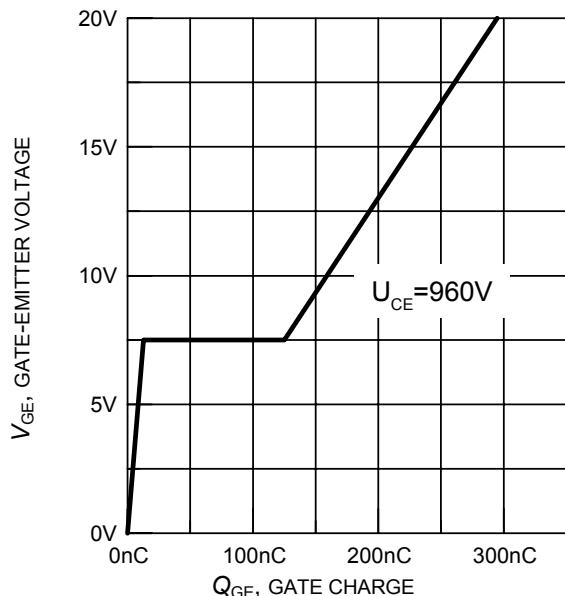


Figure 17. Typical gate charge
($I_C = 25A$)

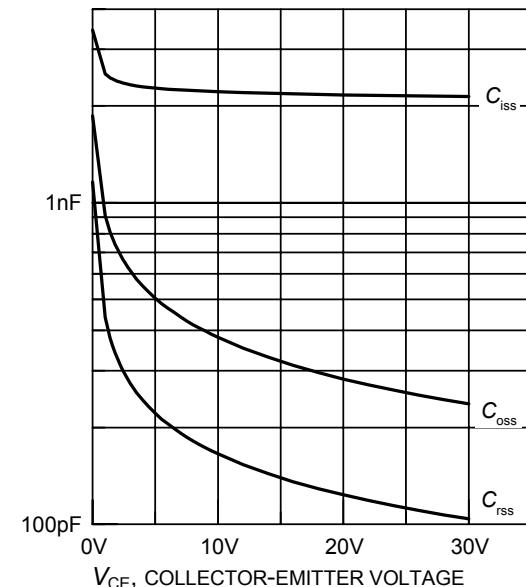


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0V, f = 1MHz$)

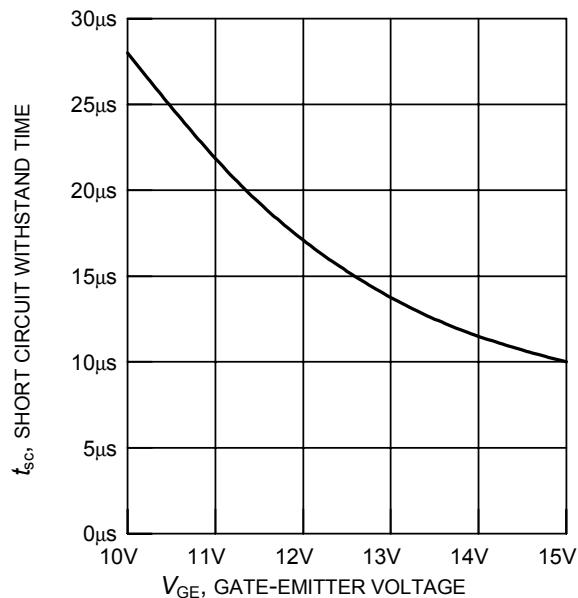


Figure 19. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE} = 1200V$, start at $T_j = 25^\circ C$)

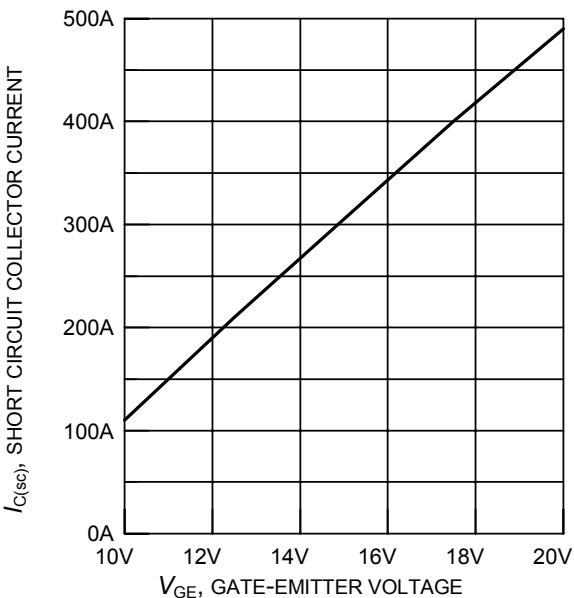


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage
($100V \leq V_{CE} \leq 1200V, T_C = 25^\circ C, T_j \leq 150^\circ C$)

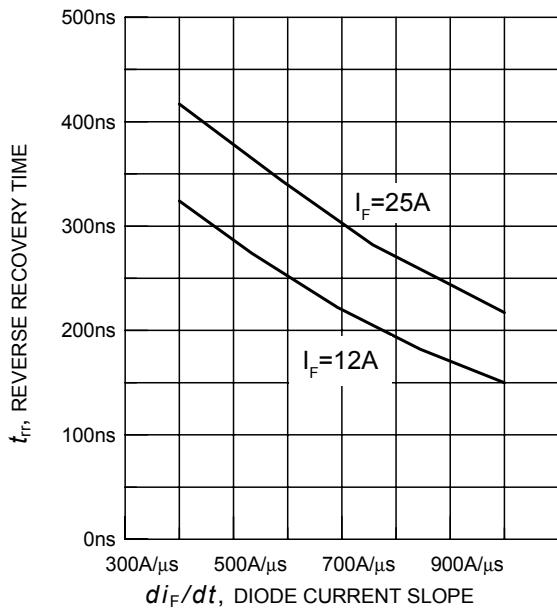


Figure 21. Typical reverse recovery time as a function of diode current slope
 $(V_R = 800V, T_j = 150^\circ C,$
dynamic test circuit in Fig.E)

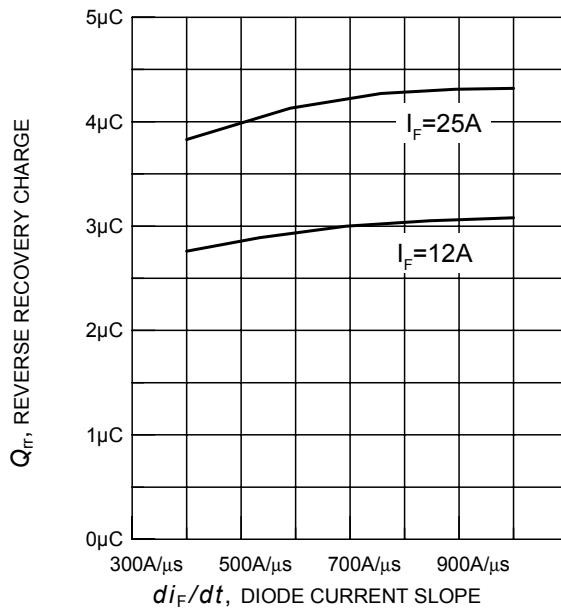


Figure 22. Typical reverse recovery charge as a function of diode current slope
 $(V_R = 800V, T_j = 150^\circ C,$
dynamic test circuit in Fig.E)

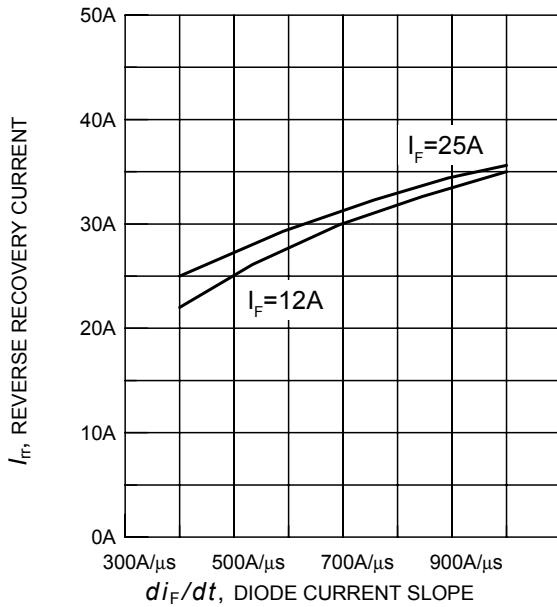


Figure 23. Typical reverse recovery current as a function of diode current slope
 $(V_R = 800V, T_j = 150^\circ C,$
dynamic test circuit in Fig.E)

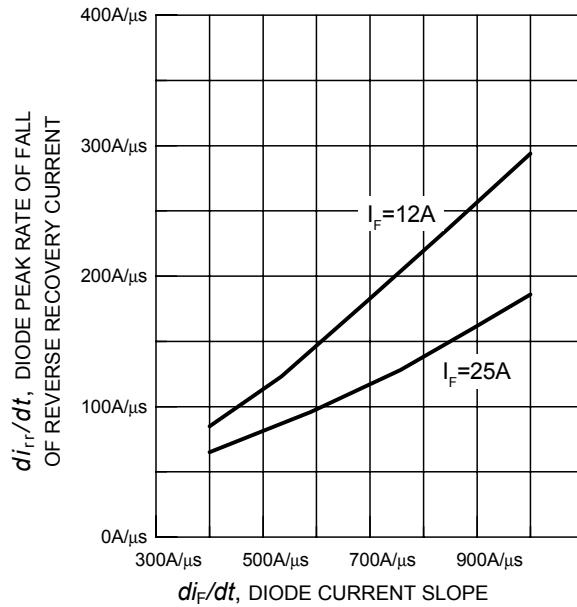


Figure 24. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope
 $(V_R = 800V, T_j = 150^\circ C,$
dynamic test circuit in Fig.E)

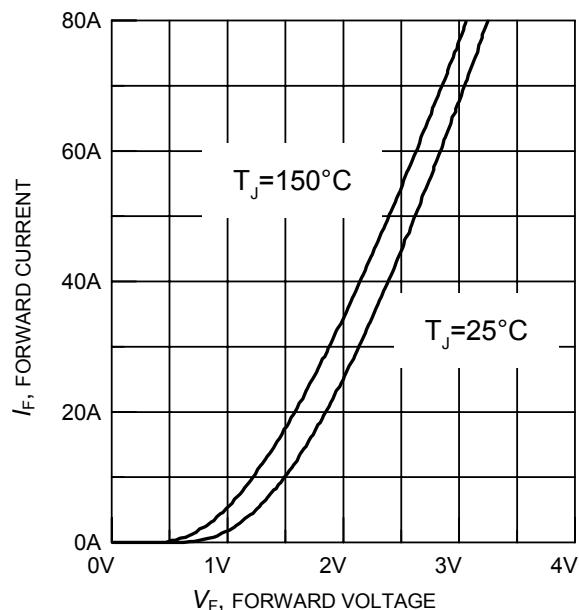


Figure 25. Typical diode forward current as a function of forward voltage

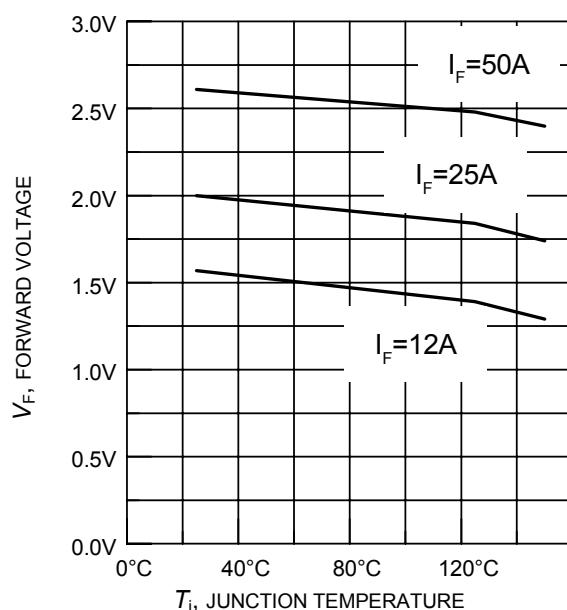


Figure 26. Typical diode forward voltage as a function of junction temperature

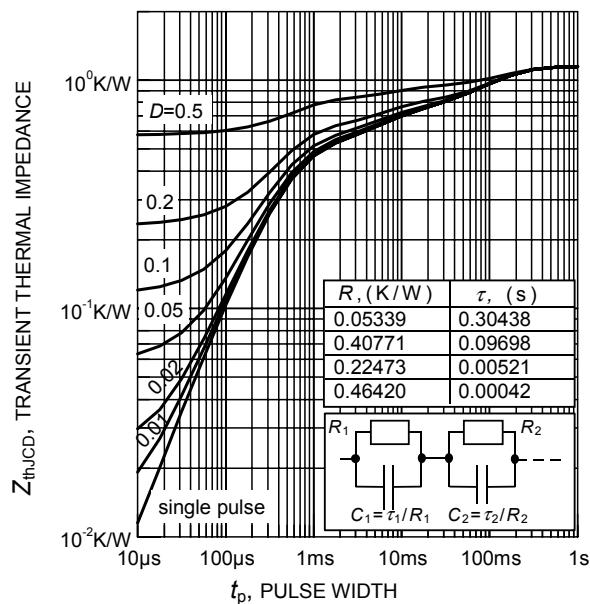
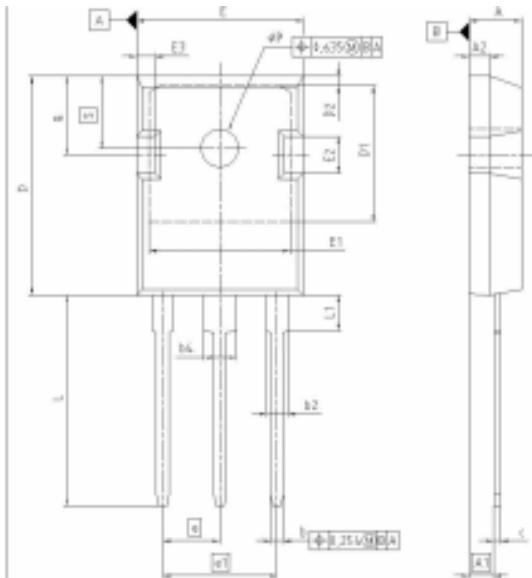


Figure 27. Diode transient thermal impedance as a function of pulse width
($D = t_p / T$)

PG-T0247-3-21



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.365	5.157	0.173	0.203
A1	2.273	2.527	0.090	0.098
A2	1.653	2.107	0.075	0.081
b	1.073	1.327	0.047	0.052
b2	1.903	2.306	0.075	0.096
b4	2.870	3.454	0.113	0.138
c	0.549	0.752	0.024	0.030
D	29.823	24.077	0.920	0.930
D1	17.323	17.851	0.682	0.702
D2	1.083	1.317	0.042	0.052
E	15.773	16.027	0.614	0.634
E1	13.893	14.147	0.547	0.567
E2	3.083	3.907	0.125	0.155
E3	1.163	1.997	0.060	0.076
F	5.450		0.215	
f1	10.900		0.430	
H	8		3	
L	29.053	20.307	0.799	0.799
L1	4.186	4.472	0.164	0.176
eP	3.558	3.661	0.140	0.144
Q	5.490	5.747	0.220	0.226
S	6.943	6.297	0.270	0.248

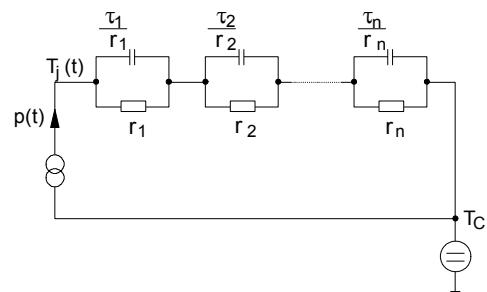
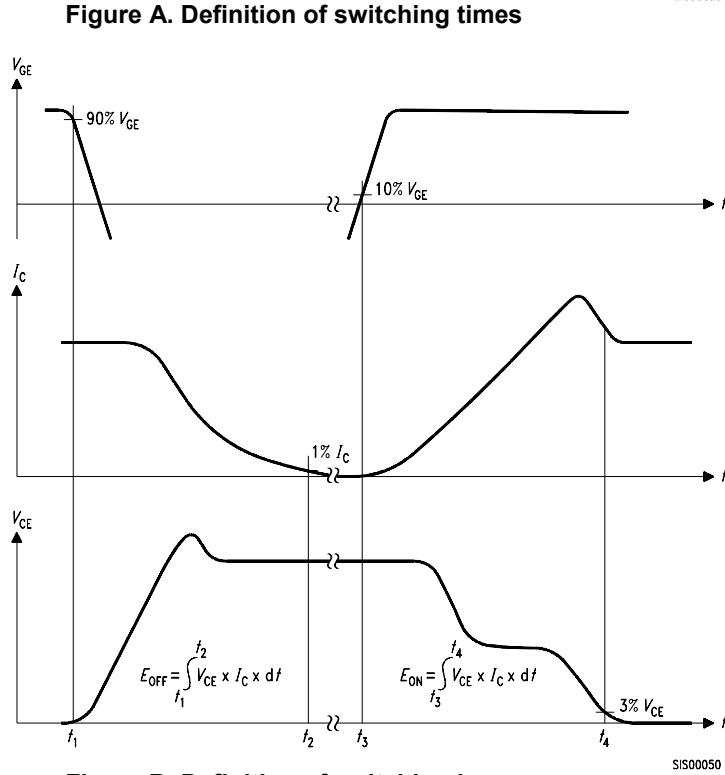
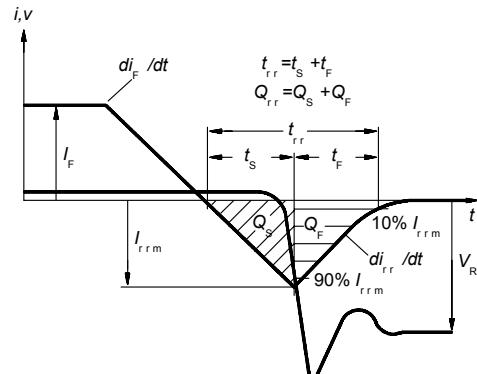
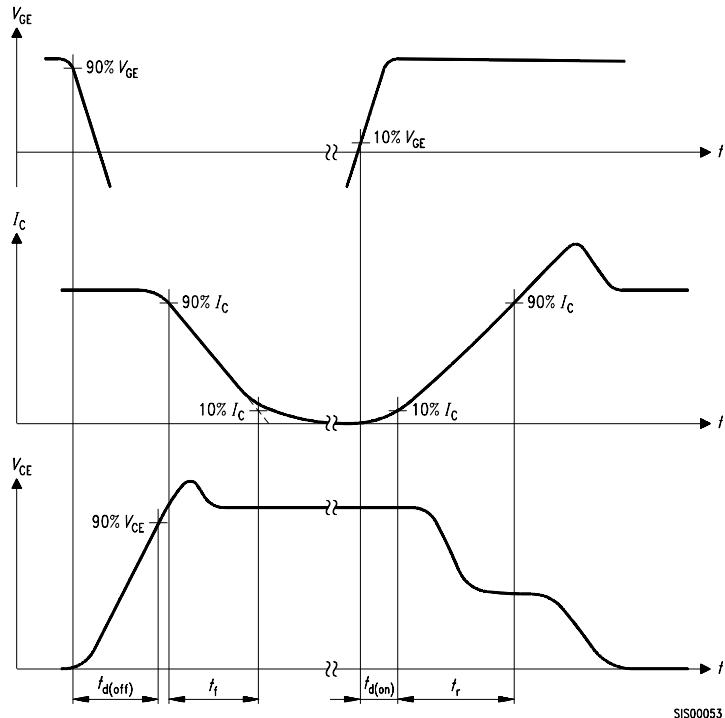


Figure D. Thermal equivalent circuit

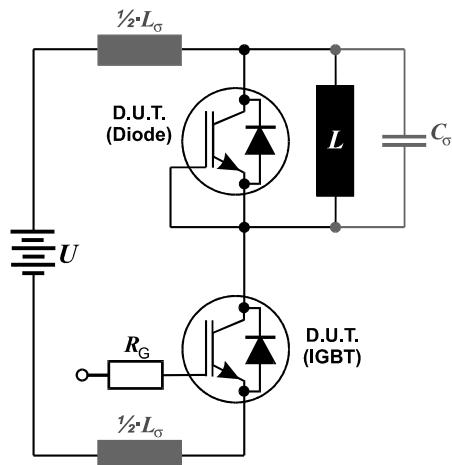


Figure E. Dynamic test circuit
Leakage inductance $L_\sigma = 180\text{nH}$,
and stray capacity $C_\sigma = 40\text{pF}$.

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