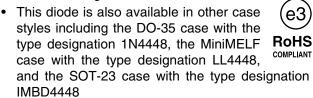


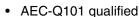
Small Signal Fast Switching Diode

Features

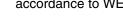
- · Silicon Epitaxial Planar Diode
- · Fast switching diode







 Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC



Mechanical Data

Case: SOD-123

Weight: approx. 10.3 mg Packaging codes/options:

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box GS08 / 3 k per 7" reel (8 mm tape), 15 k/box



Parts Table

Part	Ordering code	de Type Marking	
1N4448W-V	1N4448W-V-GS18 or 1N4448W-V-GS08	A3	Tape and Reel

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

allib	II			
Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		V_R	75	V
Peak reverse voltage		V _{RM}	100	V
Average rectified current half wave rectification with resistive load	f ≥ 50 Hz	I _{F(AV)}	150 ¹⁾	mA
Surge current	$t < 1 \text{ s and } T_j = 25 ^{\circ}\text{C}$	I _{FSM}	500	mA
Power dissipation		P _{tot}	500 ¹⁾	mW

¹⁾ Valid provided that electrodes are kept at ambient temperature.



Thermal Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to ambient air		R _{thJA}	350 ¹⁾	K/W
Junction temperature		Tj	150	°C
Storage temperature		T _{stg}	- 65 to + 150	°C

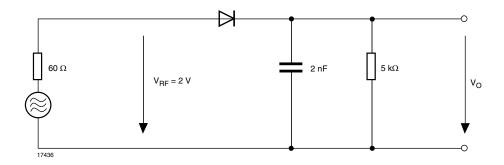
¹⁾ Valid provided that leads at a distance of 8 mm from case are kept at ambient temperature.

Electrical Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min.	Тур.	Max.	Unit
Forward voltage	I _F = 5 mA	V _F	0.62		0.72	V
	I _F = 100 mA	V _F			1	V
Leakage current	V _R = 20 V	I _R			25	nA
	V _R = 75 V	I _R			5	μΑ
	V _R = 20 V, T _J = 150 °C	I _R			50	μΑ
Capacitance	$V_F = V_R = 0 V$				4	pF
Reverse recovery time	I_F = 10 mA to I_R = 10 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}			4	ns
Rectification efficiency	f = 100 MHz, V _{RF} = 2 V	ην	0.45			

Rectification Efficieny Measurement Circuit



Rev. 1.4, 17-Aug-10



Typical Characteristics

T_{amb} = 25 °C unless otherwise specified

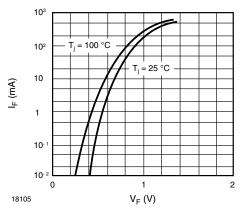


Figure 1. Forward characteristics

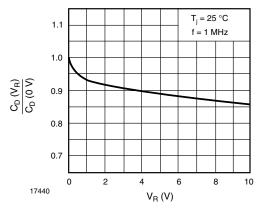


Figure 4. Relative Capacitance vs. Reverse Voltage

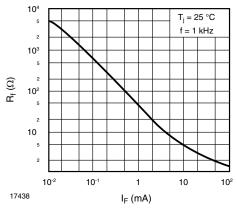


Figure 2. Dynamic Forward Resistance vs. Forward Current

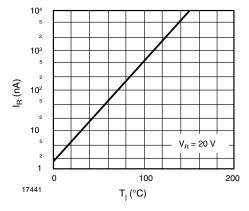


Figure 5. Leakage Current vs. Junction Temperature

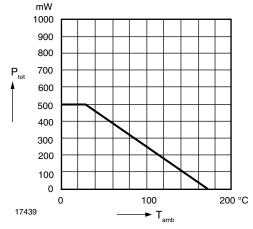


Figure 3. Admissible Power Dissipation vs. Ambient Temperature



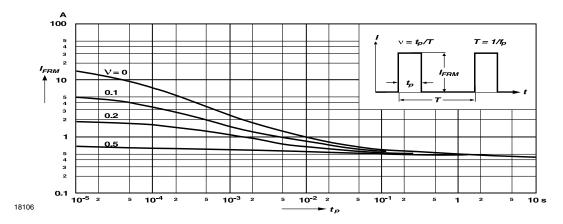
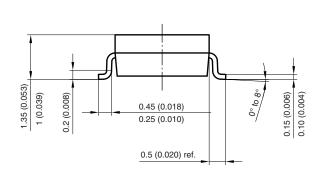
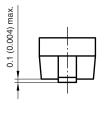


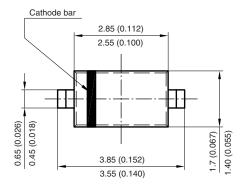
Figure 6. Admissible Repetitive Peak Forward Current vs. Pulse Duration

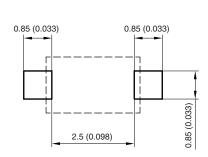
Package Dimensions in millimeters (inches): SOD-123





Mounting Pad Layout





Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4)

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