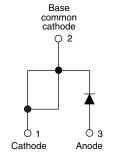


Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt®





TO-247AC modified

PRODUCT SUMMARY				
Package TO-247AC modified (2 pir				
I _{F(AV)} 30 A				
V_{R}	600 V			
V _F at I _F	2.6 V			
t _{rr} typ.	See Recovery table			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Single diode device
- Compliant to RoHS Directive 2002/95/EC
- · Designed and qualified for industrial level





ROHS

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 116 °C	30	Δ.	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	300	A	
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-		
Farmend wells as		I _F = 30 A	-	2.0	2.6	V	
Forward voltage V _F	I _F = 30 A, T _J = 150 °C	-	1.34	1.75			
Devenue leeke ne euwent		V _R = V _R rated	-	0.3	50	^	
Reverse leakage current I _R	I'R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{ rated}$	-	60	500	μΑ	
Junction capacitance	C _T	V _R = 600 V	-	33	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body - 3.5 -		nΗ			

VS-30EPH06PbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t _{rr}		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	
	T _J = 25 °C		-	31	-	ns	
		T _J = 125 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	77	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	3.5	-	Α
	IRRM	T _J = 125 °C		-	7.7	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	65	-	nC
	Q _{rr}	T _J = 125 °C		-	345	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	OL TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	0.5	0.9	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-	
Weight			-	6.0	-	g
			-	0.22	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC modified		30EI	PH06	



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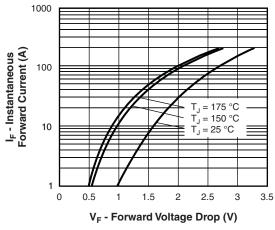


Fig. 1 - Typical Forward Voltage Drop Characteristics

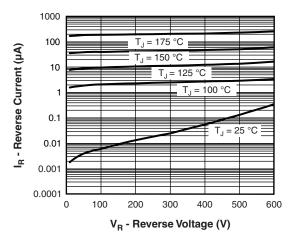


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

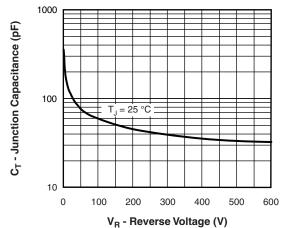


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

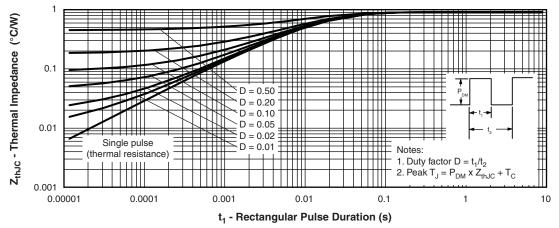


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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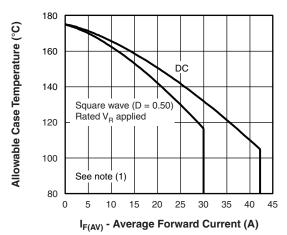


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

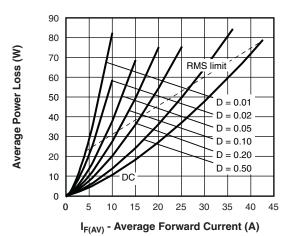


Fig. 6 - Forward Power Loss Characteristics

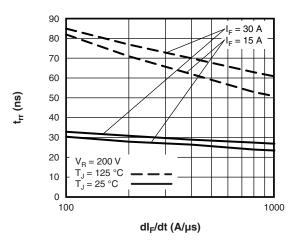


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

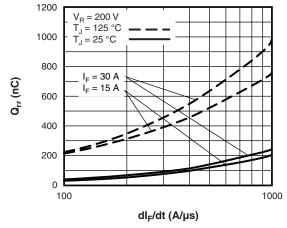


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6)}; \\ \end{array}$ Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R



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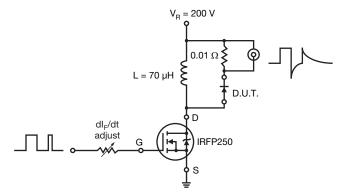
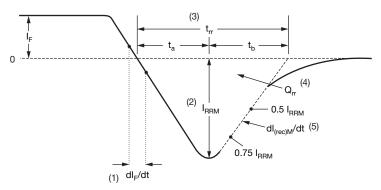


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_E/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

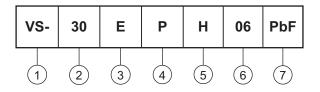
Fig. 10 - Reverse Recovery Waveform and Definitions

Vishay Semiconductors Hyperfast Rectifier, 30 A FRED Pt®



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Current rating (30 = 30 A)

Circuit configuration:

E = Single diode

Package:

P = TO-247AC modified

H = Hyperfast recovery

Voltage rating (06 = 600 V)

PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95253</u>					
Part marking information	www.vishay.com/doc?95255				

Legal Disclaimer Notice



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