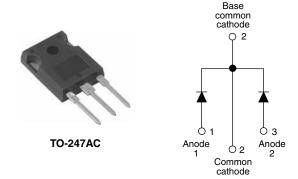


Vishay Semiconductors

Ultrafast Rectifier, 2 x 15 A FRED Pt®



PRODUCT SUMMARY					
Package	TO-247AC				
I _{F(AV)}	2 x 15 A				
V _R	300 V				
V _F at I _F	1.25 V				
t _{rr} typ.	See Recovery table				
T _J max.	175 °C				
Diode variation	Common cathode				

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level



DESCRIPTION/APPLICATIONS

300 V series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage		V_{RRM}		300	V
Average rectified forward current ————————————————————————————————————	per leg	I _{F(AV)}	T _C = 142 °C	15	
	total device			30	Α
Non-repetitive peak surge current per leg		I _{FSM}	T _J = 25 °C	140	
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	300	-	-	
Forward voltage V _F	V	I _F = 15 A	-	1.05	1.25	V
	VF	I _F = 15 A, T _J = 125 °C	=	0.85	1.00	
Reverse leakage current I _R	V _R = V _R rated	=	0.05	40		
	IR.	T _J = 125 °C, V _R = V _R rated	=	12	400	μΑ
Junction capacitance	C _T	V _R = 300 V	-	45	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	=	8	-	nΗ

VS-30CPH03PbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
l.		$I_F = 1.0 A, dI_F/dt =$	= 1.0 A, dl _F /dt = 50 A/µs, V _R = 30 V		-	40	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	32	-	ns
	T _J = 125 °C	$I_F = 15 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_P = 200 \text{ V}$	-	45	-		
Peak recovery current I _{RRM}	T _J = 25 °C		-	2.4	-	۸	
	$T_{J} = 125 ^{\circ}\text{C}$ $V_{B} = 200 \text{V}$		=	6.1	-	А	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C	11	-	38	-	nC
	Q _{rr}	T _J = 125 °C		-	137	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	0.9	2.0	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-	
Weight			-	6.0	-	g
vveignt			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC		30CF	PH03	



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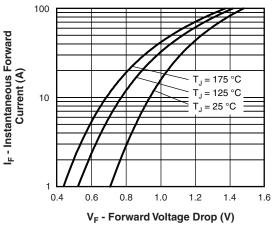


Fig. 1 - Typical Forward Voltage Drop Characteristics

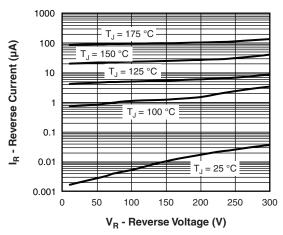


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

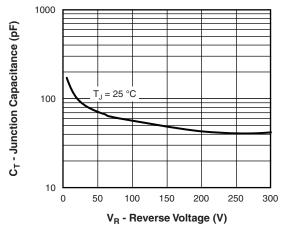


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

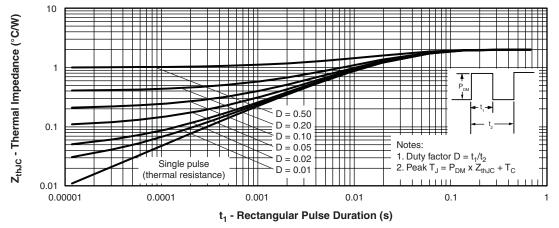


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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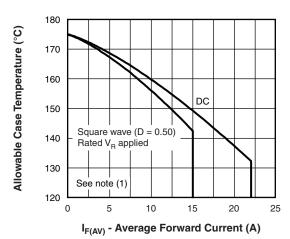


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

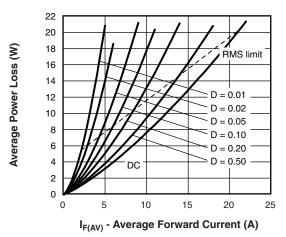


Fig. 6 - Forward Power Loss Characteristics

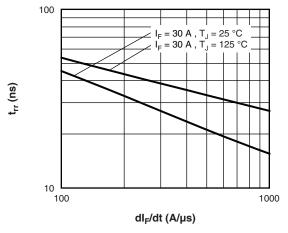


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

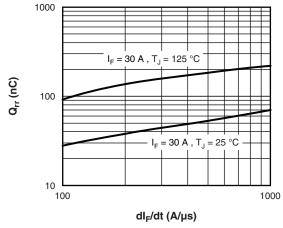


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



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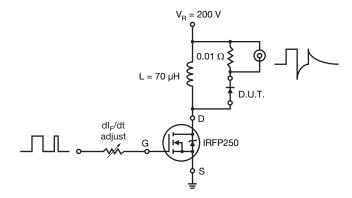
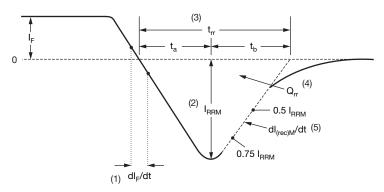


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

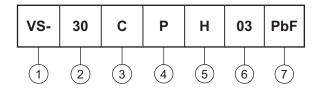
VS-30CPH03PbF

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 = 30 A)

3 - Circuit configuration

4 - Package:

P = TO-247

5 - H = Hyperfast recovery

6 - Voltage rating (03 = 300 V)

7 - PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95223</u>				
Part marking information	www.vishay.com/doc?95226			

Legal Disclaimer Notice



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