

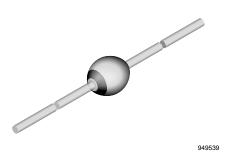
Vishay Semiconductors

RoHS COMPLIANT

HALOGEN

FREE

Ultra-Fast Avalanche Sinterglass Diode



MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

Mounting position: any

Weight: approx. 369 mg

FEATURES

- · Glass passivated
- · Hermetically sealed axial leaded glass envelope
- Low reverse current
- High reverse voltage
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

APPLICATIONS

- Switched mode power supplies
- · High-frequency inverter circuits

PARTS TABLE		
PART	TYPE DIFFERENTIATION	PACKAGE
SF4001	V _R = 50 V; I _{FAV} = 1 A	SOD-57
SF4002	V _R = 100 V; I _{FAV} = 1 A	SOD-57
SF4003	V _R = 200 V; I _{FAV} = 1 A	SOD-57
SF4004	V _R = 400 V; I _{FAV} = 1 A	SOD-57
SF4005	V _R = 600 V; I _{FAV} = 1 A	SOD-57
SF4006	V _R = 800 V; I _{FAV} = 1 A	SOD-57
SF4007	V _R = 1000 V; I _{FAV} = 1 A	SOD-57

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	SF4001	$V_{R} = V_{RRM}$	50	V
		SF4002	$V_{R} = V_{RRM}$	100	V
		SF4003	$V_{R} = V_{RRM}$	200	V
		SF4004	$V_{R} = V_{RRM}$	400	V
		SF4005	$V_{R} = V_{RRM}$	600	V
		SF4006	$V_{R} = V_{RRM}$	800	V
		SF4007	$V_{R} = V_{RRM}$	1000	V
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	30	А
Average forward current	Lead length I = 10 mm		I _{FAV}	1	А
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C
Non repetitive reverse avalanche energy	I _{(BR)R} = 0.4 A		E _R	10	mJ

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MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Junction ambient	Lead length I = 10 mm, T_L = constant	R _{thJA}	45	K/W
Junction ambient	On PC board with spacing 25 mm	R _{thJA}	100	K/W

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 1 A	SF4001	V _F	-	-	1	V
		SF4002	V _F	-	-	1	V
		SF4003	V _F	-	-	1	V
		SF4004	V _F	-	-	1	V
		SF4005	V _F	-	-	1.7	V
		SF4006	V _F	-	-	1.7	V
		SF4007	V _F	-	-	1.7	V
Reverse current	$V_{R} = V_{RRM}$		I _R	-	-	5	μA
	$V_{R} = V_{RRM}, T_{j} = 125 \text{ °C}$		I _R	-	-	50	μA
	I _R = 100 μA	SF4001	V _{(BR)R}	50	-	-	V
		SF4002	V _{(BR)R}	100	-	-	V
		SF4003	V _{(BR)R}	200	-	-	V
Reverse breakdown voltage		SF4004	V _{(BR)R}	400	-	-	V
		SF4005	V _{(BR)R}	600	-	-	V
		SF4006	V _{(BR)R}	800	-	-	V
		SF4007	V _{(BR)R}	1000	-	-	V
	I _F = 0.5 A, I _R = 1 A, i _R = 0.25 A	SF4001	t _{rr}	-	-	50	ns
		SF4002	t _{rr}	-	-	50	ns
Reverse recovery time		SF4003	t _{rr}	-	-	50	ns
		SF4004	t _{rr}	-	-	50	ns
		SF4005	t _{rr}	-	-	75	ns
		SF4006	t _{rr}	-	-	75	ns
		SF4007	t _{rr}	-	-	75	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

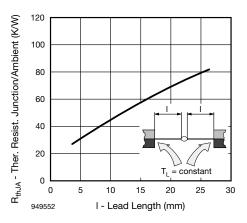


Fig. 1 - Max. Thermal Resistance vs. Lead Length

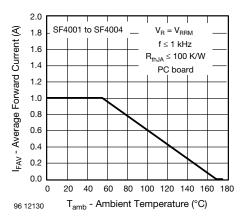


Fig. 2 - Max. Average Forward Current vs. Ambient Temperature

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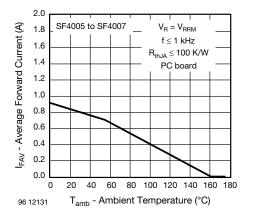


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

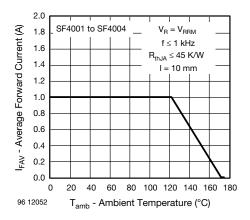


Fig. 4 - Max. Average Forward Current vs. Ambient Temperature

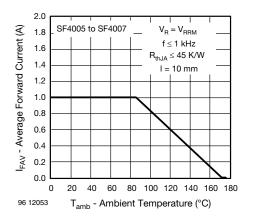


Fig. 5 - Max. Average Forward Current vs. Ambient Temperature

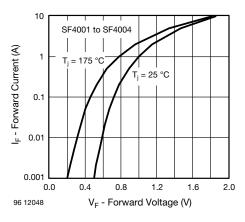


Fig. 6 - Max. Forward Current vs. Forward Voltage

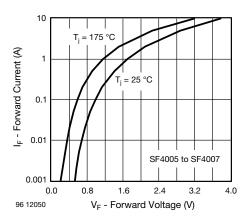


Fig. 7 - Max. Forward Current vs. Forward Voltage

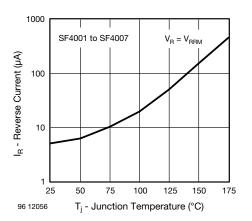


Fig. 8 - Max. Reverse Current vs. Junction Temperature

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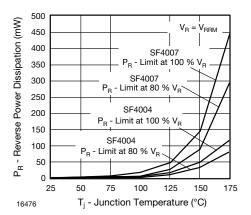


Fig. 9 - Max. Reverse Power Dissipation vs. Junction Temperature

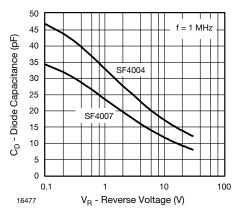
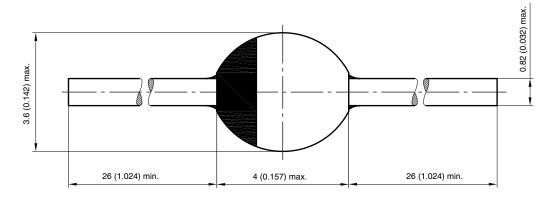


Fig. 10 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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