





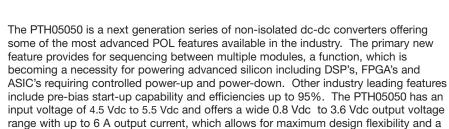
DC-DC CONVERTERS

POLA Non-isolated

NEW Product



- 5 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 3.6 Vdc)
- Auto-track[™] sequencing*
- Pre-bias start-up capability
- Efficiencies up to 95%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant









2 YEAR WARRANTY

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated C $_{in}$ = 100 $\mu\text{F},$ C $_{out}$ = 0 μF

SPECIFICATIONS

OUTPUT SPECIFICATIONS

pathway for future upgrades.

Voltage adjustability	(See Note 4)	0.8-3.6 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typ.
Load regulation		±12 mV typ.
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwidth	20 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response (See Note 5)	Overshoot	70 µs recovery time /undershoot 100 mV

INPUT SPECIFICATIONS

Input voltage range	(See Note 3)	4.5-5.5 Vdc
Input current	No load	10 mA typ.
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		3.7-4.3 Vdc typ.
Track input voltage	Pin 2 (See Note 6, 7)	±0.3 Vin

EMC CHARACTERISTICS

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

GENERAL SPECIFICATIONS

Efficiency	(See Efficienc	y Table)	95% max.
Insulation voltage			Non-isolated
Switching frequency		550	kHz to 650 kHz
Approvals and standards			EN60950 UL/cUL60950
Material flammability			UL94V-0
Dimensions	$(L \times W \times H)$		2.57 x 8.50 mm 0.495 x 0.335 in
Weight			2.9 g (0.10 oz)
MTBF	Telcordia SR-	332	7,092,000 hours

ENVIRONMENTAL SPECIFICATIONS

Thermal performance	Operating ambient,	-40 °C to +85 °C		
(See Note 2)	temperature Non-operating	-40 °C to +125 °C		
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3		

PROTECTION

Short-circuit Auto reset 12 A typ.

International Safety Standard Approvals



UL/cUL CAN/CSA-C22.2 No. 60950-1-03/UL 60950-1, File No. E174104



TÜV Product Service (EN60950) Certificate No. B 04 06 38572 044 CB Report and Certificate to IEC60950, Certificate No. US/8292/UL

*Auto-track™ is a trade mark of Texas Instruments

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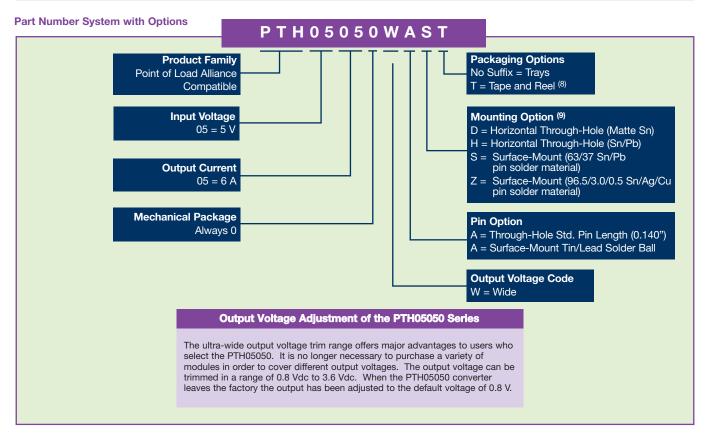


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NEW Product

OUTPUT POWER	INPUT	OUTPUT	OUTPUT	OUTPUT	EFFICIENCY	REGU	LATION	MODEL
(MAX.)	VOLTAGE	VOLTAGE	(MIN.)	(MAX.)	(MAX.)	LINE	LOAD	NUMBER (9,10)
21.6 W	4.5-5.5 Vdc	0.8-3.6 Vdc	0 A	6 A	95%	±10 mV	±12 mV	PTH05050



Notes

Remote ON/OFF. Positive Logic Pin 3 open; or V > Vin - 0.5 V Pin 3 GND; or V < 0.8 V (min - 0.2 V).

See Figure 1 for safe operating curve.

A 100 µF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 300 mA rms of ripple current.

An external output capacitor is not required for basic operation. Adding $100~\mu\text{F}$ of distributed capacitance at the load will improve the transient response.

Tesporse.

1 A/μs load step, 50 to 100% I_{omax}, C_{out} = 100 μF.

If utilized Vout will track applied voltage by ±0.3 V (up to Vo set point).

The pre-bias start-up feature is not compatible with Auto-Track TM. This is because when the module is under Auto-Track TM control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track to function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 158 for more details.

- Tape and reel packaging only available on the surface-mount versions.
- To order Pb-free (RoHS compatible) surface-mount parts replace the mounting option 'S' with 'Z', e.g. PTH05050WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH05050WAD.
- NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.

EFFICIENCY TABLE (I _O = 4 A)				
OUTPUT VOLTAGE	EFFICIENCY			
Vo = 1.0 V	85%			
Vo = 1.2 V	87%			
Vo = 1.5 V	89%			
Vo = 1.8 V	90%			
Vo = 2.0 V	91%			
Vo = 2.5 V	93%			
Vo = 3.3 V	95%			







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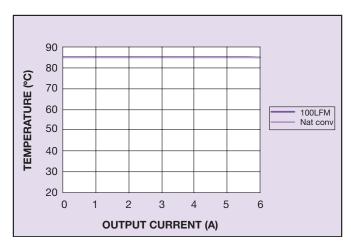


Figure 1 - Safe Operating Area
Vin = 5 V, Output Voltage = 3.3 V (See Note A)

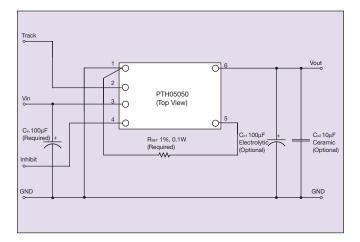


Figure 3 - Standard Application

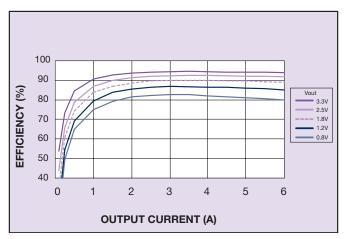


Figure 2 - Efficiency vs Load Current Vin = 5 V (See Note B)

Notes

- A SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- B Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.







DC-DC CONVERTERS POLA Non-isolated 4

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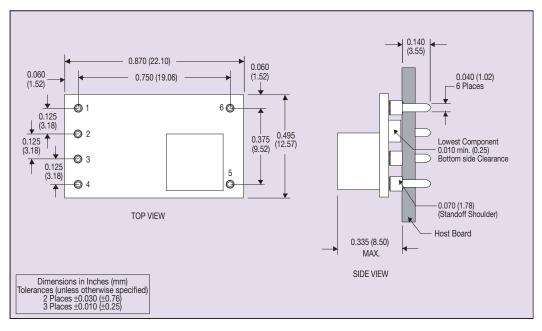


Figure 4 - Plated Through-Hole Mechanical Drawing

PIN CONNECTIONS			
PIN NO.	FUNCTION		
1	Ground		
2	Track		
3	Vin		
4	Inhibit*		
5	Vo adjust		
6	Vout		

*Denotes negative logic: Open = Normal operation Ground = Function active

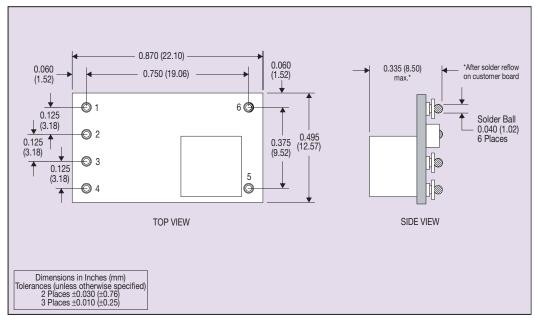


Figure 5 - Surface-Mount Mechanical Drawing

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Please consult our website for the following items: ✓ Application Note

www.artesyn.com