Vishay High Power Products

Ultrafast Soft Recovery Diode, 80 A FRED Pt[™]



- · Ultrafast recovery
- 175 °C operating junction temperature
- · Screw mounting only
- · Lead (Pb)-free plating
- · Designed and qualified for industrial level
- · Compliant to RoHS directive 2002/95/EC

BENEFITS

- · Reduced RFI and EMI
- · Higher frequency operation
- · Reduced snubbing

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

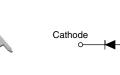
The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		200	V	
Continuous forward current	I _{F(AV)}	T _C = 112 °C	80		
Single pulse forward current	I _{FSM}	T _C = 25 °C	800	А	
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	160		
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _r	I _R = 50 μA	200	-	-	
Forward voltage	V	I _F = 80 A	-	0.98	1.13	V
	V _F	I _F = 80 A, T _J = 175 °C	-	0.79	0.92	
Reverse leakage current	1-	$V_{R} = V_{R}$ rated	-	-	50	ν μA mA
	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	2	mA
Junction capacitance	CT	V _R = 200 V		89	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body		3.5	-	nH

For technical questions, contact: diodestech@vishay.com





Anode -C

35 ns

80 A

200 V



PRODUCT SUMMARY

t_{rr}

I_{F(AV)}

 V_R

· Reduced parts count

DESCRIPTION/APPLICATIONS



RoHS





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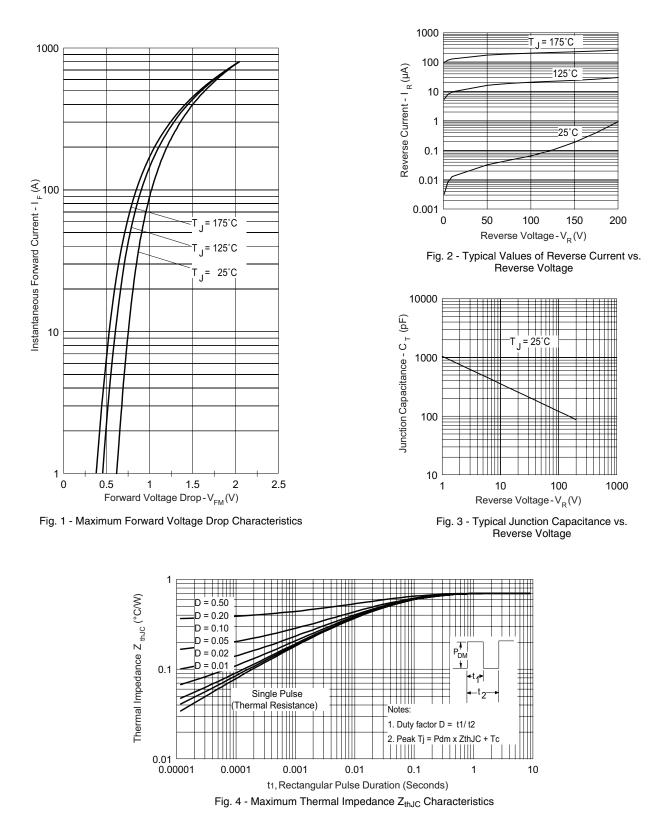
DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200$	A/μs, V _R = 30 V	-	-	35	ns
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 80 A V _R = 160 V dI _F /dt = 200 A/μs	-	32	-	
		T _J = 125 °C		-	52	-	
Peak recovery current		T _J = 25 °C		-	4.4	-	A
	IRRM	T _J = 125 °C		-	8.8	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	70	-	nC
		T _J = 125 °C		-	240	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R _{thJC}		-	-	0.70	K/W
Thermal resistance, junction to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	- r∖/vv
Weight			-	-	5.02	g
weight		-	0.18	-	oz.	
Mounting torque			1.2 (10)	-	2.4 (20)	N ⋅ m (lbf ⋅ in)
Marking device		Case style PowerTab [™]		80El	3U02	



80EBU02

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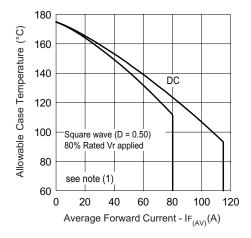


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

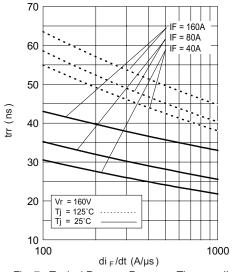


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

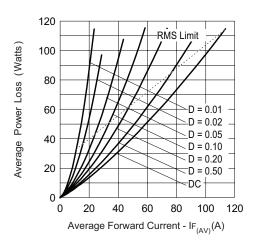
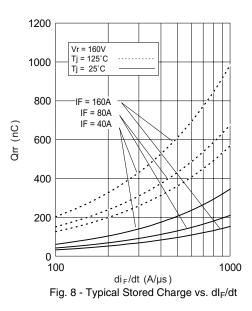


Fig. 6 - Forward Power Loss Characteristics



Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = 80 \%$ rated V_R

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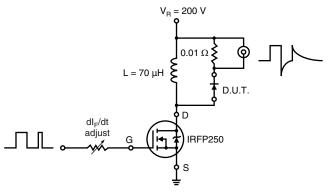
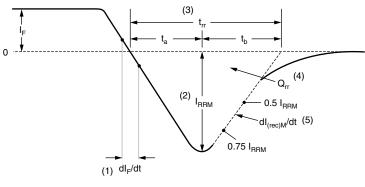
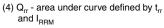


Fig. 9 - Reverse Recovery Parameter Test Circuit



(1) dI_F/dt - rate of change of current through zero crossing



 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{c}$

2

(2) I_{RRM} - peak reverse recovery current

(3) $t_{\rm rr}$ - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

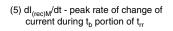


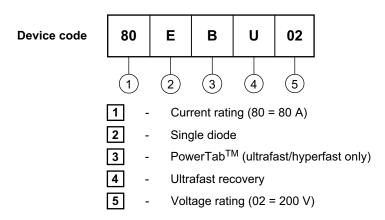
Fig. 10 - Reverse Recovery Waveform and Definitions

80EBU02

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95240				
Part marking information	www.vishay.com/doc?95370			
Application note	www.vishay.com/doc?95179			



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