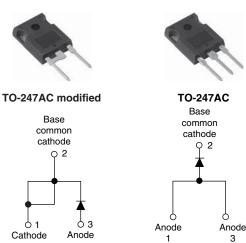


Vishay Semiconductors

Ultrafast Soft Recovery Diode, 60 A FRED Pt®



VS-60EPU02PbF

VS-60APU02PbF

PRODUCT SUMMARY					
Package	TO-247AC,				
	TO-247AC modified (2 pins)				
I _{F(AV)}	60 A				
V _R	200 V				
V _F at I _F	1.08 V				
t _{rr} typ.	See Recovery table				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Output rectification
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION/APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		200	V	
Continuous forward current	I _{F(AV)}	T _C = 127 °C	60		
Single pulse forward current	I _{FSM}	T _C = 25 °C	800	А	
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	120		
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP.			MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	N.		
Forward voltage V _F		I _F = 60 A	-	0.98	1.08	V		
		I _F = 60 A, T _J = 175 °C	-	0.81	0.88			
Povoroo lookogo ourront	1	$V_R = V_R$ rated	-	-	50	μA		
Reverse leakage current I _R		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	2	mA		
Junction capacitance	CT	V _R = 200 V	-	87	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH		

Document Number: 94021 Revision: 17-Feb-11 For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com





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DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	- 35		
Reverse recovery time	t _{rr}	$T_J = 25 \ ^\circ C$		-	28	-	ns
	T _J = 125 °C		-	50	-		
Pools receivers ourrent	eak recovery current I _{RRM}	T _J = 25 °C	$I_F = 60 \text{ A}$	-	4	-	А
Peak recovery current I _{RRM}		T _J = 125 °C	dl _F /dt = 200 A/µs V _B = 160 V	-	8	-	A
Reverse recovery charge Q _{rr}	0	T _J = 25 °C	n	-	59	-	-0
	T _J = 125 °C		-	220	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	R _{thJC}		-	-	0.70	K/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	r√vv	
Weight			-	5.5	-	g	
Weight			-	0.2	-	oz.	
Mounting torque			-	-	1.2	N ⋅ m	
		Case style TO-247AC modified		60EPU02			
Marking device		Case style TO-247AC		60APU02			

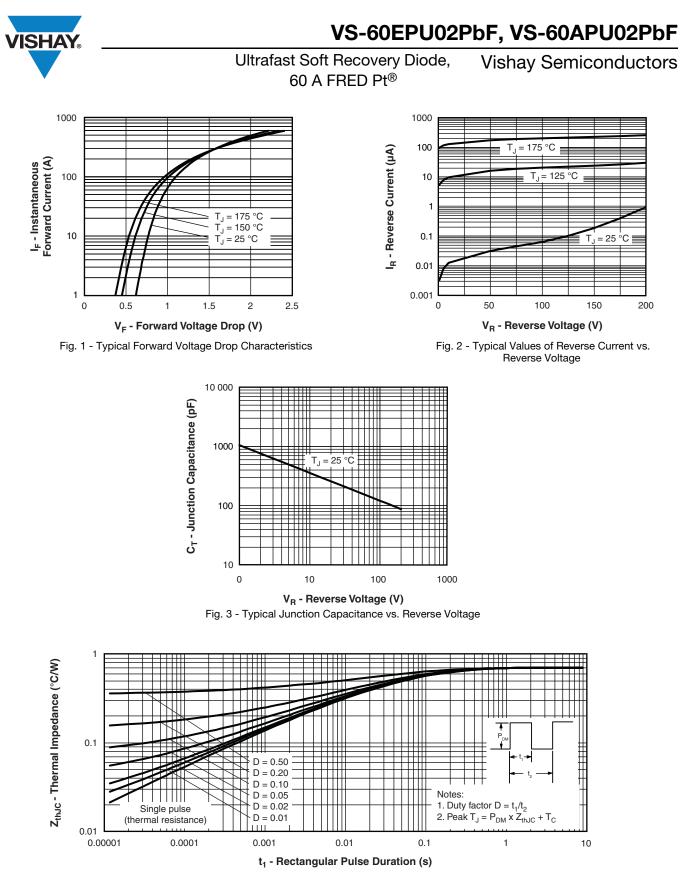


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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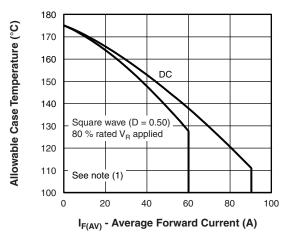
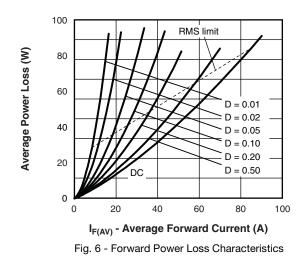


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

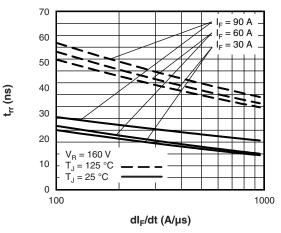
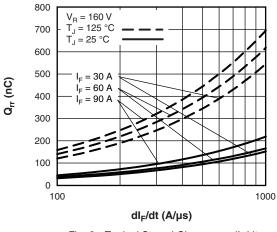
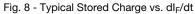


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt







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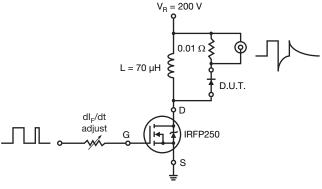


Fig. 9 - Reverse Recovery Parameter Test Circuit

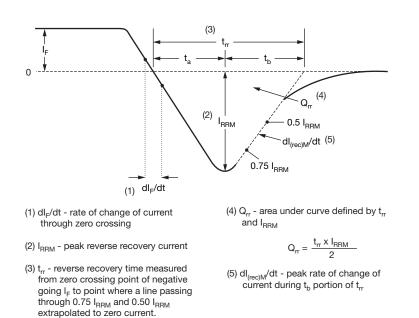


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	60	Е	Р	U	02	PbF
		2	3	4	5	6	7
	1 · 2 ·			niconduo ng (60 =		oduct	
	3		cuit conf = Singl	ïguratioi e diode	n:		
			•	e diode,	3 pins		
	4	- Pac	kage:		·		
		P =	TO-247	7AC (mo	odified)		
	5	- Тур	e of silio	con:			
		U =	Ultrafa	st recov	ery		
	6	- Vol	tage rati	ing (02 =	= 200 V))	
	7	- PbF	= Lead	d (Pb)-fre	ee		

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AC modified	www.vishay.com/doc?95253			
Dimensions	TO-247AC	www.vishay.com/doc?95223			
Part marking information	TO-247AC modified	www.vishay.com/doc?95255			
Part marking information	TO-247AC	www.vishay.com/doc?95226			
SPICE model		www.vishay.com/doc?95416			



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