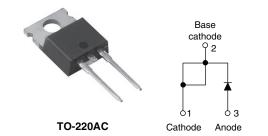


Vishay Semiconductors

Input Rectifier Diode, 10 A



PRODUCT SUMMARY				
V _F at 10 A	< 1.1 V			
I _{FSM}	200 A			
V _{RRM}	800 V/1200 V			

FEATURES

- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

APPLICATIONS

Input rectification



COMPLIANT · Vishay Semiconductors switches and output rectifiers

DESCRIPTION

The 10ETS..PbF rectifier series has been optimized for very low forward voltage drop, with moderate leakage.

which are available in identical package outlines

The glass passivation technology used has reliable operation up to 150 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS			
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$ common heatsink of 1 °C/W	12.0	16.0	A			

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL CHARACTERISTICS		VALUES	UNITS		
I _{F(AV)}	Sinusoidal waveform	10	А		
V _{RRM}		800/1200	V		
I _{FSM}		200	А		
V _F	10 A, T _J = 25 °C	1.1	V		
TJ		- 40 to 150	°C		

VOLTAGE RATINGS				
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA	
10ETS08PbF	800	900	0.5	
10ETS12PbF	1200	1300	0.5	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current I _{F(AV)}		T_{C} = 105 °C, 180° conduction half sine wave	10		
Maximum peak one cycle non-repetitive surge current	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	170	A	
		10 ms sine pulse, no voltage reapplied	200		
	l ² t	10 ms sine pulse, rated V _{RRM} applied	130	A20	
Maximum I ² t for fusing		10 ms sine pulse, no voltage reapplied	145	A ² s	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1450	A²√s	

* Pb containing terminations are not RoHS compliant, exemptions may apply

Document Number: 94337 For technical questions within your region, please contact one of the following: Revision: 22-Jul-10 DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C		1.1	V
Forward slope resistance	r _t	T.I = 150 °C		20	mΩ
Threshold voltage	V _{F(TO)}	1j = 150 C		0.82	V
Maximum reverse leakage current		T _J = 25 °C		0.05	m۸
Waximum reverse leakage current	IRM	T _J = 150 °C	$V_R = Rated V_{RRM}$	0.50	mA

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		62	°C/W
Soldering temperature	Ts		240	°C
Approximate weight			2	g
			0.07	oz.
Marking device		Constantia TO 2004 C	10ETS08	
		Case style TO-220AC	10ET	S12

Note

 $^{(1)}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W

For recommended footprint and soldering techniques refer to application note #AN-994



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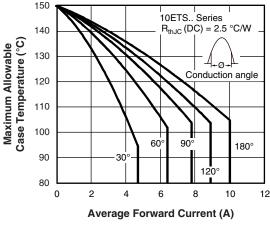


Fig. 1 - Current Rating Characteristics

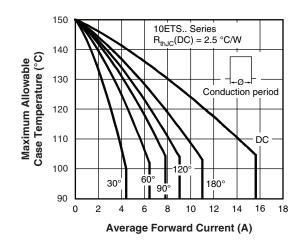


Fig. 2 - Current Rating Characteristics

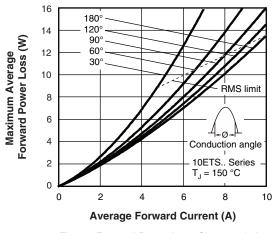


Fig. 3 - Forward Power Loss Characteristics

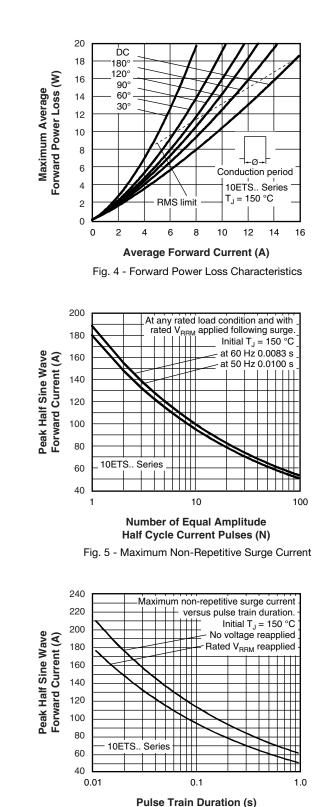


Fig. 6 - Maximum Non-Repetitve Surge Current

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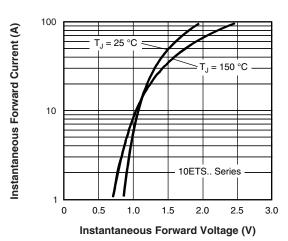


Fig. 7 - Forward Voltage Drop Characteristics

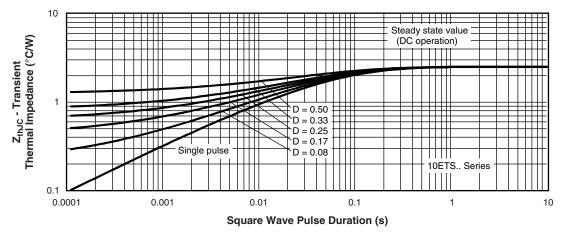


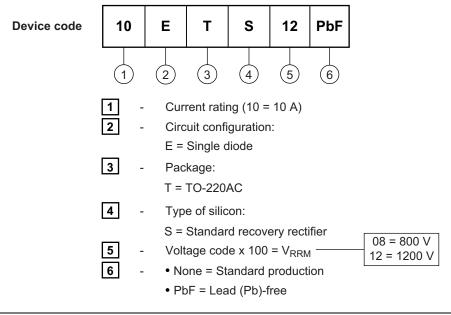
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95221	
Part marking information	www.vishay.com/doc?95224	



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