

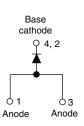
Vishay Semiconductors

Schottky Rectifier, 3.0 A



Diode variation

 E_{AS}



Single die

8 mJ

PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	3.0 A			
V _R	20 V, 30 V, 40 V			
V _F at I _F	0.49 V			
I _{RM}	20 mA at 125 °C			
T _J max.	150 °C			

FEATURES

- Popular D-PAK outline
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

DESCRIPTION

The VS-MBRD320PbF, VS-MBRD330PbF, VS-MBRD340PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.0	A		
V _{RRM}		20 to 40	V		
I _{FSM}	t _p = 5 μs sine	490	A		
V _F	3 Apk, T _J = 125 °C	0.49	V		
TJ		- 40 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRD320PbF	VS-MBRD330PbF	VS-MBRD340PbF	UNITS
Maximum DC reverse voltage	V _R	20	30	40	V
Maximum working peak reverse voltage	V _{RWM}	20		40	v

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 133 °C, rectangular waveform		3.0	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	490	А
	IFSM	10 ms sine or 6 ms rect. pulse		75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 16 mH		8.0	mJ
Repetitive avalanche current	I _{AR}			А	

Document Number: 94313 Revision: 14-Jan-11 For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop) <i>(</i> (1)	3 A	 T_J = 25 °C T_J = 125 °C 	0.48	0.6	V
		6 A		0.58	0.7	
See fig. 1	V _{FM} ⁽¹⁾	3 A		0.41	0.49	
		6 A		0.55	0.625	
Maximum reverse leakage current	U DM (1)	T _J = 25 °C	V_{R} = Rated V_{R}	0.02	0.2	mA
See fig. 2		T _J = 125 °C		10.7	20	ШA
Typical junction capacitance	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		189	-	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

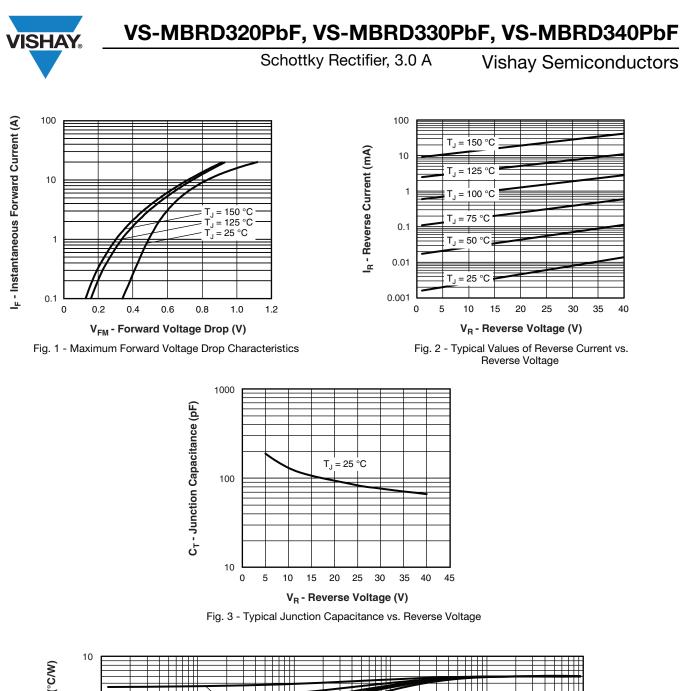
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		- 40 to 150	О°
Maximum storage temperature range	T _{Stg}		- 40 to 175	C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	6.0	°C 444
Maximum thermal resistance, junction to ambient	R _{thJA}		80	°C/W
Approvimate weight			0.3	g
Approximate weight			0.01	oz.
			MBR	D320
Marking device		Case style D-PAK (similar to TO-252AA)	MBRD330	
			MBR	D340

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



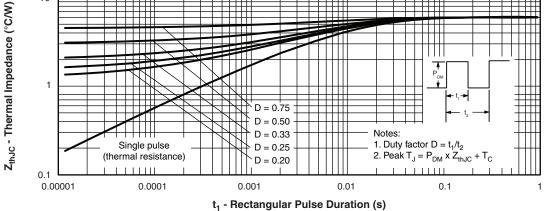
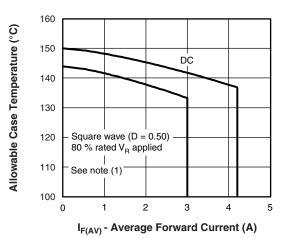


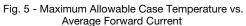
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

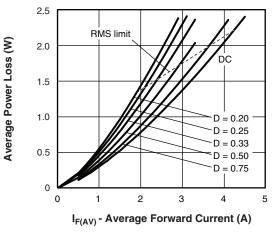


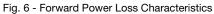
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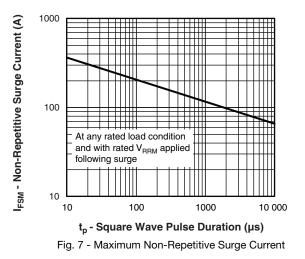
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Note

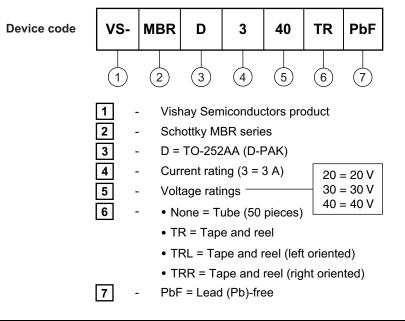
- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95016				
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			



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