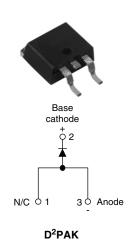


### Vishay High Power Products

# HEXFRED® Ultrafast Soft Recovery Diode, 8 A



PRODUCT SUMMARY					
V <sub>R</sub>	600 V				
V <sub>F</sub> at 8 A at 25 °C	1.7 V				
I <sub>F(AV)</sub>	8 A				
t <sub>rr</sub> (typical)	18 ns				
T <sub>J</sub> (maximum)	150 °C				
Q <sub>rr</sub> (typical)	65 nC				
dl <sub>(rec)M</sub> /dt (typical)	240 A/μs				
I <sub>RRM</sub>	5.0 A				

#### **FEATURES**

- Ultrafast recovery
- · Ultrasoft recovery
- Very low I<sub>RRM</sub>
- Very low Q<sub>rr</sub>
- Specified at operating conditions
- Compliant to RoHS directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition
- · AEC-Q101 qualified





RoHS\*

HALOGEN FREE

#### **BENEFITS**

- · Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- · Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

#### **DESCRIPTION**

HFA08TB60S is a state of the art ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 V and 8 A continuous current, the HFA08TB60S is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (IRRM) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA08TB60S is ideally suited for applications in power supplies (PFC boost diode) and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	$V_R$		600	V	
Maximum continuous forward current	I <sub>F</sub>	T <sub>C</sub> = 100 °C	8		
Single pulse forward current	I <sub>FSM</sub>		60	Α	
Maximum repetitive forward current	I <sub>FRM</sub>		24		
Maximum power dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	36	W	
Maximum power dissipation		T <sub>C</sub> = 100 °C	14	VV	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to + 150	°C	

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

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### HFA08TB60SPbF

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<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA		600	-	-	
		I <sub>F</sub> = 8.0 A		=	1.4	1.7	V
Maximum forward voltage V <sub>FM</sub>	I <sub>F</sub> = 16 A	See fig. 1	-	1.7	2.1		
		I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 125 °C		-	1.4	1.7	
Maximum reverse		$V_R = V_R$ rated	Soo fig. 0	-	0.3	5.0	
leakage current	I <sub>RM</sub>	$T_J = 125  ^{\circ}\text{C},  V_R = 0.8  \text{x}  V_R  \text{rated}$	See fig. 2	-	100	500	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	See fig. 3	=	10	25	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		=	8.0	-	nH

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	18	-	
Reverse recovery time See fig. 5, 6	t <sub>rr1</sub>	T <sub>J</sub> = 25 °C		-	37	55	ns
000 lig. 0, 0	t <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	55	90	
Peak recovery current	I <sub>RRM1</sub>	T <sub>J</sub> = 25 °C		-	3.5	5.0	Α
reak recovery current	$I_{RRM2}$ $I_{J} = 125  ^{\circ}\text{C}$ $I_{F} = 8.0  \text{A}$	-	4.5	8.0	^		
Reverse recovery charge	Q <sub>rr1</sub>	T <sub>J</sub> = 25 °C	$dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	65	138	nC
See fig. 7	Q <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	124	360	110
Peak rate of fall of recovery current during $t_b$ See fig. 8	dI <sub>(rec)M</sub> /dt1	T <sub>J</sub> = 25 °C		-	240	-	- A/μs
	dI <sub>(rec)M</sub> /dt2	T <sub>J</sub> = 125 °C		-	210	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T <sub>lead</sub>	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	3.5	K/W
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	80	N/VV
Weight			-	2.0	-	g
vveigni			-	0.07	-	oz.
Marking device		Case style D <sup>2</sup> PAK		HFA08	TB60S	



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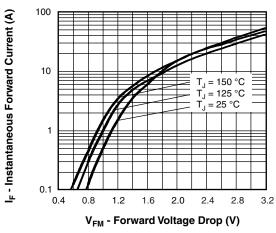


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

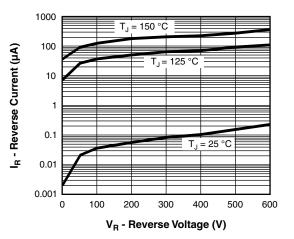


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

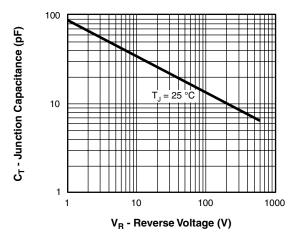


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

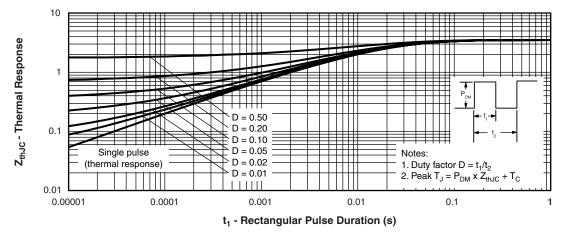


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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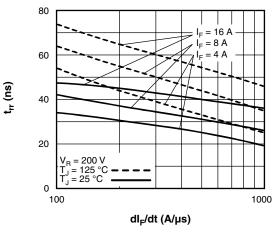


Fig. 5 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

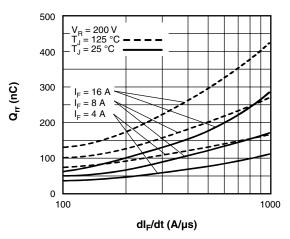


Fig. 7 - Typical Stored Charge vs. dI<sub>F</sub>/dt

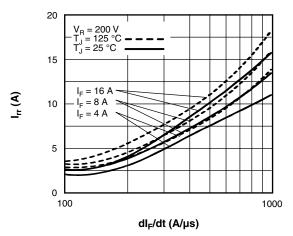


Fig. 6 - Typical Recovery Current vs. dl<sub>F</sub>/dt

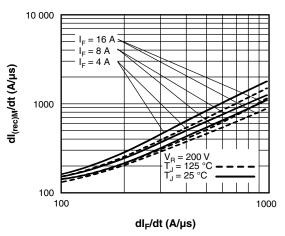


Fig. 8 - Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$ 



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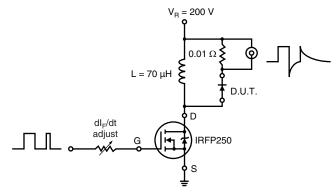
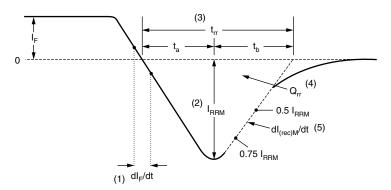


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$
- (2)  $I_{\text{RRM}}$  peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RBM</sub> and 0.50 I<sub>RBM</sub> extrapolated to zero current.
- (5)  $dI_{(rec)M}/dt$  peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 10 - Reverse Recovery Waveform and Definitions

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95046				
Part marking information	www.vishay.com/doc?95054				
Packaging information	www.vishay.com/doc?95032				

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