VISHAY.

VS-87CNQ020A PbF Series

Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

3

Anode

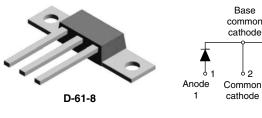
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Anode

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VS-87CNQ020APbF



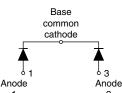
VS-87CNQ020ASMPbF







D-61-8-SL



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Common

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Anode

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PRODUCT SUMMARY				
I _{F(AV)}	2 x 40 A			
V _R at 125 °C	20 V			
V _R at 150 °C	10 V			
I _{RM}	550 mA at 125 °C			

FEATURES

- 150 °C T_J operation
- Center tap module
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	80	A		
V _{RRM}		20	V		
I _{FSM}	t _p = 5 μs sine	6000	A		
V _F	40 Apk, T _J = 125 °C (per leg)	0.32	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VS-87CNQ020APbF	UNITS
Maximum DC reverse valtage	V _R	125 °C	20	V
Maximum DC reverse voltage		150 °C	10	

* Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	_ TEST CONDITIONS		VALUES	UNITS
Maximum average per leg					40	
forward current	per device	I _{F(AV)}	50 % duty cycle at T_C = 135 °C, rectangular waveform		80	
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	6000	A	
		10 ms sine or 6 ms rect. pulse		1100		
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.12 mH		36	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		8	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	40 A	T 05 %C	0.45	- V
		80 A	T _J = 25 °C	0.51	
Maximum forward valtage dwap new lag		40 A	T 105 %C	0.32	
Maximum forward voltage drop per leg		80 A	T _J = 125 °C	0.39	
		40 A	T 150 %	0.29	
		80 A	T _J = 150 °C	0.37	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = 5 V	90	
			V _R = 3.3 V	70	
		T _J = 150 °C	V _R = 10 V	480	mA
		T _J = 25 °C		5.5	
		T _J = 125 °C	V _R = Rated V _R	550	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.191	V
Forward slope resistance	r _t			2.3	mΩ
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		6500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.5		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

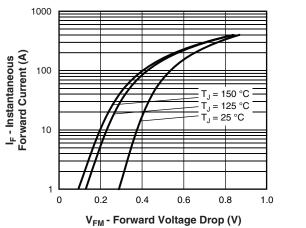
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storag	ge	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal	per leg	в	DC operation	0.85	°C/W
resistance, junction to case	per package	R _{thJC}	DC operation	0.42	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	0/11
Approvimente weight				7.8	g
Approximate weight				0.28	oz.
minimu				40 (35)	kgf · cm
Mounting torque	maximum			58 (50)	(lbf · in)
Marking device			Case style D-61	87CN0	2020A
			Case style D-61-8-SM	87CNQ0	20ASM
			Case style D-61-8-SL	87CNQ	020ASL

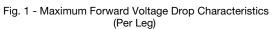


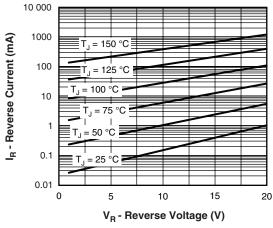
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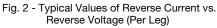
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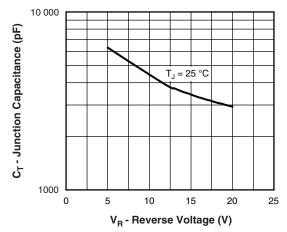


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

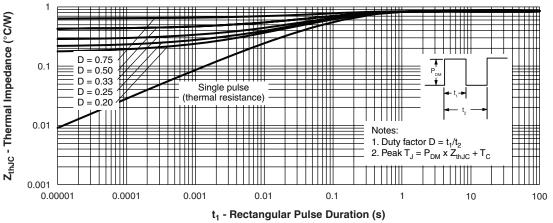
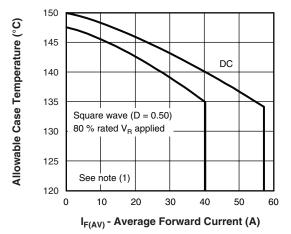


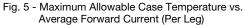
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

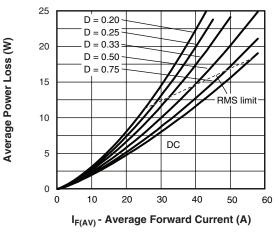
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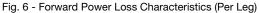
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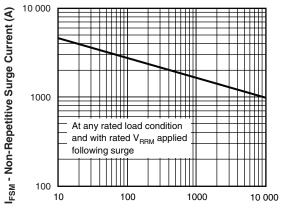
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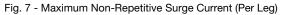








tp - Square Wave Pulse Duration (µs)



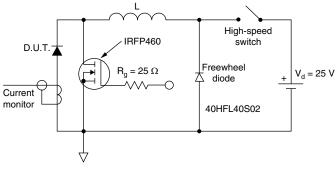


Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{CAVA} \times V_{EVA}$ at (I_{CAVA}/D) (see f
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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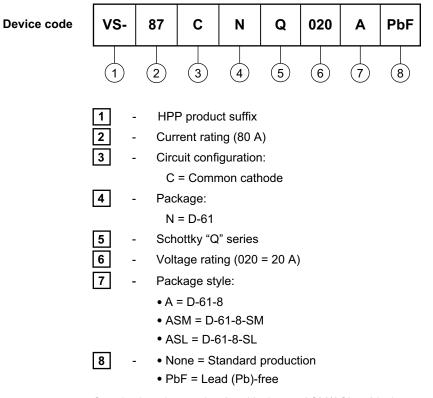


Schottky Rectifier

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ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95354						
Part marking information	www.vishay.com/doc?95356					



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