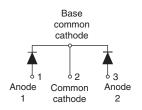


Vishay Semiconductors

# **Schottky Rectifier**

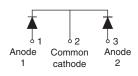
# New Generation 3 D-61 Package, 2 x 40 A





VS-80CNQ...ASM



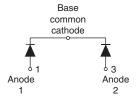


D-61-8-SM

VS-80CNQ...ASL







PRODUCT SUMMARY				
I <sub>F(AV)</sub>	2 x 40 A			
$V_{R}$	35 V to 45 V			

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap module
- · Very low forward voltage drop
- High frequency operation
- · High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- · New fully transfer-mold low profile, small footprint, high current package
- · Designed and qualified for industrial level

#### **DESCRIPTION**

The center tap Schottky rectifier module series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	80	A		
V <sub>RRM</sub>	Range	35 to 45	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	5800	Α		
V <sub>F</sub>	40 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.47	V		
T <sub>J</sub>	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-80CNQ035A	VS-80CNQ040A	VS-80CNQ045A	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	35	40	45	V
Maximum working peak reverse voltage	$V_{RWM}$	33	40	45	V



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ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per leg				40	
forward current See fig. 5	per device	$I_{F(AV)}$ 50 % duty cycle at $T_C$ = 114 °C, rectangular waveform		80	٠	
Maximum peak one cycle non-repetitive surge current per leg			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	5800	А
See fig. 7	t per leg	IFSM	10 ms sine or 6 ms rect. pulse		750	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	$T_J = 25$ °C, $I_{AS} = 8$ A, $L = 1.7$ mH		54	mJ
Repetitive avalanche current per leg I <sub>AR</sub>		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \text{ x } V_R$ typical		8	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	T <sub>J</sub> = 25 °C	0.52	. V
		80 A		0.66	
See fig. 1		40 A	- T <sub>J</sub> = 125 °C	0.47	
		80 A		0.61	
Maximum reverse leakage current per leg		T <sub>J</sub> = 25 °C	V Detect V	5	A
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	250	mA mA
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = T <sub>J</sub> maximum		0.26	V
Forward slope resistance	r <sub>t</sub>			3.93	mΩ
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C 2600		pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.5 nh		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µ:		V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,\,duty\,cycle < 2~\%$ 



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PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		$R_{thJC}$	DC operation See fig. 4	0.85		
			DC operation	0.42 °C/W		
Typical thermal resistance, case to heatsink (D-61-8 only)		R <sub>thCS</sub>	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approximate weight				7.8	g	
				0.28	OZ.	
Mounting torque (D-61-8 only)	minimum			40 (35)	kgf · cm	
	maximum			58 (50)	(lbf · in)	
Marking device				80CNQ035A		
			Case style D-61-8	80CNQ040A		
			80CNQ045A			
			Case style D-61-8-SM	80CNQ035ASM		
				80CNQ040ASM		
				80CNQ045ASM		
			Case style D-61-8-SL	80CNQ035ASL		
				80CNQ040ASL		
				80CNQ045ASL		



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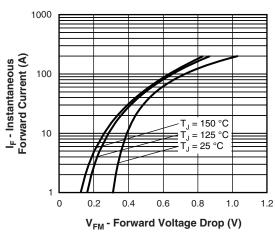


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

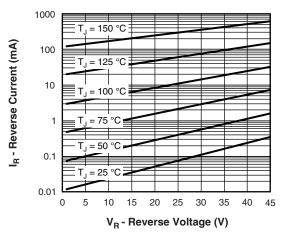


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

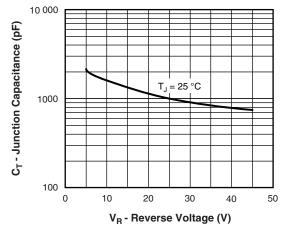


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

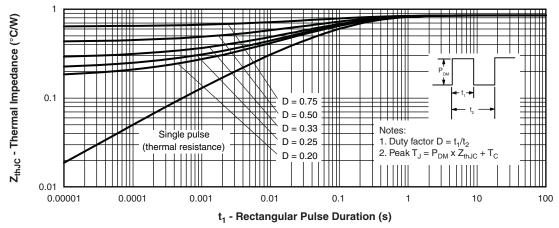


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics (Per Leg)



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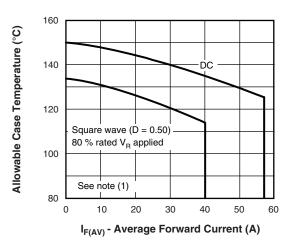


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

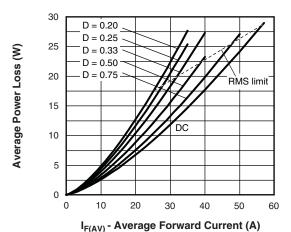


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

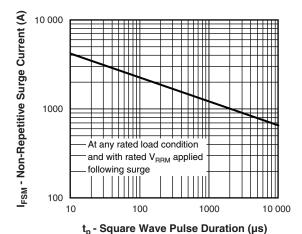


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

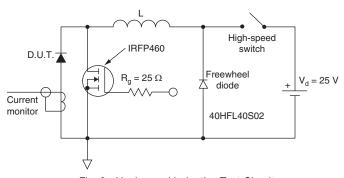


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

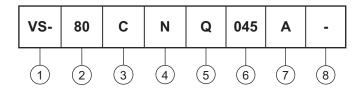


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#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (80 = 80 A)

Circuit configuration:

C = Common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

035 = 35 V 040 = 40 V

6 - Voltage ratings

045 = 45 V

Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = Standard production

• PbF = Lead (Pb)-free (D-61-8 only)

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95354</u>					
Part marking information	www.vishay.com/doc?95356				

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Document Number: 91000 www.vishay.com
Revision: 11-Mar-11 1