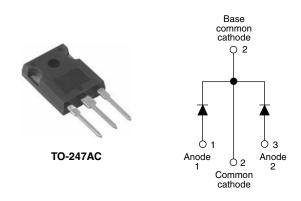
Vishay Semiconductors

Ultrafast Rectifier, 2 x 15 A FRED Pt®



PRODUCT SUMMARY				
Package	TO-247AC			
I _{F(AV)}	2 x 15 A			
V _R	400 V			
V _F at I _F	1.25 V			
t _{rr} typ.	See Recovery table			
T _J max.	175 °C			
Diode variation	Common cathode			

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

FRED Pt[®] series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		V _{RRM}		400	V	
Average restified forward ourrest	per leg	F(A)A		15		
Average rectified forward current	tal device		Rated V _R , T _C = 149 °C	30	۸	
Non-repetitive peak surge current per leg		I _{FSM}	T _C = 25 °C	200	A	
Peak repetitive forward current per leg		I _{FRM}	Rated V_R , T_C = 149 °C, square wave, 20 kHz	30		
Operating junction and storage temperatures		T_J,T_Stg		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	400	-	-		
	I _F = 15 A	-	1.17	1.25	V		
Forward voltage V _F		I _F = 15 A, T _J = 150 °C	-	0.93	1.12		
Reverse leakage current I _R		$V_R = V_R$ rated	-	0.3	10		
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	30	500	μΑ	
Junction capacitance	CT	V _R = 400 V	-	28	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	12	-	nH	

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For technical questions within your region, please contact one of the following: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u>

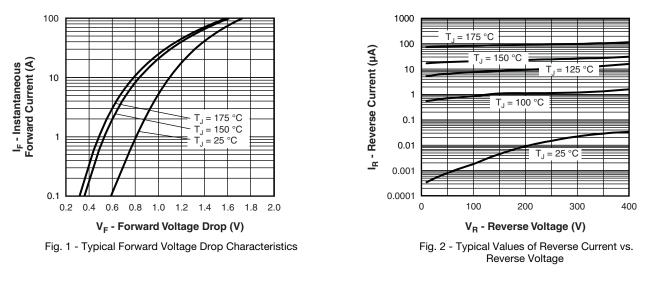


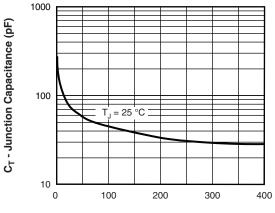
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	36	60	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	46	-	ns
	T _J = 125 °C	I _F = 15 A dI _F /dt = 200 A/μs V _B = 200 V	-	80	-		
Peak recovery current I _{RRM}	T _J = 25 °C		-	3.6	-	- A	
	T _J = 125 °C		-	8.7	-		
Reverse recovery charge Q _{rr}	0	T _J = 25 °C	n	-	84	-	nC
	T _J = 125 °C		-	345	-	iic	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	0.8	1.5	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-	
Waight			-	6.0	-	g
Weight			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC		30C	PU04	

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V_R - Reverse Voltage (V) Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

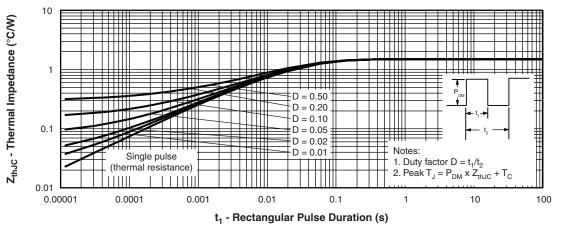


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

VS-30CPU04PbF

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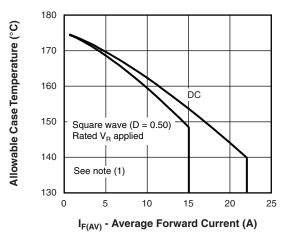
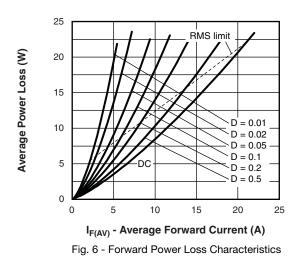


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

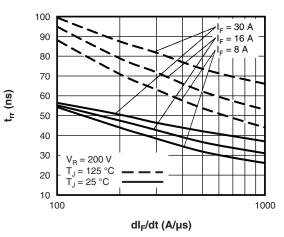


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

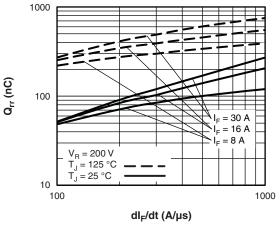


Fig. 8 - Typical Stored Charge vs. dl_F/dt



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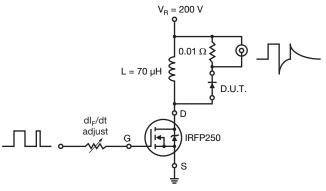


Fig. 9 - Reverse Recovery Parameter Test Circuit

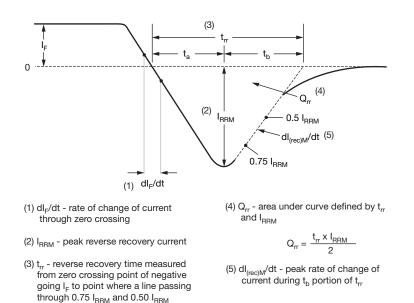


Fig. 10 - Reverse Recovery Waveform and Definitions

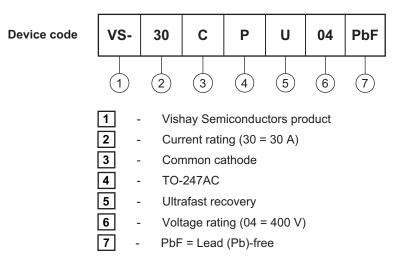
extrapolated to zero current.

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ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95223					
Part marking information	www.vishay.com/doc?95226				



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