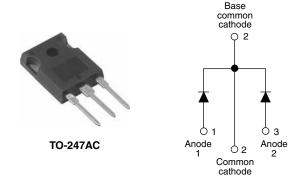


Vishay Semiconductors

# Ultrafast Rectifier, 2 x 15 A FRED Pt®



PRODUCT SUMMARY					
Package	TO-247AC				
I <sub>F(AV)</sub>	2 x 15 A				
V <sub>R</sub>	200 V				
V <sub>F</sub> at I <sub>F</sub>	1.05 V				
t <sub>rr</sub> typ.	See Recovery table				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				

#### **FEATURES**

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level



#### **DESCRIPTION/APPLICATIONS**

VS-MUR3020WTPbF is the state of the art ultrafast recovery rectifier specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage		$V_{RRM}$		200	V	
Average rectified forward current	per leg	I <sub>F(AV)</sub>		15		
Average rectified forward current total c	al device		Rated V <sub>R</sub> , T <sub>C</sub> = 150 °C	30		
Non-repetitive peak surge current per leg		I <sub>FSM</sub>		200	Α	
Peak repetitive forward current per leg		I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 150 °C	30		
Operating junction and storage temperatures		T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-	.,	
Forward voltage V <sub>F</sub>		I <sub>F</sub> = 15 A	-	-	1.05	V	
r of ward voltage v <sub>F</sub>	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	-	0.85			
Reverse leakage current I <sub>R</sub>		$V_R = V_R$ rated	-	-	10		
		$T_J = 150 ^{\circ}\text{C},  V_R = V_R \text{ rated}$	-	-	500	μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	55	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	12	-	nH	

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# VS-MUR3020WTPbF

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	22	-	ns
	T <sub>J</sub> = 125 °C		-	39	-		
Peak recovery current I <sub>RRM</sub>	,	T <sub>J</sub> = 25 °C	125 °C	-	1.6	-	А
	IRRM	T <sub>J</sub> = 125 °C		-	4.1	-	
Reverse recovery charge Q <sub>r</sub>	0	T <sub>J</sub> = 25 °C		-	19	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	90	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per leg	$R_{\text{thJC}}$		-	-	1.5	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Maight			-	6.0	-	g
Weight			-	0.21	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC		MUR3	020WT	•



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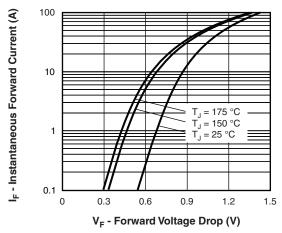


Fig. 1 - Typical Forward Voltage Drop Characteristics

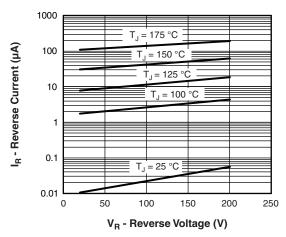


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

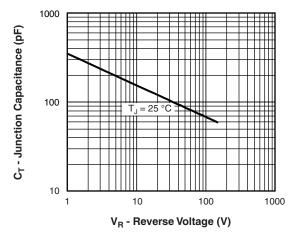


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

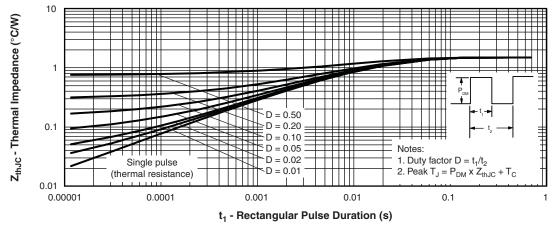


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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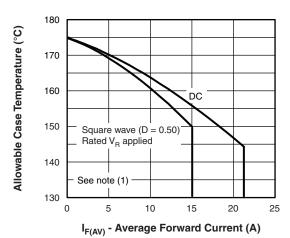


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

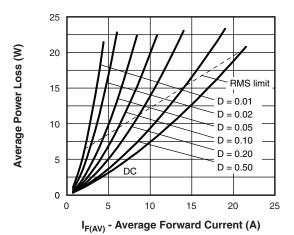


Fig. 6 - Forward Power Loss Characteristics

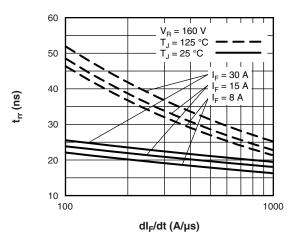


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

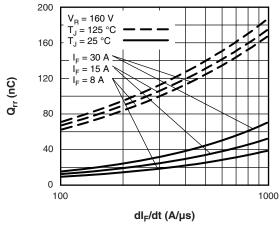


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 



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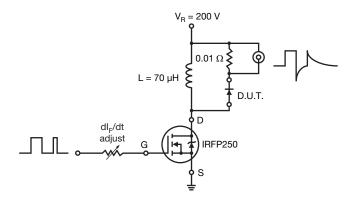
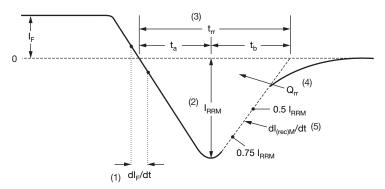


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $t_{\rm rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{\rm F}$  to point where a line passing through 0.75  $I_{\rm RRM}$  and 0.50  $I_{\rm RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{\text{RRM}}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

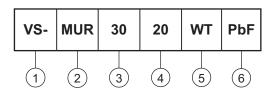
### VS-MUR3020WTPbF

# Vishay Semiconductors Ultrafast Rectifier, 2 x 15 A FRED Pt®



#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Ultrafast MUR series (TO-247AC)

**3** - Current rating (30 = 30 A)

4 - Voltage rating (20 = 200 V)

5 - WT = Center tap (dual) TO-247

6 - PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
Part marking information	www.vishay.com/doc?95226				

### **Legal Disclaimer Notice**



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