



Leaded Varistors

AdvanceD-MP series

Series/Type:	SIOV-S14K***E2K1
Ordering code:	B72214P2***K101
Date:	2009-07-16
Version:	b

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Applications

Overtoltage protection

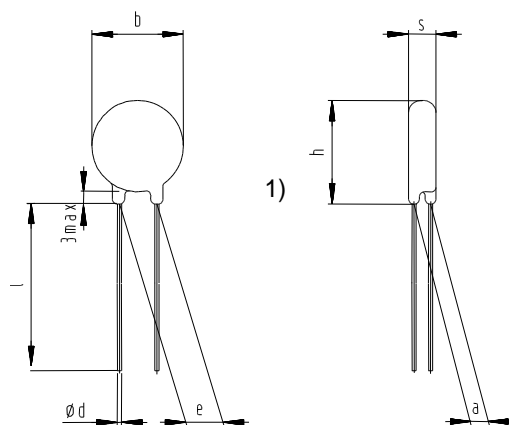
Features

- UL approval to UL1449 (file number E321126), for use in Type 2 SPD's.
- Wide operating voltage range 130 ... 680 V_{RMS}
- Ideally suited for AC applications where low level repetitive surges are expected

SIOV nomenclature

S	=	Disk type
14	=	Rated disk diameter
K	=	Tolerance of V _V at 1mA : ±10%
***	=	Max. AC voltage
E2K1	=	AdvanceD-MP series

Dimensional drawings in mm



b _{max}	=	See Table 1
h _{max}	=	See Table 1
s _{max}	=	See Table 1
e	=	7.5 ±1.0
a	=	See Table 1
l _{min}	=	25.0
Ød	=	0.8 ±0.05

1) seating plane in accordance with IEC 60717

Table 1

Type SIOV-	Ordering Code	b_{\max} [mm]	h_{\max} [mm]	s_{\max} [mm]	$a \pm 1.0$ [mm]
S14K130E2K1	B72214P2131K101	16.0	20.0	4.7	2.0
S14K140E2K1	B72214P2141K101	16.0	20.0	4.8	2.1
S14K150E2K1	B72214P2151K101	16.0	20.0	4.9	2.2
S14K175E2K1	B72214P2171K101	16.0	20.0	5.1	2.4
S14K210E2K1	B72214P2211K101	16.0	20.0	5.4	2.7
S14K230E2K1	B72214P2231K101	16.0	20.0	5.6	2.9
S14K250E2K1	B72214P2251K101	16.0	20.0	5.7	3.0
S14K275E2K1	B72214P2271K101	16.0	20.0	5.9	3.2
S14K300E2K1	B72214P2301K101	16.0	20.0	6.1	3.5
S14K320E2K1	B72214P2321K101	16.0	20.0	6.3	3.7
S14K350E2K1	B72214P2351K101	16.5	20.5	6.7	4.1
S14K385E2K1	B72214P2381K101	16.5	20.5	7.7	4.4
S14K420E2K1	B72214P2421K101	16.5	20.5	8.2	4.7
S14K460E2K1	B72214P2461K101	16.5	20.5	8.5	5.1
S14K510E2K1	B72214P2511K101	17.0	21.0	8.9	5.4
S14K550E2K1	B72214P2551K101	17.0	21.0	9.4	5.9
S14K625E2K1	B72214P2621K101	17.0	21.0	9.9	6.4
S14K680E2K1	B72214P2681K101	17.0	21.0	10.5	7.0

Electrical data

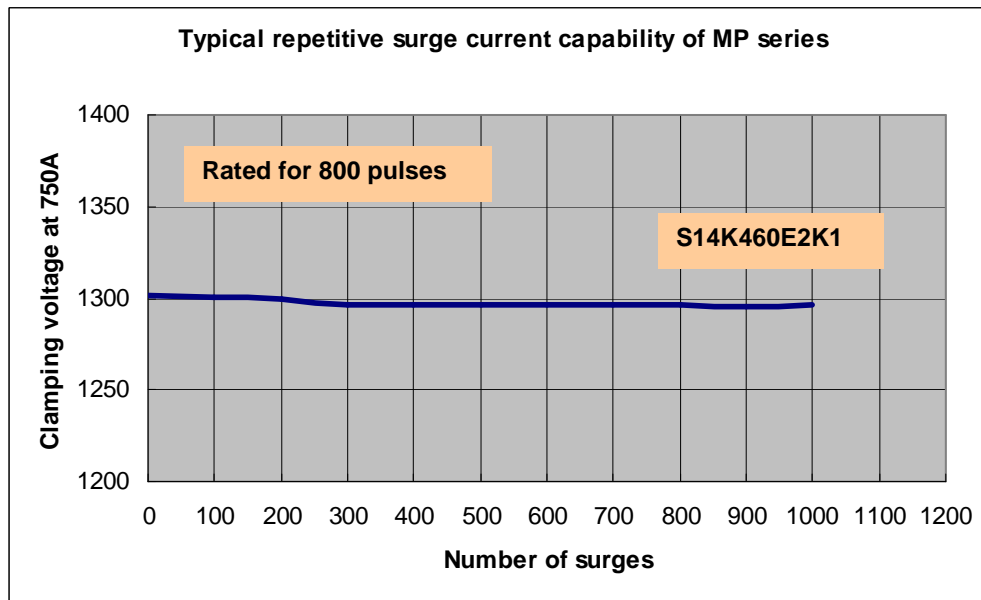
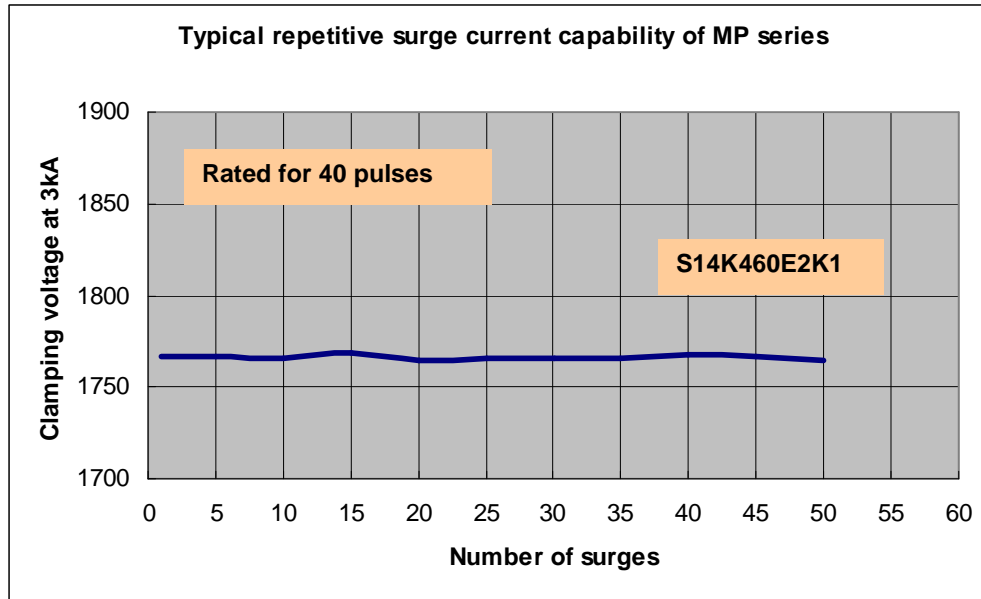
Maximum Ratings (85 °C)

Type SIOV- S14K	V _{RMS} [V]	V _{DC} [V]	I _{max} (8/20 μs) 1 time [A]*	W _{max} (2 ms) 1 time [J]	P _{max} [W]
130E2K1	130	170	6000	60	0.6
140E2K1	140	180	6000	65	0.6
150E2K1	150	200	6000	70	0.6
175E2K1	175	225	6000	80	0.6
210E2K1	210	270	6000	95	0.6
230E2K1	230	300	6000	105	0.6
250E2K1	250	320	6000	115	0.6
275E2K1	275	350	6000	130	0.6
300E2K1	300	385	6000	140	0.6
320E2K1	320	420	6000	150	0.6
350E2K1	350	460	6000	165	0.6
385E2K1	385	505	6000	180	0.6
420E2K1	420	560	6000	190	0.6
460E2K1	460	615	6000	200	0.6
510E2K1	510	670	6000	200	0.6
550E2K1	550	745	6000	220	0.6
625E2K1	625	825	6000	240	0.6
680E2K1	680	895	6000	260	0.6

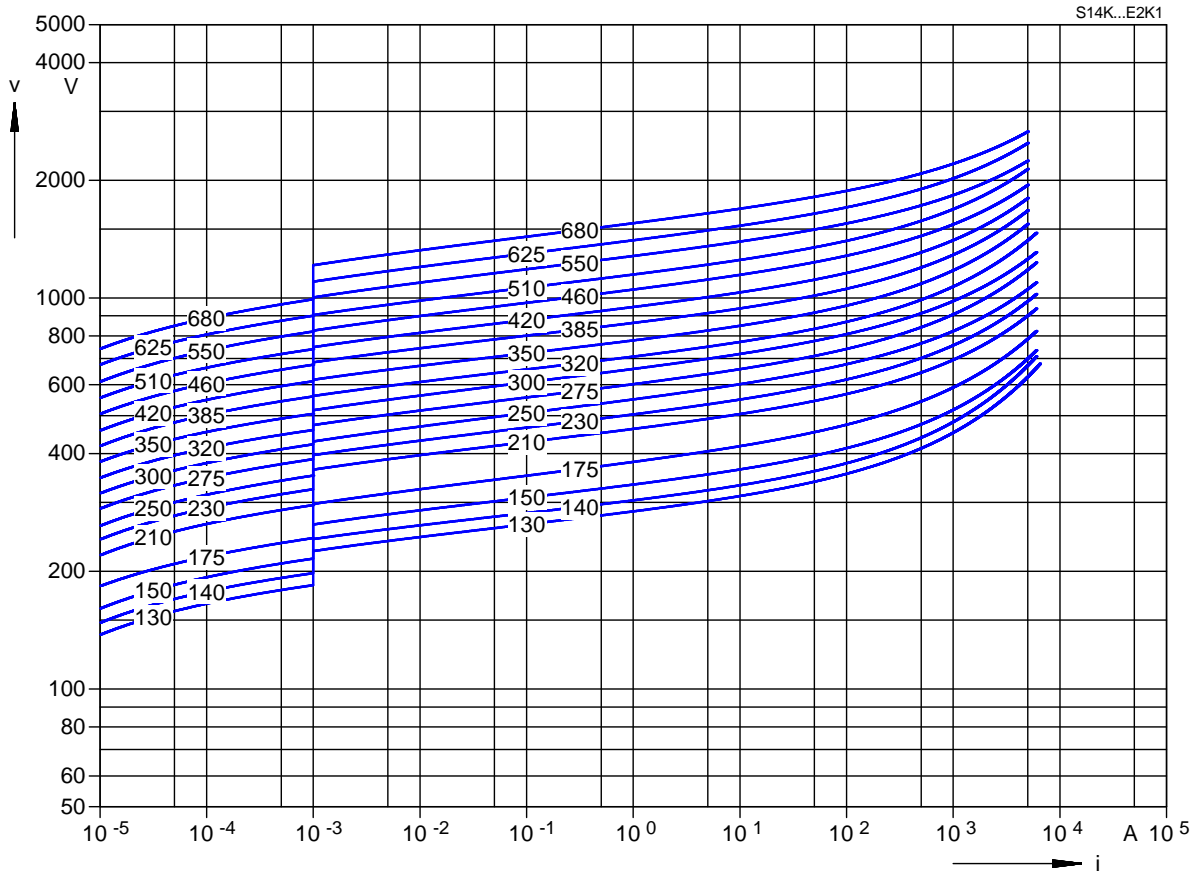
Characteristics (25 °C)

Type SIOV- S14K	V _v (1 mA) [V]	ΔV _v (1 mA) [%]	Max Clamping Voltage		C _{typ} (1 kHz) [pF]	Duty Cycle Surge Rating (8/20 μs)	
			V _c [V]	I _c [A]		3 kA* times	750 A* times
130E2K1	205	±10	340	50	760	40	800
140E2K1	220	±10	360	50	715	40	800
150E2K1	240	±10	395	50	670	40	800
175E2K1	270	±10	455	50	575	40	800
210E2K1	330	±10	545	50	375	40	800
230E2K1	360	±10	595	50	340	40	800
250E2K1	390	±10	650	50	320	40	800
275E2K1	430	±10	710	50	290	40	800
300E2K1	470	±10	775	50	285	40	800
320E2K1	510	±10	840	50	280	40	800
350E2K1	560	±10	910	50	260	40	800
385E2K1	620	±10	1025	50	240	40	800
420E2K1	680	±10	1120	50	210	40	800
460E2K1	750	±10	1240	50	180	40	800
510E2K1	820	±10	1355	50	170	15	500
550E2K1	910	±10	1500	50	155	15	500
625E2K1	1000	±10	1650	50	140	15	500
680E2K1	1100	±10	1815	50	130	15	500

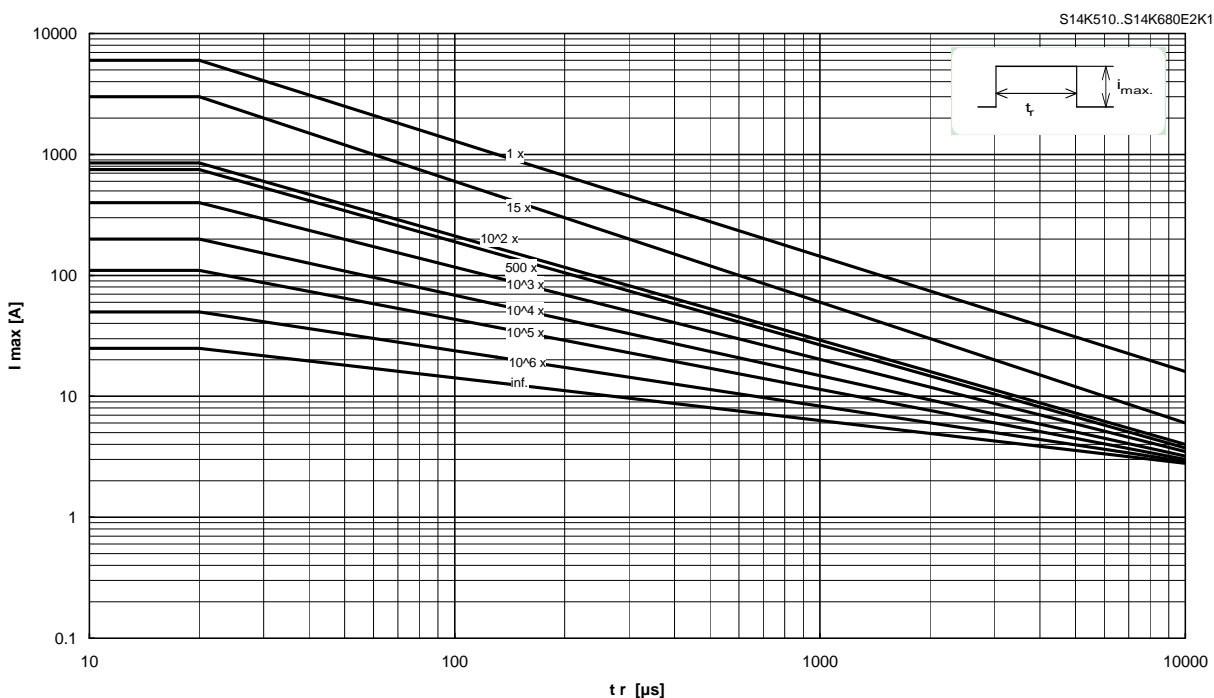
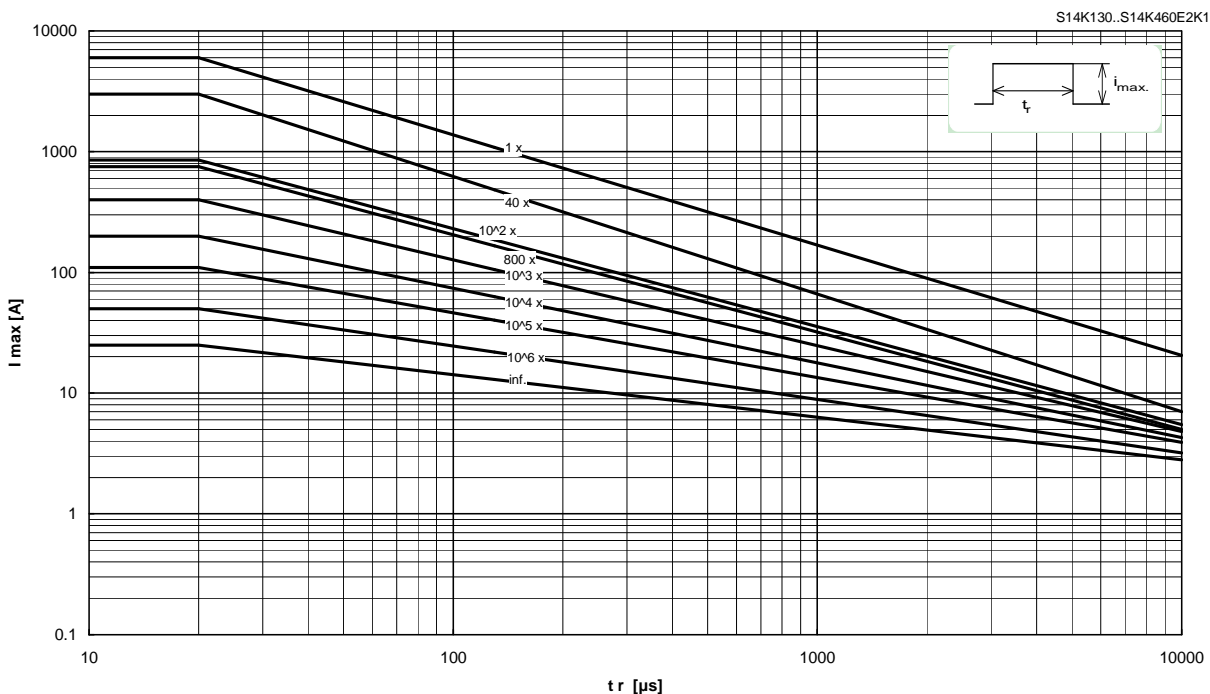
*The specified current value shows the actual 8/20μs peak current throughout the MOV, not the combination wave form.



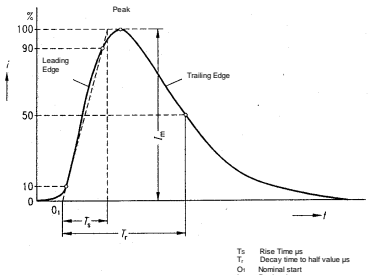
v/i Characteristic



Derating curves(the specified current value in derating curve is the actual peak current throughout the MOV)



Reliability Data Electrical

Characteristics	Test Methods/Description	Specifications
Varistor Voltage	The voltage between two terminals with the specified measuring current applied is called V_V (1 mA _{DC} @ 0.2 ... 2 s).	To meet the specified value.
Clamping Voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20µs) illustrated below applied. 	To meet the specified value.
Surge current derating, 8/20 µs	CECC 42 000, test C 2.1 100 surge currents (8/20 µs), unipolar, interval 30 s, amplitude corresponding to derating curve for 100 impulses at 20 µs	ΔV/V (1 mA) ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	CECC 42 000, test C 2.1 100 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 100 impulses at 2 ms	ΔV/V (1 mA) ≤10% (measured in direction of surge current) No visible damage

Reliability Data Mechanical

Characteristics	Test Methods/Description	Specifications
Tensile strength	IEC 60068-2-21, test Ua1 After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage. Force for wire diameter: 1.0 mm = 20 N	$ \Delta V/V (1 \text{ mA}) \leq 5\%$ No break of solder joint, no wire break
Vibration	IEC 60068-2, test Fc Frequency range: 10 55 Hz Amplitude: 0.75 mm or 98 m/s ² Duration: 6 h (3 x 2 h) Pulse: sine wave After repeatedly applying a single harmonic vibration according to the table above, the change of V_v shall be measured and the part shall be visually examined.	$ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage
Solderability	IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245°C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.

Characteristics	Test Methods/Description	Specifications
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s: Each lead shall be dipped into a solder bath having a temperature of 260 ±5 °C to a point 2.0 to 2.5 mm from the body of the unit, be held there for 10 ±1 s and then be stored at room temperature and normal humidity for 1 to 2 hours. The change of V_v shall be measured and the part shall be visually examined.	$ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage
Bump	IEC 60068-2-29, test Eb Pulse duration: 6 ms Max. acceleration: 400m/s ² Number of bumps: 4000 Pulse: half sine	$ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage
Flammability	IEC 60695-2-2 (needle flame test) Severity: vertical 10 s	5 s max.
Electric strength	CECC 42 000, test 4.7 Metal balls method, 2500 V _{RMS} , 60 s The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown

Reliability Data Environmental

Characteristics	Test Methods/Description	Specifications
Max. AC operating voltage	CECC 42 000, test 4.20 1000 h at UCT After having continuously applied the maximum allowable voltage at UCT ± 2 °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_v shall be measured.	$ \Delta V/V (1 \text{ mA}) \leq 10\%$
Damp heat, steady state	The specimen shall be subjected to 40 ± 2 °C, 90 to 95 % r.H. for 56 days without load / with 10% of the maximum continuous DC operating voltage V_{DC} . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_v shall be measured. Thereafter, insulation resistance R_{ins} shall be measured according to CECC 42 000, test 4.8 at $V = 500 \text{ V}$.	$ \Delta V/V (1 \text{ mA}) \leq 10\%$ $R_{ins} \geq 1 \text{ M}\Omega$
Climatic sequence	CECC 42 000, test 4.16 The specimen shall be subjected to: a) dry heat at UCT, 16 h b) damp heat, 1st cycle: 55 °C, 93% r.H., 24 h c) cold, LCT, 2 h d) damp heat, additional 5 cycles: 55 °C/25 °C, 93% r.H., 24 h/cycle. Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_v shall be measured. Thereafter, insulation resistance R_{ins} shall be measured according to CECC 42 000, test 4.8 at $V = 500 \text{ V}$.	$ \Delta V/V (1 \text{ mA}) \leq 10\%$ $R_{ins} \geq 1 \text{ M}\Omega$
Fast temperature cycling	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	$ \Delta V/V (1 \text{ mA}) \leq 5\%$ No visible damage

Note:

UCT = Upper category temperature

LCT = Lower category temperature

 R_{ins} = Insulation resistance to CECC 42 000, test 4.8

Cautions and warnings

General

1. EPCOS metal oxide varistors (SIOVs) are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
2. Ensure suitability of SIOVs through reliability testing during the design-in phase. The SIOVs should be evaluated taking into consideration worst-case conditions.
3. For applications of SIOVs in line-to ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

1. Store SIOVs only in original packaging. Do not open the package before storage.
2. Storage conditions in original packaging:

Storage temperature:	-25 °C ... +45 °C
Relative humidity:	<75% annual average, <95% on maximum 30 days a year.
Dew precipitation:	Is to be avoided.
3. Avoid contamination of SIOVs surface during storage, handling and processing.
4. Avoid storage of SIOVs in harmful environments which can affect the function during long-term operation (examples given under operation precautions).
5. The SIOV type series should be soldered within the time specified.

SIOV-S, -Q, -LS	24 month
ETFV and SFS types	12 month.

Handling

1. SIOVs must not be dropped.
2. Components must not be touched with bare hands. Gloves are recommended.
3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

1. Use rosin-type flux or non-activated flux.
2. Insufficient preheating may cause ceramic cracks.
3. Rapid cooling by dipping in solvent is not recommended.
4. Complete removal of flux is recommended.

Mounting

1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason the SIOVs should be physically shielded from adjacent components.

Operation

1. Use SIOVs only within the specified temperature operating range
2. Use SIOVs only within the specified voltage and current ranges.
3. Environmental conditions must not harm the SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in the presence of deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas, etc), corrosive agents, humid or salty conditions, Avoid contact with any liquids and solvents.

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