Transmissive Optoschmitt Sensor

FEATURES

- Direct TTL interface
- Buffer logic
- 0.060 in.(1.52 mm) dia. detector aperture
- 0.120 in.(3.05 mm) slot width
- 0.050 in.(1.27) offset pin circle detector eads



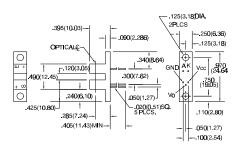
DESCRIPTION

The HOA2001 consists of an infrared emitting diode facing an Optoschmitt detector encased in a black thermoplastic housing. The photodetector consists of a photodiode, amplifier, voltage regulator, Schmitt trigger and an NPN output transistor with 10 $k\Omega$ (nominal) pullup resistor. The buffer logic provides a high output when the optical path is clear, and a low output when the path is interrupted. The HOA2001 employs plastic molded components. For additional component information see SEP8506 and SDP8600.

Housing material is polyester. Housings are soluble in chlorinated hydrocarbons and ketones. Recommended cleaning agents are methanol and isopropanol.

OUTLINE DIMENSIONS in inches (mm)

3 plc decimals ±0.010(0.25) ±0.020(0.51) 2 plc decimals



DIM 062.ds4

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ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	TEST CONDITIONS
IR EMITTER						
Forward Voltage	VF			1.6	V	I _E =20 mA
Reverse Leakage Current	I _R			10	μΑ	V _R =3 V
DETECTOR						
Operating Supply Voltage	Vcc	4.5		10	V	
Low Level Supply Current	IccL	4.0		12	mA	Vcc=5 V
Low Level Supply Current		5.0		15		Vcc=12 V
High Level Supply Current	Іссн	2.0		10	mA	Vcc=5 V
High Level Supply Current		3.0		12		Vcc=12 V
Low Level Output Voltage	Vol			0.4	V	IoL=12.8 mA, I _F =0 mA
High Level Output Voltage	Voн	2.4			V	I _{он=} 0, I _F =10 mA
Hysteresis (2)	HYST		10		%	
Propagation Delay, Low-High	tplH		5		μs	Vcc=5 V, I _F =10 mA
Propagation Delay, High-Low	t _{PHL}		5		μs	Vcc=5 V, I _F =10 mA
Rise Time	tr		60		ns	R _L =390 Ω , C _L =50 pF
Fall Time	tf		15		ns	R _L =390 Ω , C _L =50 pF
COUPLED CHARACTERISTICS						
IRED Trigger Current	IFT				mA	Vcc=5 V
HOA2001-001				10		

- Notes

 1. It is recommended that a bypass capacitor, 0.1 µF typical, be added between V_{CC} and GND near the device in order to stabilize
- power supply line.

 2. Hysteresis is defined as the difference between the operating and release threshold intensities, expressed as a percentage of the operate threshold intensity.

ABSOLUTE MAXIMUM RATINGS SCHEMATIC (25°C Free-Air Temperature unless otherwise noted) Q Vcc Operating Temperature Range -40°C to 70°C **≥10** kΩ Voltage -40°C to 85°C Storage Temperature Range regulator Soldering Temperature (5 sec) 240°C Anode IR EMITTER -O Vo Power Dissipation 100 mW (1) Reverse Voltage 3 V Continuous Forward Current 50 mA DETECTOR Supply Voltage 12 V (2) Output Sink Current 18 mA O GND Cathode **Duration of Output** Short to V_{CC} or Ground 1.0 sec.

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SWITCHING WAVEFORM

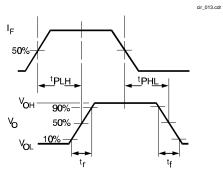
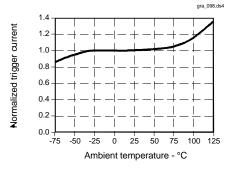
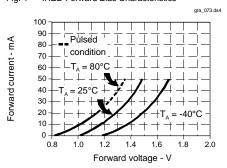


Fig. 2 IRED Trigger Current vs Temperature



All Performance Curves Show Typical Values

Fig. 1 IRED Forward Bias Characteristics



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