

4. TIMING CHARACTERISTICS
(over operating temperature and voltage range unless otherwise noted. Does not include R & C shifts over temperature)
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
4.1	TRIP POINT RANGE	1800	5200	PPM	Ta = 25°C (See note 4.1)
4.2	TRIP POINT TOLERANCE	-16.0	16.0	%	Variation from calculated value (See note 4.2)
4.3	TRIP POINT DELAY & POWER UP DELAY				
	DELATED VERSION	5.0	16.0	SEC	Ta = 25°C, 1800 TO 5200 PPM
	NON-DELATED VERSION		1.2	SEC	Ta = 25°C, 1800 TO 5200 PPM (See note 4.3)
4.4	DELAY SET TIME TOLERANCE THE SMALLER OF 4.4.1 & 4.4.2				
4.4.1		-40	+40	%	% of delay set time
4.4.2		-4.0	4.0	SEC	
4.5	PASSING DELAY FAIL TO PASS CONDITION		20	%	% of delay set time (See note 4.5)
4.6	EXTERNAL CAPACITOR LEAKAGE COEFFICIENT		0.05		(See note 4.6)

5. LATCH CHARACTERISTICS
(over operating temperature and voltage range unless otherwise noted. External components must be connected between the output and the program/oscillator pin)
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS#
5.1	LATCH VOLTAGE				(See note 5.1)
5.11		3.5	4.5	V	
5.12		2.0	4.5	V	at VS = 5.0
5.2	LATCH CURRENT		1.0	mA	Sourcing (See note 5.2)

6. EXTERNAL R&C CHARACTERISTICS
(over operating temperature and voltage range unless otherwise noted)
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
6.1	EXTERNAL RESISTOR		±5	%	Directly related to 4.2 Trip Accuracy
6.2	EXTERNAL CAPACITOR		±5	%	Directly related to 4.2 Trip Accuracy
6.3	EXTERNAL CAPACITOR LEAKAGE COEFFICIENT		0.05		(See note 6.3)

3.1 Operate point is defined as the gauss level above which the internal circuitry will always be indicating the presence of a south pole at the IC surface.

3.2 Release point is defined as the gauss level below which the internal circuitry will always be indicating the presence of a north pole at the IC surface.

4.1 Trip point is the frequency in PPM (pulses per minute) that causes the output to change state. An inverted output is low when the speed is greater than the trip point. For example, a 2 pole magnet translates to 1 pulse/revolution while the 4 pole magnet translates into 2 pulses/revolution.

The trip point is externally adjustable by varying the value of R and C. Maximum required value of the external capacitor is 3.3 uF.

4.2 This is the accuracy required from unit to unit and includes R and C each varying ±5%, over speed, supply voltage, and temperature.

4.3 Trip point delay is the delay in output response to an input frequency below the trip point. The time delay is determined by the value of the external resistor and capacitor. Delayed version powers up in the passing condition.

4.5 Passing delay tolerance is based on delay set time, its' tolerance is proportional to delay set time tolerance.

4.6 Capacitor leakage coefficient is used to calculate leakage current in the following formula:

$$\text{Leakage Current} = \text{Leakage Coefficient} \times \text{Capacitance} \times \text{Capacitor Voltage}$$

where capacitance is value of the external capacitor.

Capacitor Voltage is the actual variable voltage across the capacitor in operation.

5.1 The open collector output can be used to drive external circuitry that applies a latching voltage to the program/oscillator pin to latch the IC output in the fail state. This latch mode is to be used only on products with delay set times greater than 5 seconds and with inverted output version of the SS421L. Va = externally applied voltage on the RC pin.

5.2 Latching current is the sourcing current required of the applied external voltage to maintain the latch.

6.3 Leakage coefficient is the external capacitor leakage current coefficient such that:

$$\text{Leakage Current} = \text{Leakage Coefficient} \times \text{Capacitance} \times \text{Capacitor Voltage}$$

For example: Leakage Coefficient = 0.05; Capacitance = 3.3 uF; Capacitor Voltage at 3V; DC leakage current 0.49 uA.

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