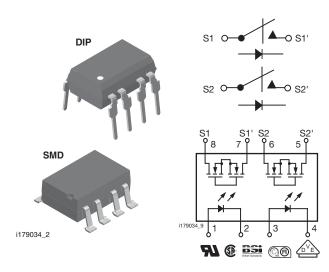


Vishay Semiconductors

Dual 1 Form A Solid State Relay



DESCRIPTION

The LH1520 dual 1 form A relays are SPST normally open switches that can replace electromechanical relays in many applications. They are constructed using a GaAIAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology is comprised of a photodiode array, switch control circuitry, and MOSFET switches. In addition, the LH1520 SSRs employ current limiting circuitry, enabling them to pass FCC 68.302 and other regulatory surge requirements when overvoltage protection is provided.

FEATURES

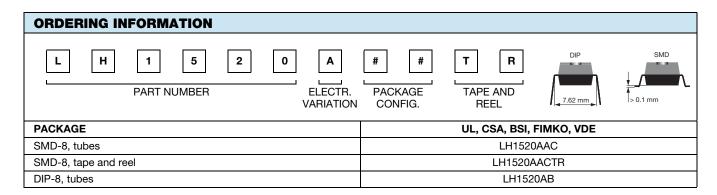
- Dual channel (LH1500)
- Current limit protection
- Isolation test voltage 5300 V_{RMS}
- Typical R_{ON} 20 Ω
- Load voltage 350 V
- Load current 150 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- General telecom switching
- On/off hook control
- Ring delay
- Dial pulse
- Ground start
- Ground fault protection
- Instrumentation
- Industrial controls

AGENCY APPROVALS

- UL1577: file no. E52744 system code H, double protection
- CSA: certification no. 093751
- BSI/BABT: certification no. 7980
- DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending), available with option 1
- FIMKO: approval



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Vishay Semiconductors Dual 1 Form A Solid State Relay



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT			· ·					
LED continuous forward current		I _F	50	mA				
LED reverse voltage	I _R ≤ 10 μA	V _R	8	V				
OUTPUT								
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	350	V				
Continuous DC load current, one pole operating		ار	150	mA				
Continuous DC load current, two poles operating		١L	110	mA				
Peak load current (single shot), form B	t = 100 ms	Ι _Ρ	(2)					
SSR			• • •					
Ambient temperature range		T _{amb}	- 40 to + 85	°C				
Storage temperature range		T _{stg}	- 40 to + 150	°C				
Pin soldering temperature ⁽³⁾	t = 10 s max.	T _{sld}	260	°C				
Input to output isolation test voltage	t = 1 s, I_{ISO} = 10 μ A max.	V _{ISO}	5300	V _{RMS}				
Pole-to-pole isolation voltage (S1 to S2) ⁽¹⁾ (dry air, dust free, at sea level)			1600	V				
Output power dissipation (continuous)		P _{diss}	600	mW				

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability. Breakdown occurs between the output pins external to the package.

(1)

(2) Refer to current limit performance application note for a discussion on relay operation during transient currents.

Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP). (3)

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
LED forward current, switch turn-on	I _L = 100 mA, t = 10 ms	I _{Fon}		1	2	mA	
LED forward current, switch turn-off	$V_L = \pm 300 V$	I _{Foff}	0.2	1.1		mA	
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V	
OUTPUT							
On-resistance	I _F = 5 mA, I _L = 50 mA	R _{ON}	12	20	25	Ω	
Off-resistance	$I_{F} = 0 \text{ mA}, V_{L} = \pm 100 \text{ V}$	R _{OFF}	0.5	300		GΩ	
Current limit	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	I _{LMT}	230	270	370	mA	
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο		0.32	200	nA	
	$I_{F} = 0 \text{ mA}, V_{L} = \pm 350 \text{ V}$	Ι _Ο			1	μA	
Output capacitance	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}$	Co		55		pF	
	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co		10		pF	
Pole-to-pole capacitance (S1 to S2)	I _F = 5 mA			0.5		pF	
Switch offset	I _F = 5 mA	V _{OS}		0.15		μV	
TRANSFER							
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}		1.1		pF	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Turn-on time	I _F = 5 mA, I _L = 50 mA	t _{on}		1.4	2	ms	
Turn-off time	I _F = 5 mA, I _L = 50 mA	t _{off}		0.7	2	ms	

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Dual 1 Form A Solid State Relay

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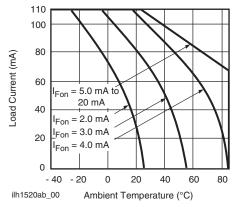


Fig. 1 - Recommended Operating Conditions

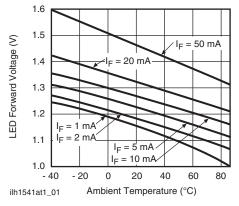
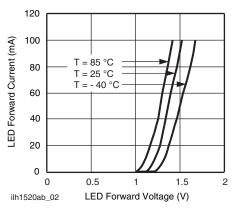


Fig. 2 - LED Voltage vs. Temperature





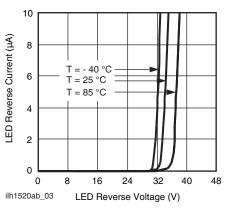


Fig. 4 - LED Reverse Current vs. LED Reverse Voltage

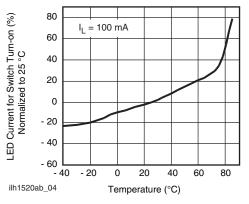
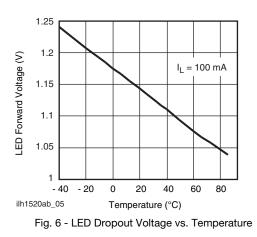


Fig. 5 - LED Current for Switch Turn-on vs. Temperature



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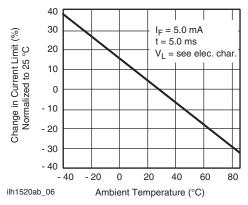


Fig. 7 - Current Limit vs. Temperature

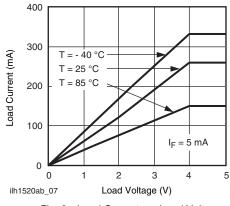


Fig. 8 - Load Current vs. Load Voltage

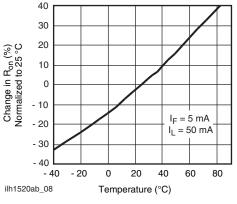


Fig. 9 - On-Resistance vs. Temperature

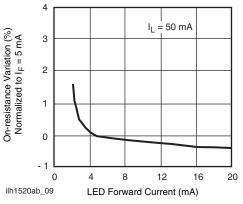
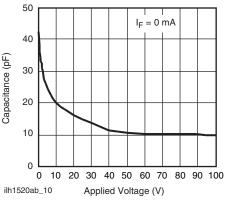
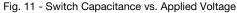


Fig. 10 - Variation in On-resistance vs. LED Current





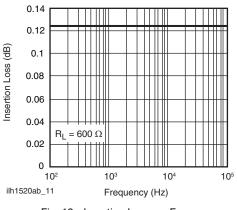


Fig. 12 - Insertion Loss vs. Frequency

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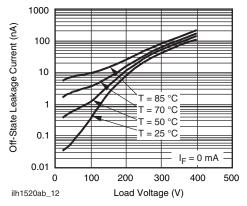
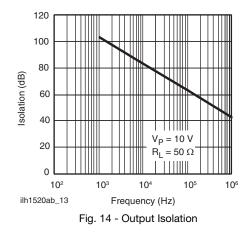


Fig. 13 - Leakage Current vs. Applied Voltage



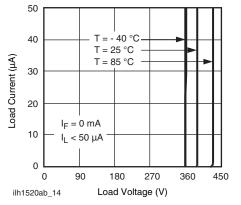


Fig. 15 - Switch Breakdown Voltage vs. Load Current

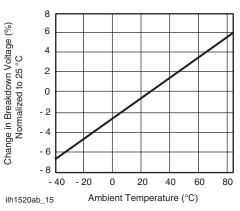


Fig. 16 - Switch Breakdown Voltage vs. Temperature

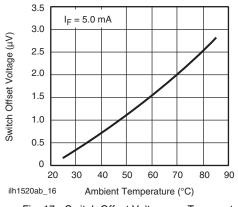
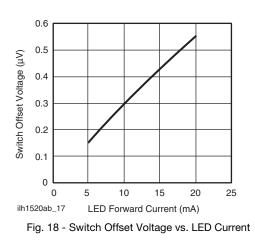


Fig. 17 - Switch Offset Voltage vs. Temperature



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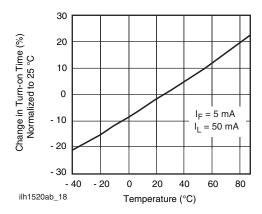
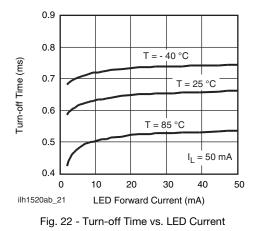


Fig. 19 - Turn-on Time vs. Temperature



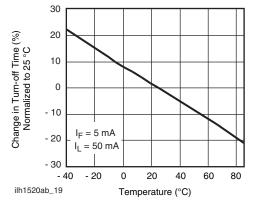


Fig. 20 - Turn-off Time vs. Temperature

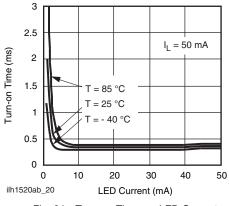


Fig. 21 - Turn-on Time vs. LED Current

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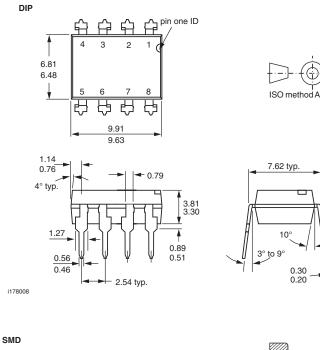
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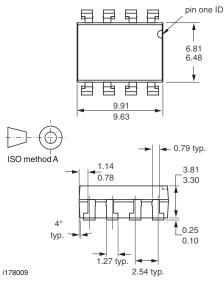
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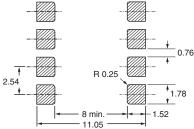
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PACKAGE DIMENSIONS in millimeters



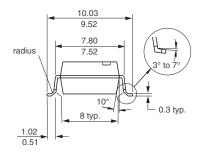




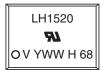
10

0.30

0.20



PACKAGE MARKING (example)



Note

• Tape and reel suffix (TR) is not part of the package marking.

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