COMPLIANT

HALOGEN

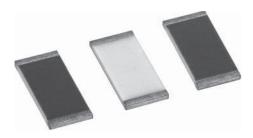
FREE





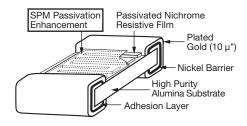
CONSTRUCTION

## Precision Low TCR High Temperature Thin Film Resistor, Surface Mount Chip, ± 5 ppm/°C TCR, 0.02 % Tolerance



meet your exact requirements. These resistors are ideal for use in oil industry precision applications requiring low noise, long term stability under high temperature, ultra low temperature coefficient of resistance, and low voltage coefficient. The chip resistors are available in any resistance ohmic value in the range specified below.

# Vishay's proven precision thin film wraparound resistors will



#### **FEATURES**

- 55 °C to 215 °C operating temperature range
- TCR of ± 5 ppm/°C standard
- Tolerances to ± 0.02 %
- Anti corrosion resistant film with (SPM) special passivation method
- Stable film and performance characteristics
- 0.5 % max. at 2000 h, 215 °C, 25 % rated power
- Non-standard resistance values available
- · Very low noise and voltage coefficient (< - 30 dB, 0.1 ppm/V)
- UL 94 V-0 flame resistant
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

#### **TYPICAL PERFORMANCE**

	ABSOLUTE	
TCR	5	
TOL.	0.02	

STANDARD ELECTRICAL SPECIFICATIONS				
TEST	SPECIFICATIONS	CONDITIONS		
Material	Passivated nichrome	-		
Resistance Range	250 Ω to 50 kΩ	-		
TCR: Absolute	± 5 ppm/°C	- 55 °C to + 125 °C		
Tolerance: Absolute	± 0.1 % to ± 0.02 %	+ 25 °C		
Stability: Absolute	$\Delta R \pm 0.5 \%$	2000 h at 215 °C, 25 % rated power		
Stability: Ratio	-	-		
Voltage Coefficient	± 0.1 ppm/V (typical)	-		
Working Voltage	75 V to 200 V	-		
Operating Temperature Range	- 55 °C to + 215 °C	-		
Storage Temperature Range	- 55 °C to + 215 °C	-		
Noise	< - 35 dB (typical)	-		
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C		

COMPONENT RATINGS					
CASE SIZE POWER RATING (mW)		WORKING VOLTAGE (V)	RESISTANCE RANGE ( $\Omega$ )		
0805	250 at 70 °C	100	250 to 50K		

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

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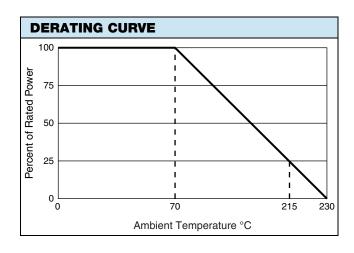
### **PLTT**

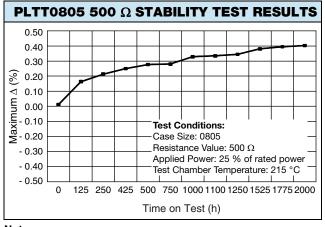
## Vishay Thin Film Precision Low TCR High Temperature Thin Film Resistor, Surface Mount Chip, ± 5 ppm/°C TCR, 0.02 % Tolerance

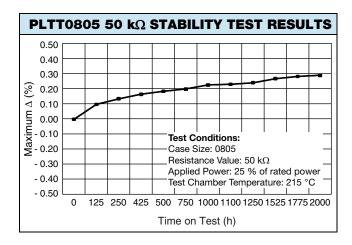


DIMENSIONS in inches						
T W						
CASE SIZE	TERM	L	w	Т	D	E
0805	G	0.080 ± 0.006	0.050 ± 0.005	0.015 to 0.033	0.016 ± 0.008	0.015 ± 0.005

ENVIRONMENTAL TESTS - TYPICAL				
ENVIRONMENTAL TEST	10 kΩ ΔR ± (%)	100 kΩ ΔR ± (%)		
Thermal Shock	0.02	0.02		
Short Time Overload	0.01	0.01		
Low Temperature Operation	0.01	0.01		
Resistance to Solder Heat	0.01	0.01		
Moisture Resistance	0.02	0.02		
High Temperature Exposure	0.02	0.02		
Load Life (25 % Power, 2000 h, + 215 °C)	0.5	0.5		
TCR	± 5 ppm/°C	± 5 ppm/°C		







#### Note

 Performance obtained with following mounting conditions PCB: Polymide IPC-7831A STD land patterns Solder paste: PbSnAg (93.5/5/1.5)





Precision Low TCR High Temperature Thin Film Resistor, Vishay Thin Film Surface Mount Chip, ± 5 ppm/°C TCR, 0.02 % Tolerance

GLOBAL PART NUMBER INFORMATION				
P L T T O	8 0 5	Z 1 [	0 0 1 0	G T 1
GLOBAL CASE TCR MODEL SIZE CHARACTERISTIC	RESISTANCE	TOLERANCE	TERMINATION	PACKAGING
PLTT 0805 Z = ± 5 ppm/°C	The first 3 digits are significant figures and the last digit specifies the number of zeros to follow. "R" designates the decimal point.   Example: $1001 = 1 \text{ k}\Omega$ $2500 = 250 \Omega$ Special values with more than 4 significant figures, use a R for value below 1 k $\Omega$ and a K for values greater than 1 k $\Omega$ to signify a decimal point. $982R6 = 982.6 \Omega$ $532R41 = 532.41 \Omega$	$A = \pm 0.05 \%$	G = Wraparound Gold over Ni barrier (10 μ" min. Au)	WS = WAFFLE WI = 100 min./1mult (item single lot date code) WP = 100 min./1mult (package unit single lot date code)  TAPE AND REEL T1 = 1000 min., 100 mult T5 = 500 min., 500 mult TF = Full reel TS = 100 min., 1 mult TI = 100 min./1mult (item single lot date code) TP = 100 min., 1 mult (package unit single lot date code)

## **Legal Disclaimer Notice**



Vishay

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