



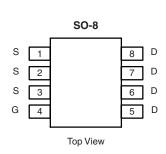
N-Channel 150-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
150	0.050 at V _{GS} = 10 V	5.0		

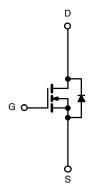
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4488DY-T1-E3 (Lead (Pb)-free) Si4488DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V_{DS}	150		V
Gate-Source Voltage		V _{GS}	± 20		
Ocaliana Daria Ocara (T. 450.00)3	T _A = 25 °C	- I _D	5.0	3.5	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		4.0	2.8	
Pulsed Drain Current		I _{DM}	50		Α
Avalanche Current	L = 0.1 mH	I _{AS}	25		
Continuous Source Current (Diode Conduction) ^a	·	I _S	2.8 1.4		
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	3.1	1.56	W
	T _A = 70 °C		2.0	1.0	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manipulation to Applicate	t ≤ 10 s	- R _{thJA}	33	40		
Maximum Junction-to-Ambient ^a	Steady State	¹ ¹thJA	65	80	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	21		

a. Surface Mounted on 1" x 1" FR4 board.

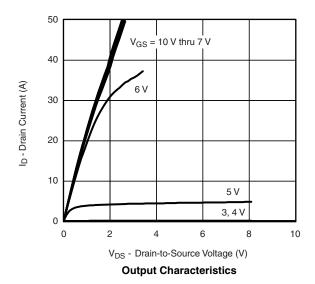
Vishay Siliconix

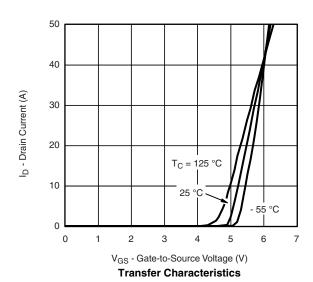


SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted							
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA	
Zava Cata Valtaga Drain Current	I _{DSS}	V _{DS} = 120 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current		$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 5 A		0.041	0.050	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 5 A		18		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 2.8 A, V _{GS} = 0 V		0.75	1.1	V	
Dynamic ^b							
Total Gate Charge	Q_g			30	36	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		8.5			
Gate-Drain Charge	Q_{gd}			8.5			
Gate Resistance	R_g		0.2	0.85	1.2	Ω	
Turn-On Delay Time	t _{d(on)}			12	18		
Rise Time	t _r	V_{DD} = 75 V, R_L = 15 Ω		7	11		
Turn-Off Delay Time	t _{d(off)}	$I_{D} \cong 5 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		22	33	ns	
Fall Time	t _f			10	15		
Source-Drain Reverse Recovery Time t _{rr}		I _F = 2.8 A, dI/dt = 100 A/μs		40	70		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





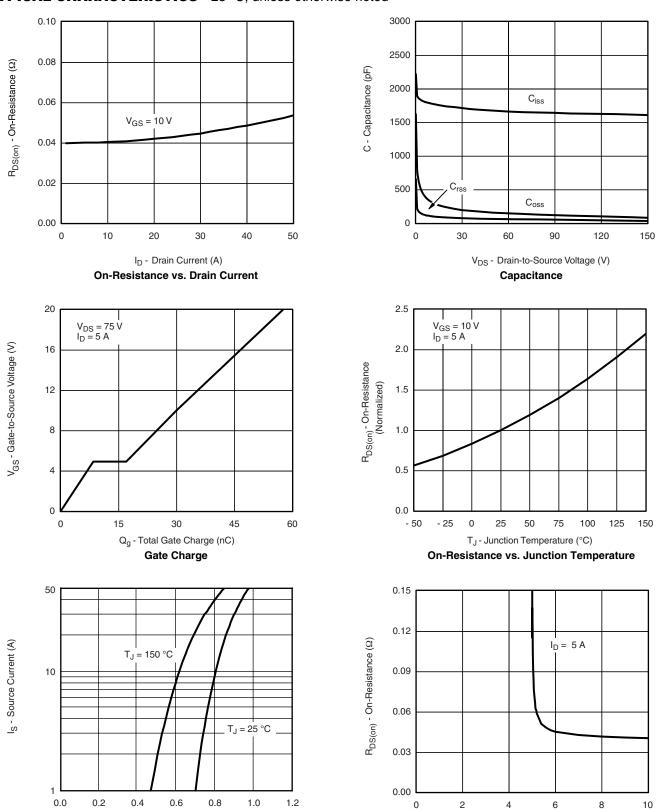
Notes: a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.







TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



V_{SD} - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

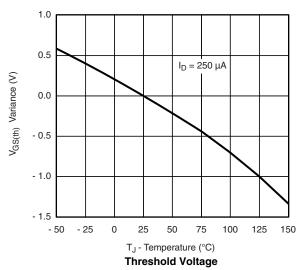
 V_{GS} - Gate-to-Source Voltage (V)

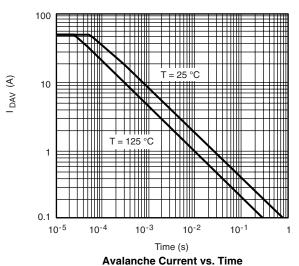
On-Resistance vs. Gate-to-Source Voltage

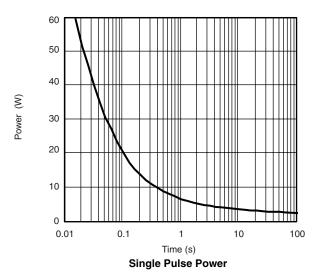
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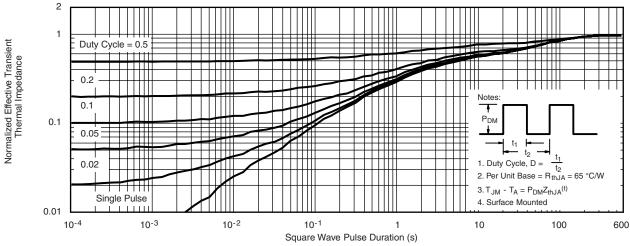
VISHAY

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





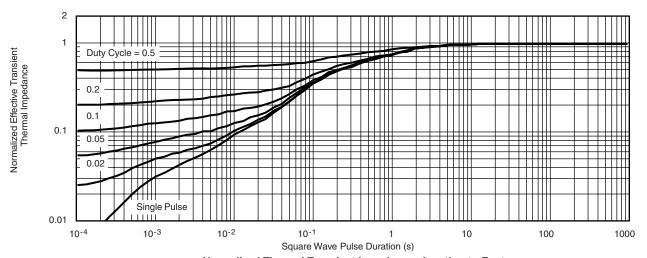




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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