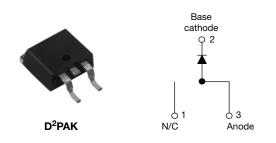


Vishay High Power Products

### Schottky Rectifier, 8 A



8 A

80 V/100 V

**PRODUCT SUMMARY** 

I<sub>F(AV)</sub>

 $V_{\mathsf{R}}$ 

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



COMPLIANT

HALOGEN

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

#### DESCRIPTION

The VS-8TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	8	А			
V <sub>RRM</sub>	Range	80/100	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	А			
V <sub>F</sub>	8 Apk, T <sub>J</sub> = 125 °C	0.58	V			
TJ	Range	- 55 to 175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-8TQ080SPbF	VS-8TQ100SPbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	80	100	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	80	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 157 °C, rectangular waveform		8	А
Maximum peak one cycle non-repetitive surge current	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	850	A
See fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	230	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH		7.50	mJ
Repetitive avalanche current	I <sub>AR</sub>			0.50	А

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# VS-8TQ080SPbF, VS-8TQ100SPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	8 A	T <sub>J</sub> = 25 °C	0.72	V
		16 A		0.88	
		8 A	- T <sub>J</sub> = 125 °C	0.58	
		16 A		0.69	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.55	mA
See fig. 2		T <sub>J</sub> = 125 °C		7	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		500	pF
Typical series inductance	Ls	Measured lead to lead 5 mm from package body		8	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

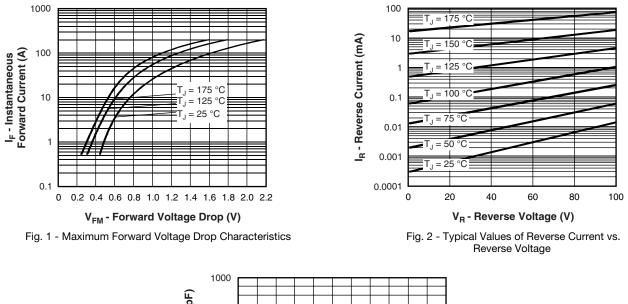
 $^{(1)}\,$  Pulse width < 300  $\mu s,\,duty\,cycle$  < 2  $\,\%$ 

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS
Maximum junction and storage temperature ran	ge	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resist junction to case	ance,	R <sub>thJC</sub>	DC operation See fig. 4	2.0	°C/W
Typical thermal resistant case to heatsink	ce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	C/W
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf · in)
Marking device			Case style D <sup>2</sup> PAK	8TQ080S	
			Case Sigle D-FAR	8TQ100S	



## VS-8TQ080SPbF, VS-8TQ100SPbF

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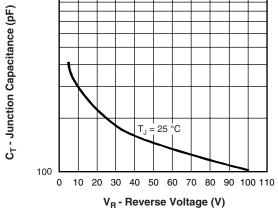


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

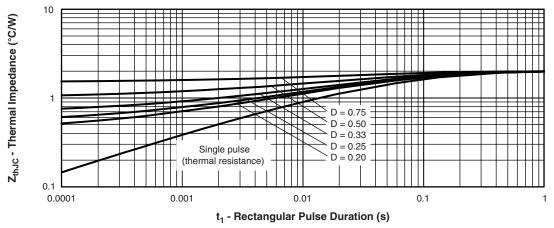
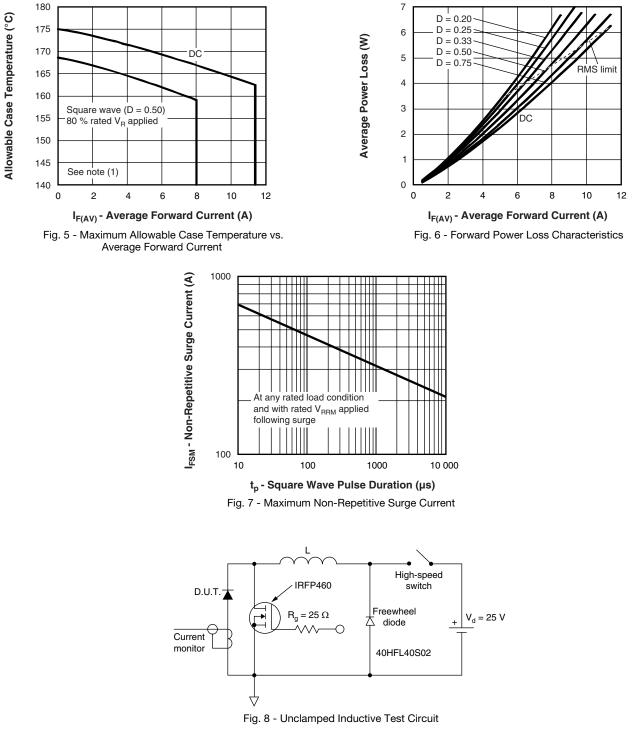


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

# VS-8TQ080SPbF, VS-8TQ100SPbF

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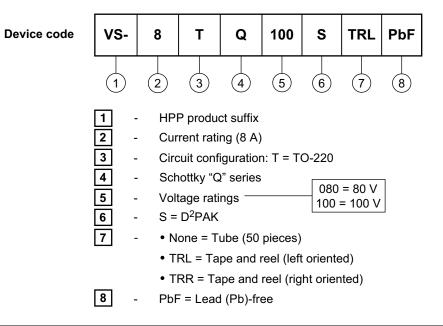
#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = Forward power loss = I_{F(AVA} \times V_{FMA} at (I_{F(AVA}/D) (see$ 
  - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95046			
Part marking information	www.vishay.com/doc?95054		
Packaging information	www.vishay.com/doc?95032		
SPICE models	www.vishay.com/doc?95291		



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