



Delphi Series E48SR12007, 84W Eighth Brick DC/DC Power Modules: 48V in, 12V/7A out

The Delphi Series E48SR12007 Eighth Brick, 48V input, single output, isolated DC/DC converter is the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. This product provides 84 watts of power with 92.0% efficiency in an industry standard footprint. With creative design technology and optimization of component placement, this converter possesses outstanding electrical and thermal performances, as well as extremely high reliability under highly stressful operating conditions. All Delphi E48SR models are fully protected from abnormal input/output voltage, current, temperature conditions and also meet all safety requirements with basic insulation.



FEATURES

- High efficiency: 92.0% @ 12V/7A
- Size: 58.4mmx22.80mmx8.4mm (2.30"x0.90"x0.33")
- Standard footprint
- Industry standard pin out
- 2:1 Input voltage range
- Fixed frequency operation
- Input UVLO, Output OCP, OVP, OTP
- 2250V isolation and basic insulation
- No minimum load required
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- UL/cUL 60950-1 (US & Canada) recognized, and TUV (EN60950-1) certified
- CE mark meets 73/23/EEC and 93/68/EEC directive

OPTIONS

- Positive On/Off logic
- Short pin lengths
- SMD pin

APPLICATIONS

- Telecom/Datacom
- Wireless Networks
- Optical Network Equipment
- Server and Data Storage
- Industrial/Test Equipment



TECHNICAL SPECIFICATIONS

(T_A=25°C, airflow rate=300 LFM, V_{in}=48Vdc, nominal Vout unless otherwise noted.)

PARAMETER	NOTES and CONDITIONS	E48SR12007 (Standard)			
		Min.	Тур.	Units	
ABSOLUTE MAXIMUM RATINGS					
Input Voltage Continuous				80	Vdc
Transient (100ms)	100ms			100	Vdc
Operating Temperature	Refer to Figure21 for measuring point	-40		115	°C
Storage Temperature	Treat to Figure 2 From Headering point	-55		125	°C
Input/Output Isolation Voltage				2250	Vdc
INPUT CHARACTERISTICS					
Operating Input Voltage		36		75	Vdc
Input Under-Voltage Lockout Turn-On Voltage Threshold		33	34	35	Vdc
Turn-Off Voltage Threshold Turn-Off Voltage Threshold		31	32	33	Vdc
Lockout Hysteresis Voltage		1	2	3	Vdc
Maximum Input Current	100% Load, 36Vin			3.0	Α
No-Load Input Current			60	100	mA
Off Converter Input Current			3	10	mA
Inrush Current (I ² t)	D. D. thru 12uH industor 51 le to 20M1 le		O.F.	1	A ² s
Input Reflected-Ripple Current Input Voltage Ripple Rejection	P-P thru 12µH inductor, 5Hz to 20MHz 120 Hz		25 50		mA dB
OUTPUT CHARACTERISTICS	120112		30		UB
Output Voltage Set Point	Vin=48V, Io=Io.max, Tc=25°C	11.880	12.000	12.120	Vdc
Output Voltage Regulation					
Over Load	lo=lo,min to lo,max		±10	±15	mV
Over Line	Vin=36V to 75V		±10	±15	mV
Over Temperature	Tc=-40°C to 85°C	44.70	±100	40.04	mV
Total Output Voltage Range Output Voltage Ripple and Noise	Over sample load, line and temperature 5Hz to 20MHz bandwidth	11.76		12.24	Vdc
Peak-to-Peak	Full Load, 1µF ceramic, 10µF tantalum		40	120	mV
RMS	Full Load, 1µF ceramic, 10µF tantalum		10	25	mV
Operating Output Current Range		0		7	Α
Output Over Current Protection		110		140	%
DYNAMIC CHARACTERISTICS					
Output Voltage Current Transient	48V, 10μF Tan & 1μF Ceramic load cap, 0.1A/μs		200	200	\/
Positive Step Change in Output Current Negative Step Change in Output Current	50% Io.max to 75% Io.max 75% Io.max to 50% Io.max		200	360 360	mV mV
Settling Time (within 1% Vout nominal)	73 /6 IO.IIIAX to 30 /6 IO.IIIAX		200	300	us
Turn-On Transient			200		
Start-Up Time, From On/Off Control			6	12	ms
Start-Up Time, From Input			6	12	ms
Maximum Output Capacitance	Full load; 5% overshoot of Vout at startup			2000	μF
EFFICIENCY			00.0		0/
100% Load 60% Load			92.0 92.0		% %
ISOLATION CHARACTERISTICS			92.0		/0
Input to Output				2250	Vdc
Isolation Resistance			100		MΩ
Isolation Capacitance			1500		pF
FEATURE CHARACTERISTICS			0		
Switching Frequency ON/OFF Control Nagative Remote On/Off logic			350		kHz
ON/OFF Control, Negative Remote On/Off logic Logic Low (Module On)	Von/off at Ion/off=1.0mA	0		0.7	V
Logic High (Module Off)	Von/off at Ion/off=0.0 µA	2.4		18	V
ON/OFF Control, Positive Remote On/Off logic	Total delonion of pri			.0	•
Logic Low (Module Off)	Von/off at Ion/off=1.0mA	0		0.7	V
Logic High (Module On)	Von/off at Ion/off=0.0 μA	2.4		18	V
ON/OFF Current (for both remote on/off logic)	Ion/off at Von/off=0.0V	0.7		1	mA
Leakage Current (for both remote on/off logic)	Logic High, Von/off=15V	40		50	uA
Output Voltage Trim Range	Pout ≤ max rated power	-10		10	%
Output Voltage Remote Sense Range	Pout ≤ max rated power		44.4	10	%
Output Over-Voltage Protection GENERAL SPECIFICATIONS	Over full temp range;		14.4		V
MTBF	lo=80% of lo, max; Ta=25°C, airflow rate=200FLM		2.03		M hour
Weight	10 00 % of 10, max, 14 20 0, airilow rate-2001 Livi		19.6		Grams
Over-Temperature Shutdown	Refer to Figure 21 for measuring point		120		°C

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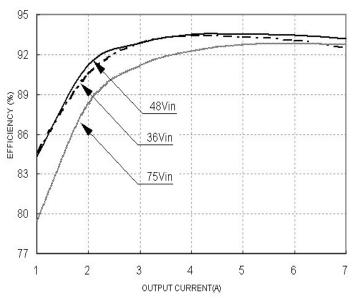


Figure 1: Efficiency vs. load current for minimum, nominal, and maximum input voltage at 25°C, 300LFM airflow.

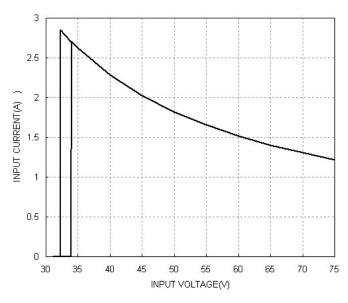


Figure 3: Typical full load input characteristics at 25°C

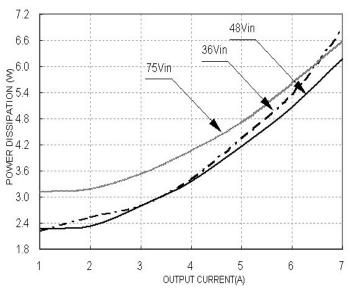
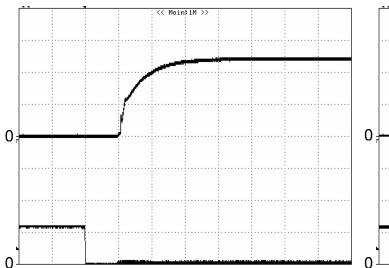


Figure 2: Power dissipation vs. load current for minimum, nominal, and maximum input voltage at 25°C, 300LFM airflow.

For Negative Remote On/Off Logic



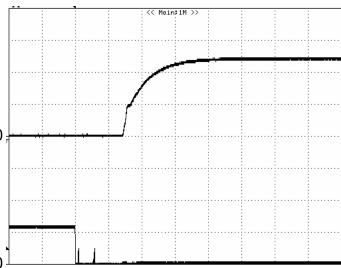
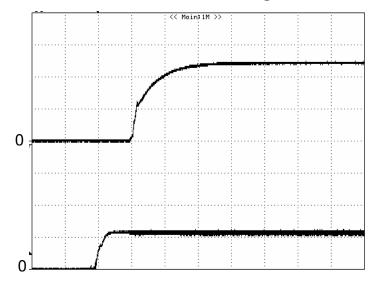


Figure 4: Turn-on transient at full rated load current (resistor load) (2ms/div). Vin=48V. Top Trace: Vout, 5V/div; Bottom Trace: ON/OFF input, 5V/div

Figure 5: Turn-on transient at zero load current (2ms/div). Vin=48V. Top Trace: Vout, 5V/div; Bottom Trace: ON/OFF input, 5V/div

For Positive Remote On/Off Logic



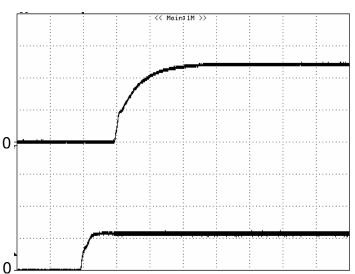
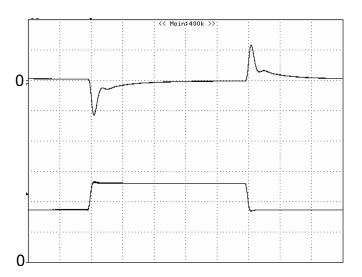


Figure 6: Turn-on transient at full rated load current (resistor load) (2ms/div) for positive on/off mode. Vin=48V. Top Trace: Vout, 5V/div; Bottom Trace: ON/OFF input, 5V/div

Figure 7: Turn-on transient at zero load current (2ms/div) for positive on/off mode. Vin=48V. Top Trace: Vout, 5V/div; Bottom Trace: ON/OFF input, 5V/div



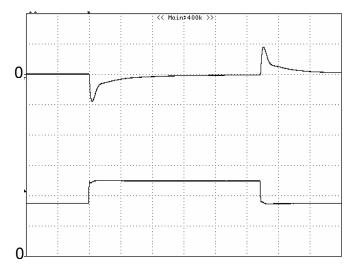
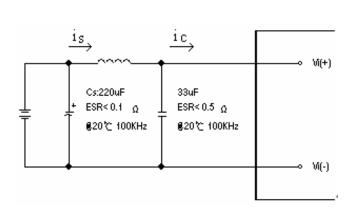


Figure 8: Output voltage response to step-change in load current (75%-50%-75% of lo, max; di/dt = $0.1A/\mu$ s). Load cap: 10μ F tantalum capacitor and 1μ F ceramic capacitor. Top Trace: Vout (0.2V/div, 200us/div), Bottom Trace: lout (1A/div). Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module

Figure 9: Output voltage response to step-change in load current (75%-50%-75% of Io, max; di/dt = $2.5A/\mu s$). Load cap: $47\mu F$, $35m\Omega$ ESR solid electrolytic capacitor and $1\mu F$ ceramic capacitor. Top Trace: Vout (0.2V/div, 200us/div), Bottom Trace: lout (1A/div). Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module



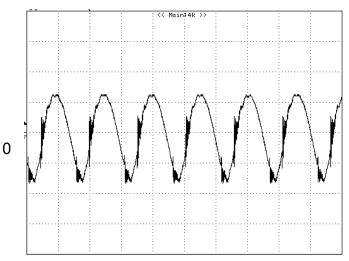
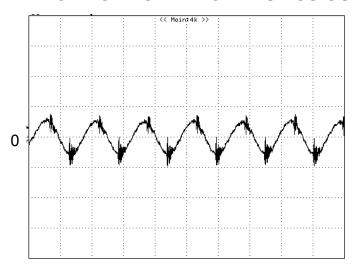


Figure 10: Test set-up diagram showing measurement points for Input Terminal Ripple Current and Input Reflected Ripple Current.

Note: Measured input reflected-ripple current with a simulated source Inductance (L_{TEST}) of 12 μ H. Capacitor Cs offset possible battery impedance. Measured current as shown below

Figure 11: Input Terminal Ripple Current, i_c, at full rated output current and nominal input voltage with 12μH source impedance and 33μF electrolytic capacitor (500 mA/div, 2us/div)



Copper Strip

Vo(+)

10u

The proper Strip

Vo(-)

Vo(-)

Figure 12: Input reflected ripple current, i_s, through a 12µH source inductor at nominal input voltage and rated load current (20 mA/div, 2us/div)

Figure 13: Output voltage noise and ripple measurement test setup



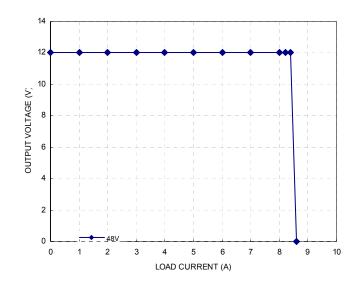


Figure 14: Output voltage ripple at nominal input voltage and rated load current (Io=7A)(20 mV/div, 2us/div) Load capacitance: 1μF ceramic capacitor and 10μF tantalum

Figure 15: Output voltage vs. load current showing typical current limit curves and converter shutdown points

Load capacitance: $1\mu F$ ceramic capacitor and $10\mu F$ tantalum capacitor. Bandwidth: 20 MHz. Scope measurements should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.

DESIGN CONSIDERATIONS

Input Source Impedance

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few μ H, we advise adding a 10 to 100 μ F electrolytic capacitor (ESR < 0.7 Ω at 100 kHz) mounted close to the input of the module to improve the stability.

Layout and EMC Considerations

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. An external input filter module is available for easier EMC compliance design. Application notes to assist designers in addressing these issues are pending to release.

Safety Considerations

The power module must be installed in compliance with the spacing and separation requirements of the end-user's safety agency standard, i.e., UL60950, CAN/CSA-C22.2 No. 60950-00 and EN60950: 2000 and IEC60950-1999, if the system in which the power module is to be used must meet safety agency requirements.

Basic insulation based on 75 Vdc input is provided between the input and output of the module for the purpose of applying insulation requirements when the input to this DC-to-DC converter is identified as TNV-2 or SELV. An additional evaluation is needed if the source is other than TNV-2 or SELV.

When the input source is SELV circuit, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 75 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from the ac mains by reinforced or double insulation.
- The input terminals of the module are not operator accessible.
- If the metal baseplate is grounded, one Vi pin and one Vo pin shall also be grounded.
- A SELV reliability test is conducted on the system where the module is used, in combination with the module, to ensure that under a single fault, hazardous voltage does not appear at the module's output.

When installed into a Class II equipment (without grounding), spacing consideration should be given to the end-use installation, as the spacing between the module and mounting surface have not been evaluated.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a normal-blow fuse with 10A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

Soldering and Cleaning Considerations

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying are especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

FEATURES DESCRIPTIONS

Over-Current Protection

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will automatically shut down (hiccup mode).

The modules will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

Over-Voltage Protection

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the module will shut down (Hiccup mode). The modules will try to restart after shutdown. If the fault condition still exists, the module will shut down again. This restart trial will continue until the fault condition is corrected.

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down.

The module will try to restart after shutdown. If the over-temperature condition still exists during restart, the module will shut down again. This restart trial will continue until the temperature is within specification.

Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during logic high. Positive logic turns the modules on during logic high and off during logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi(-) terminal. The switch can be an open collector or open drain.

For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi(-). For positive logic if the remote on/off feature is not used, please leave the on/off pin floating.

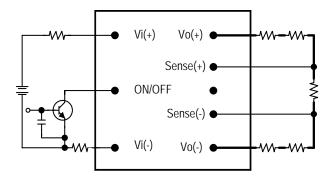


Figure 16: Remote on/off implementation

Remote Sense

Remote sense compensates for voltage drops on the output by sensing the actual output voltage at the point of load. The voltage between the remote sense pins and the output terminals must not exceed the output voltage sense range given here:

$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \le 10\% \times Vout$$

This limit includes any increase in voltage due to remote sense compensation and output voltage set point adjustment (trim).

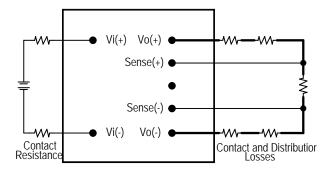


Figure 17: Effective circuit configuration for remote sense operation

If the remote sense feature is not used to regulate the output at the point of load, please connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

The output voltage can be increased by both the remote sense and the trim; however, the maximum increase is the larger of either the remote sense or the trim, not the sum of both.

When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power does not exceed the maximum rated power.

FEATURES DESCRIPTIONS (CON.)

Output Voltage Adjustment (TRIM)

To increase or decrease the output voltage set point, the modules may be connected with an external resistor between the TRIM pin and either the SENSE(+) or SENSE(-). The TRIM pin should be left open if this feature is not used.

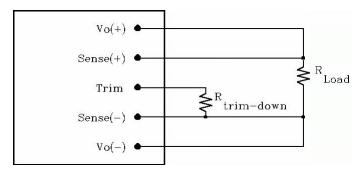


Figure 18: Circuit configuration for trim-down (decrease output voltage)

If the external resistor is connected between the TRIM and SENSE (-) pins, the output voltage set point decreases (Fig. 18). The external resistor value required to obtain a percentage of output voltage change \triangle % is defined as:

$$Rtrim - down = \left[\frac{511}{\Delta} - 10.22\right] (K\Omega)$$

Ex. When Trim-down -10%(12V×0.9=10.8V)

Rtrim - down =
$$\left[\frac{511}{10} - 10.22\right] (K\Omega) = 40.88(K\Omega)$$

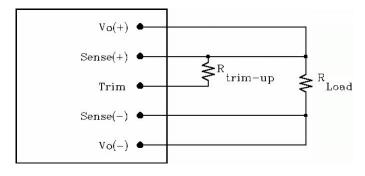


Figure 19: Circuit configuration for trim-up (increase output voltage)

If the external resistor is connected between the TRIM and SENSE (+), the output voltage set point increases (Fig. 19). The external resistor value required obtaining a percentage output voltage change \triangle % is defined as:

$$Rtrim - up = \frac{5.11 \times Vo \times (100 + \Delta)}{1.225 \times \Delta} - \frac{511}{\Delta} - 10.22 (K\Omega)$$

Ex. When Trim-up +10%(12 V×1.1=13.2V)

$$Rtrim - up = \frac{5.11 \times 12 \times (100 + 10)}{1.225 \times 10} - \frac{511}{10} - 10.22 = 489.3 (K\Omega)$$

The output voltage can be increased by both the remote sense and the trim, however the maximum increase is the larger of either the remote sense or the trim, not the sum of both.

When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

THERMAL CONSIDERATIONS

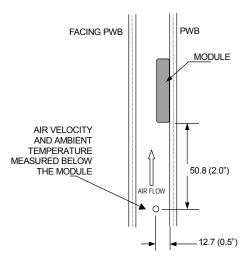
Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 20: Wind tunnel test setup

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.

THERMAL CURVES

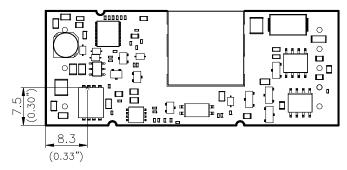


Figure 21: Hot spot temperature measured point *The allowed maximum hot spot temperature is defined at 115 $\mathcal C$

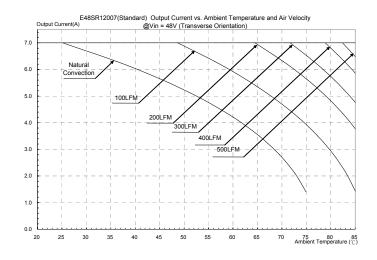


Figure 22: Output current vs. ambient temperature and air velocity@Vin=48V (Transverse Orientation)

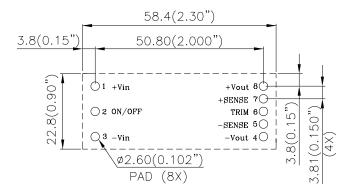
PICK AND PLACE LOCATION

58.4(2.30") PIN 1 26.50(1.043") 99 00.0 (0.28") MIN. AREA PICK AND PLACE LOCATION

NOTES:
ALL DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)

X.XXmm±0.25mm(X.XXX in.±0.010 in.)

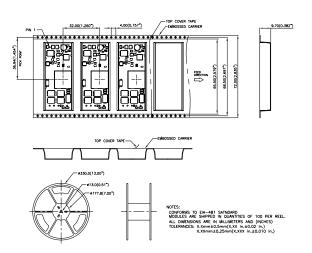
RECOMMENDED PAD LAYOUT (SMD)



RECOMENDED P.W.B. PAD LAYOUT

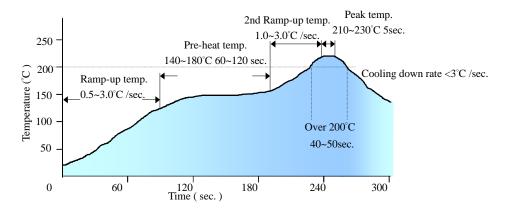
NOTES:
DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

SURFACE-MOUNT TAPE & REEL



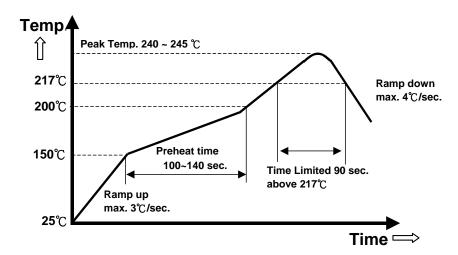
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LEADED (Sn/Pb) PROCESS RECOMMENDED TEMPERATURE PROFILE



Note: The temperature refers to the pin of E48SR, measured on the pin +Vout joint.

LEAD FREE (SAC) PROCESS RECOMMENDED TEMPERATURE PROFILE



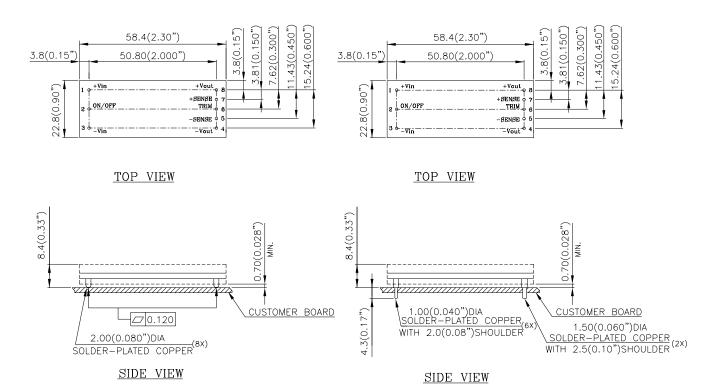
Note: The temperature refers to the pin of E48SR, measured on the pin +Vout joint.

^{*} For modules with through-hole pins and the optional heatspreader, they are intended for wave soldering assembly onto system boards, please do not subject such modules through reflow temperature profile.

MECHANICAL DRAWING (WITHOUT HEATSPREADER)

Surface-mount module

Through-hole module



NOTES:

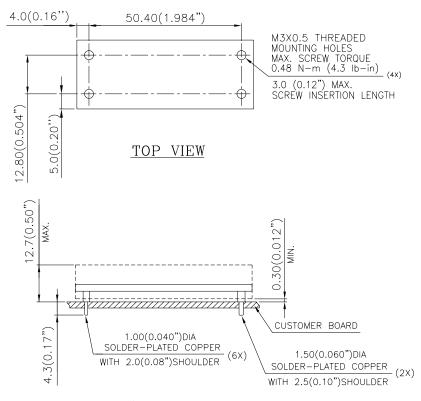
DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

<u>Pin No.</u> <u>Name</u>		<u>Function</u>			
1	+Vin	Positive input voltage			
2	ON/OFF	Remote ON/OFF			
3	-Vin	Negative input voltage			
4	-Vout	Negative output voltage			
5	-SENSE	Negative remote sense			
6	TRIM	Output voltage trim			
7	+SENSE	Positive remote sense			
8	+Vout	Positive output voltage			

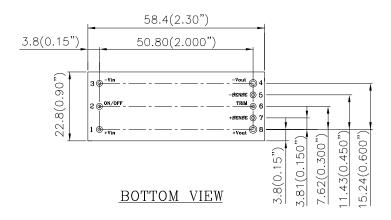
MECHANICAL DRAWING (WITH HEATSPREADER)

* For modules with through-hole pins and the optional heatspreader, they are intended for wave soldering assembly onto system boards, please do not subject such modules through reflow temperature profile.

THROUGH-HOLE MODULE



SIDE VIEW



NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)
No excess than 2.0mm for screws

PART NUMBERING SYSTEM

E	48	S	R	120	07	N	R	F	Α
Type of Product	Input Voltage	Number of Outputs	Product Series	Output Voltage	Output Current	ON/OFF Logic	Pin Length		Option Code
E- Eighth Brick	48V	S- Single	R- Regular	120-12V	07- 7A	N- Negative (Default) P- Positive	R - 0.170" (Default) N - 0.145" K - 0.110" M - SMD pin	(Lead Free)	A - Standard Functions (Default) H - with Heat Spreader

MODEL LIST

MODEL NAME	INPUT		OUTPUT		EFF @ 100% LOAD	
E48SR1R225NRFA	36V~75V	1.3A	1.2V	25A	88.0%	
E48SR1R525NRFA	36V~75V	1.5A	1.5V	25A	89.5%	
E48SR1R825NRFA	36V~75V	1.8A	1.8V	25A	90.5%	
E48SR2R520NRFA	36V~75V	1.9A	2.5V	20A	89.0%	
E48SR3R320NRFA	36V~75V	2.5A	3.3V	20A	90.5%	
E48SR05012NRFA	36V~75V	2.1A	5.0V	12A	91.5%	
E48SR12005NRFA	36V~75V	2.2A	12V	5A	92.0%	
E48SR12006NRFA	36V~75V	2.6A	12V	6A	92.5%	
E48SR12007NRFA	36V~75V	3.0A	12V	7A	92.0%	
E48SR15004NRFA	36V~75V	2.2A	15V	4A	92.0%	

Default remote on/off logic is negative and pin length is 0.170"

For different remote on/off logic and pin length, please refer to part numbering system above or contact your local sales office.

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WARRANTY

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

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