

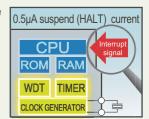


Features

1 Low current consumption

 $0.5\mu A$ suspend (HALT) mode and leakage current controlled at high temperatures

Low power equivalent to a mask ROM is achieved. Original low-leak low-power processes are utilized for low current consumption throughout the entire temperature range. As a result battery consumption is optimized, even at high temperatures.



applicable models:

M.610482, ML610482P, ML6100482, ML6100482P, ML6100411, ML6100411P, ML6100412, ML6100412P, ML6100421, ML6100421P, ML6100422P, ML610042P, ML6100429, ML6100429, ML6100429, ML6100439, ML61004

OKI SEMICONDUCTOR's ultra Low Power Microcontroller ML610400 / ML610300 series Ver.1.0

4 Simple tools

On-chip debugging

Repeated program loading and debugging are handled on the chip itself. As a debugging interface, a dedicated test terminal is provided, making it possible to debug all peripheral functions of the microcontroller.

In addition, a uniform development environment enables optional settings to be easily made via button operation to a compiler or other application, while an LCD image tool allows viewing of the LCD image on a PC without the need for an LCD panel.

Applicable models :

All products with built-in Flash memory

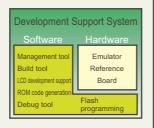
7 LCD driver built in

Integrated LCD driver compatible with both dot matrix and segment displays

OKI SEMICONDUCTOR's versatile lineup supports a wide range of LCDs, from multi-dot displays comprised of 1536 dots to small 55-segment displays. In addition, a boost circuit that generates the required LCD bias is included, eliminating the need for external circuitry.

Applicable models

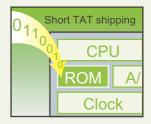
All low power microcontrollers with built-in LCD driver



5 Chip writing possible

Flash chip products available

Similar to mask ROM, the customer's program code is written at OKI SEMICONDUCTOR's facility, then shipped via short TAT.

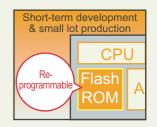


Applicable models : All products enabling Chip Support

8 Supports Flash memory

Integrated Flash memory for programs

The built-in Flash significantly reduces development and production time compared with mask ROM. Also, reprogramming is possible via serial interface for on-chip debugging.



Applicable models : Products with built-in Flash memory

Low voltage operating

Works on a single 1.5V battery

Most models support low voltage operation from 1.1V. making them compatible with 1.5V battery sets. This contributes to lower power consumption and greater miniaturization.

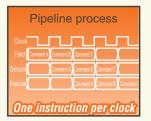
ML610482, ML610482P, ML6100482, ML6100482P, ML6100411, ML6100411P, ML6100412, ML6100412P, ML6100421, ML6100421P, ML6100421P, ML6100422P, ML6100428, ML6100428P, ML6100438P, ML6100438P, ML6100438P, ML6100438P, ML6100438P,

ML610Q439, ML610Q439P

High performance

One instruction per clock cycle

Utilizes OKI SEMICONDUCTOR's own RISC 8bit CPU, featuring a pipeline process that enables execution of most instructions in a single clock cycle and delivers 16bit MCU-class performance.



These ultra-low power consumption microcontrollers ensure longer operating times in battery-driven applications.

Low operating voltage

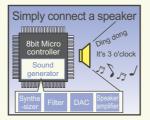
V Operation

The increasing concern over global warming demands components and devices that minimize energy consumption. OKI SEMICONDUCTOR meets these needs by offering a low-power microcontroller equipped with Flash memory that enables reading at only 1V and features industry-leading operating, suspend, and stop current levels. In addition, the a high-performance CPU is utilized capable of processing one instruction per clock cycle for high performance operation.

Built-in speaker amp offers superior sound and voice playback

Monolithic IC delivers excellent audio and voice reproduction

Proprietary voice synthesis algorithms, together with an integrated low-pass filter, D/A converter, speaker amp, and voice analysis technologies ensure superior audio and voice playback on a single chip.



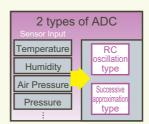
Applicable models

ML610340, ML610Q340, ML610346, ML610Q346, ML610347, ML610Q347, ML610348, ML610Q348

2 types of A/D converters built-in

2 built-in A/D converters: RC Oscillation Type + Successive Approximation Type

The RC oscillation type A/D converter is designed for temperature and humidity measurements while the successive approximation type is used to measure the output voltage. This enables a variety of measurements using a single microcontroller.



ML610Q411, ML610Q411P, ML610Q412, ML610Q412P, ML610Q415, ML610Q421, ML610Q421P. ML610Q422, ML610Q432P, ML610Q431, ML610Q432, ML610Q435, ML610Q436, ML610Q438, ML610Q438P, ML610Q439, ML610Q439, ML610Q439, ML610Q439P, ML610Q43P, ML610Q43P, ML610Q43P, ML610Q43P, ML610Q43PP, ML610Q4PP, ML610Q4PP, ML610Q4PP, ML610Q4PP, ML610Q4PP, ML610Q4PP, ML610Q4PP, ML610Q4PP, ML6

Extensive worldwide sales

Record sales in a variety of fields

OKI SEMICONDUCTOR's low-power microcontrollers have been adopted in a wide range of devices and applications, from low-end consumer equipment to industrial sets requiring high reliability - a testament to their versatility and dependability.



Applicable models: All models currently mass-produced

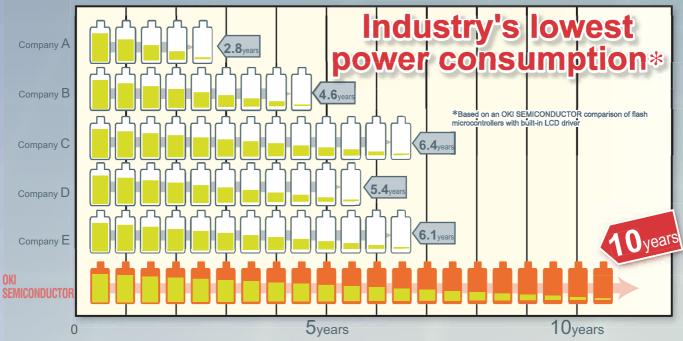
10 years of driving on a single battery



OKI SEMICONDUCTOR's original low-power consumption technology* results in the industry's lowest current consumption during both normal operation and stop. In addition, both dual- (selectable low/high speed) and triple- (selectable low/medium/high speed) clock functionality are offered for longer battery life, while current leakage is prevented for low current consumption throughout the entire temperature range.

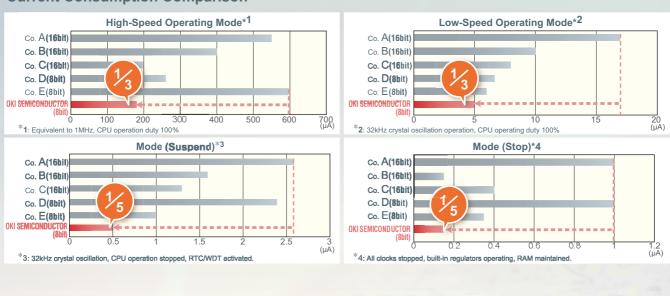
* Intermittent operation can reduce the average current consumption of the battery even if a current on the order of mA is required.

In this case OKI SEMICONDUCTOR recommends intentionally reducing operating time or increasing the operating frequency.



Calculation: Theoretical values when driving via II-ion battery at 3V and performing low-/high-speed/suspend (HALT) operations every 5 seconds in the following order: 1) Low-speed (32kHz) for 1.5s, 2) High-speed (1MHz) operation for 10ms, 3) suspend (HALT) operation for 3.49s.

Current Consumption Comparison









Key Features

Ultra-low current consumption

Low current consumption is ensured under all operating modes: normal, suspend, stop

800μA (@4MHz CPU operation) 5μA (@32kHz CPU operation) 70μA (@500kHz CPU operation) 0.5μA (during Suspend (HALT) Mode 0.15μA (during Stop Mode)

1V Flash ROM operation

The internal Flash ROM can be read using only 1V. Microcontroller operating voltage range: 1.1V to 3.6V

High precision

2 types of A/D converters built in for high precision operation (16/24bit RISC oscillation type + 12bit Successive approximation type)

Fewer parts required

Requires no resonator or oscillator for peripheral IC

Built-in PLL (Phase Locked Loop) oscillation circuit

The internal PLL circuit generates the high-speed 4.096MHz clock by multiplying the 32.768kHz clock

RISC CPU One instruction per clock Performance equivalent to a 16bit microcontroller

Flash ROM Driver

Successive approximation type 12bit

RC oscillation type 16/24bit

500kHz built-in RC oscillation

4MHz built-in PLL oscillation

32kHz crystal oscillation

On-chip debug circuit

Supports various panel sizes

Dot Matrix Type: 144 to 1,536 dots Segment Type: 55 to 185 dots

Programmable display allocation function

Compatible with changing LCD designs



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On-chip debugging reduces development time

Supports debug and Flash memory writing via OKI SEMICONDUCTOR's 'µEASE' emulator



1V Flash ROM operation

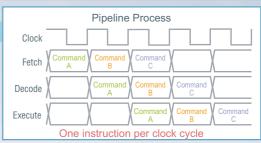
A unique low-power method enables Flash memory reading at only 1V. In addition, reducing the charge/discharge current through bit-line division enables low power consumption equivalent to mask ROMs.

Applicable models: All models except low power microcontrollers with speech capability

Flash Memory Operation Comparison 40% reduction Voltage (V)

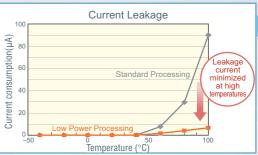
High-speed 1 instruction/clock cycle 8bit RISC CPU 'U8' core

The 8bit RISC CPU utilizes parallel processing via a pipeline architecture for high-speed, 1 instruction/clock cycle operation - reducing CPU operating time significantly.



Few current leakage at high temperature

Original low-power processing prevents current leakage at high temperatures, making OKI SEMICONDUCTOR MCUs compatible with even industrial applications.

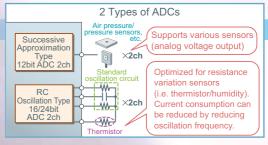




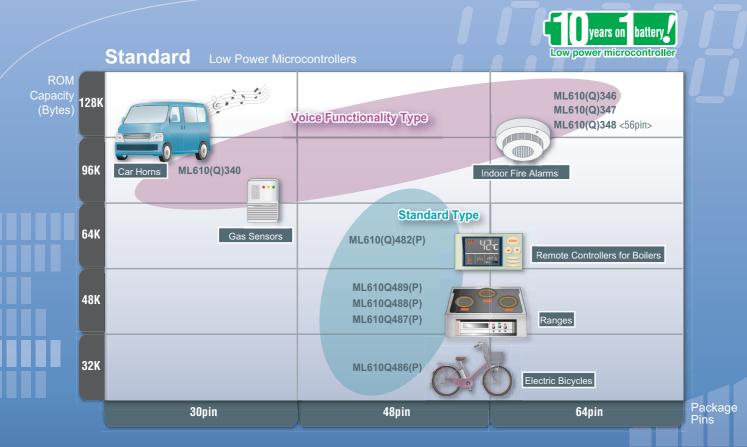
2 types if A/D converters built in

2 types of A/D converters are built in, a Successive Approximation type requiring analog voltage input and an RC Oscillation type that counts the number of oscillations caused by a resistive sensor (i.e. thermistor, humidity) and external capacitor. Select the optimum sensor based on the characteristics, measurement accuracy, and current consumption.

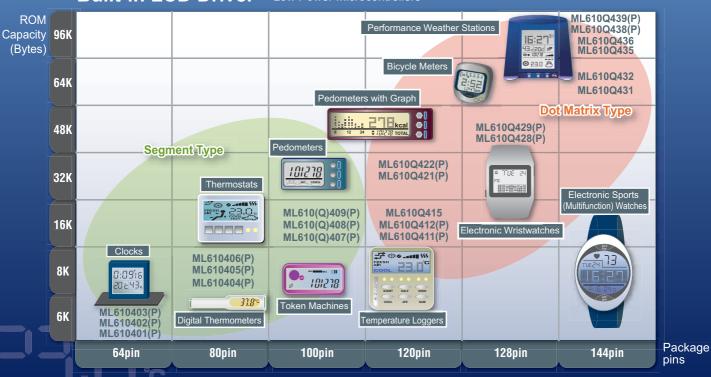
Applicable models: Low power MCUs with built-in dot matrix LCD driver



Lineup



Built-in LCD Driver Low Power Microcontrollers





FLASH MASK ROM type

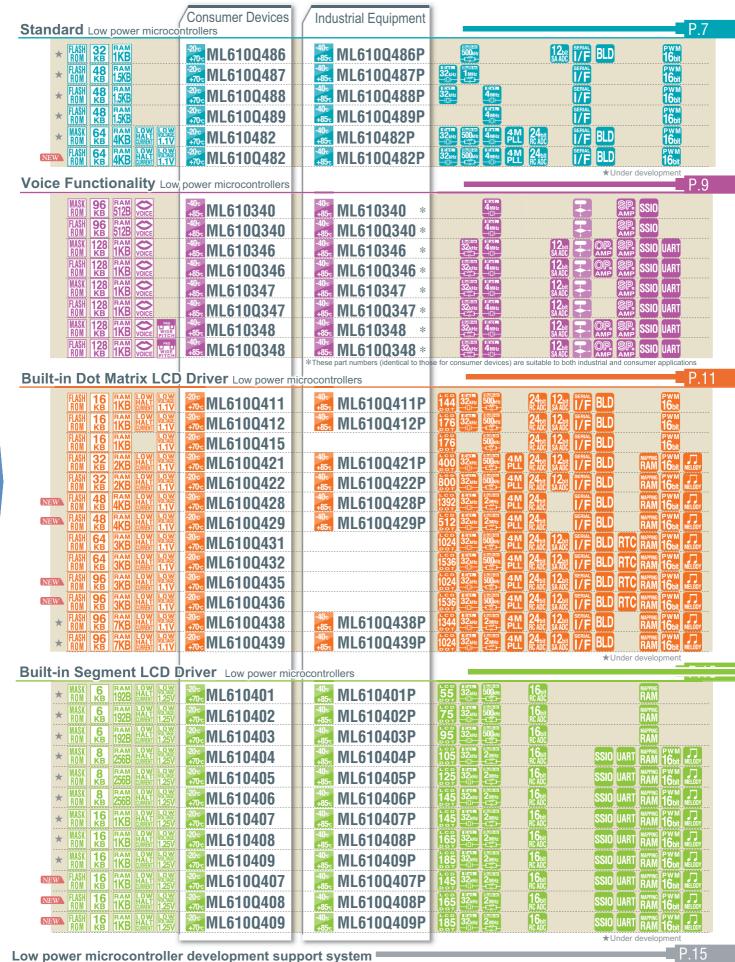
6 8 16 32 48 64 96 128 ROM size



SP Integrated speaker amp







IDU8 integrated development environment (source editor, code generation tool, debugger), on-chip debug emulator, Flash writing software, LCD tool, reference board with microcontroller, demo kit

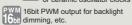
Current consumption at during Suspend (HALT) mode: 0.5μA to 0.9μA

Built-in RTC (Real Time Clock) for sets with calendar function

LOW VOLTAGE VOLTAGE (VDD) 1.1V 1.25V operation



Compatible with external 4.096MHz crystal resonator or ceramic oscillator clocks









Melody output





Display allocation RAM

Standard Type

These low power microcontrollers are ideal for compact, battery-driven systems without an LCD. The units integrate program memory (32kB-64kB), RAM (1kB-4kB), 3 types of serial communication ports (I²C, SSIO, UART), a battery voltage detection circuit, 2 types of A/D converters (24bit RC, 12bit successive approximation), timer, PWM, general-purpose port, and LED driver terminal into a single chip.





LINEUP	Consumer Grade	Industrial Grade		Package
* FLASH 32 RAM 1KB	²⁰ c ML610Q486	40c 485c ML610Q486P	12bt FERNAL BLD PWM SA ADD 16bt	
* FLASH 48 RAM 1.5KB	²⁰ c ML610Q487	40c ML610Q487P	32kHz 1MHz 1/F 16bit	
* FLASH 48 RAM 1.5KB	²⁰ c ML610Q488	40c ML610Q488P	32krtz 4mitz J/F 16bit	
★ FLASH 48 RAM 1.5KB	²⁰ c ML610Q489	40c ML610Q489P	4mitz JF 16bit	TQFP48
* MASK 64 RAM LOW VOLTAGE ROM KB CURRENT 1.1V	²⁰ c ML610482	⁴⁰ ML610482P	32kitz 500kit 4mitz PLL RCADC 1/F BLD PWM 16bit	
FLASH 64 RAM LOW LOW ROW ROW LOW	^{20c} _{+70c} ML610Q482	40c ML610Q482P	324tt 500er 4mir PLL RCADE 1/F BLD 16bit	
★ Under development			Please refer to pages 5 and 6 for icon descriptions	Actual size shown

System Diagram

Flash/Mask ROM (Driveable from 1V)

Pin-compatible mask ROM available (1.1V to 3.6V operation)

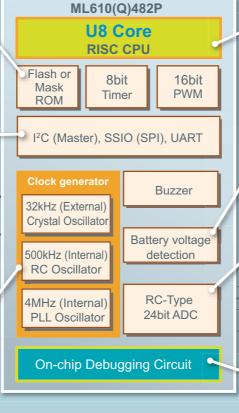
Supports a variety of serial interfaces

I²C, SSIO, and UART interfaces are provided for broad compatibility

32.768kHz Crystal Resonator

Triple clock functionality contributes to low power consumption designs

Current consumption can be controlled by adjusting the clock speed:
Low (32kHz)
Medium (500kHz)
High (4MHz)



High speed RISC CPU "U8" Core

This high-speed processor executes one instruction per clock cycle via parallel processing utilizing pipeline architecture

Battery voltage detection

The built-in A/D converter enables precise voltage measurement (±2%) with low power consumption

Built-in 2ch ADC enables both temperature and humidity measurements

The integrated RC-type ADC is capable of measuring the temperature and humidity with high accuracy

×2ch

On-chip debugging shortens development time

OKI SEMICONDUCTOR's µEASE provides support for debugging and Flash memory writing

Triple clock system facilitates low power design

Three different clocks can be selected for CPU operation: lowspeed (external) 32kHz crystal oscillation, medium-speed (internal) 500kHz RC oscillation, and high-speed (internal) 4MHz PLL oscillation. The medium- and high-speed clocks can be divided into 1/8ths in order to minimize current consumption. In addition, the units are compatible with external 4MHz precision oscillators for applications requiring greater accuracy.

Current Consumption

Speed	Frequency	Current Consumption
Low speed	32kHz (External Crystal Oscillator)	5µA
Medium speed	500kHz (Internal RC Oscillator)	70µA
High speed	4MHz (Internal PLL Oscillator)	800μΑ

Applicable models: ML610482, ML610482P, ML610Q482, ML610Q482P

Clock generator block Frequency division 1/8,1/4,1/2,1/1 1/2 **Clock selection** ON/OF 0 high External precision Crystal resonator Crystal/ceramic resonator or external clock

Power management via clock selection

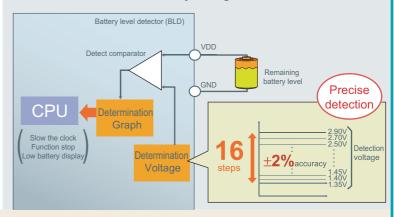
Battery voltage detection function (BLD) accurately detects remaining charge

Precise (±2%) battery voltage detection makes it possible to manage operation and performance based on the remaining battery level.

In addition, 16-step voltage detection is performed between 1.35V and 2.9V, with a voltage accuracy of ±2%.

Applicable models: ML610Q486, ML610Q486P, ML61048, ML610482P, ML610Q482, ML610Q482P

Precise battery voltage detection



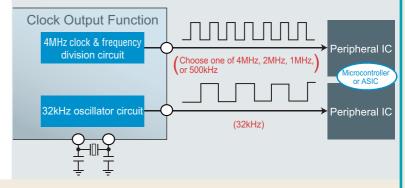
Clock output reduces system costs

Both the low and high speed clocks can be output, eliminating the need for an oscillator for the external peripheral IC, reducing system costs.

Applicable models : All models except ML610Q486, ML610Q486P, ML610Q489, ML610Q489P

Chacifications

Low-/high-speed clock output simplifies system configuration



- *1:4MHz generated via internal PLL/ceramic/crystal oscillation, 500kHz via RC oscillation, 32kHz via crystal oscillation
- *3: Only the Master function is compatible with Fast Mode (400kbps)/Standard Mode (100kbps)
- *5: The test area comprises 1Kbyte

- *2: Current consumption during Suspend (HALT) mode
- *4: Only the Master function supports Standard Mode (50kbps) *6: No compatible chip select signals exist for 8bit/16bit SPI bus

		Operating Conditions				ROM	/RAM		Functio					
	Part No.	Operating			Current *2 Operating		ROM RAM				Serial por	rt	Package	Chip Support
		voltage (V)	frequency	consumption (Typ.@HALT)	temperature (°C)	capacity (Byte)	capacity (Byte)	WDT	ADC(method)	I ² C	SSIO*6	UART		Support
★ ML6	10Q486 / ML610Q486P	1.6 to 3.6	500kHz	15μΑ	-20 to +70 / -40 to +85	Flash 32K ^{*5}	1K	1	4 (Sequential)	1 *4	1	1	TQFP48	0
★ ML6	10Q487 / ML610Q487P	1.8 to 3.8	1MHz 32.768kHz	1.7μΑ	-20 to +70 / -40 to +85	Flash 48K *5	1.5K	1	-	1	1	2	TQFP48	0
★ ML6	10Q488 / ML610Q488P	1.8 to 3.8	4MHz 32.768kHz	1.7μΑ	-20 to +70 / -40 to +85	Flash 48K	1.5K	1	_	1	1	2	TQFP48	0
★ML6	10Q489 / ML610Q489P	1.8 to 3.8	4MHz 31.25kHz (4MHz frequency division)	-	-20 to +70 / -40 to +85	Flash 48K	1.5K	1	-	1	1	2	TQFP48	0
★ML6	10482 / ML610482P	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Mask 64K	4K	1	2 (RC oscillation)	1 *3	1	1	TQFP48	0
EW ML6	10Q482 / ML610Q482P	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 64K	4K	1	2 (RC oscillation)	1 *3	1	1	TQFP48	0

Voice Functionality Type

OKI SEMICONDUCTOR low power microcontrollers with voice output function feature program memory (96kB to 128kB), RAM (512B/1kB), successive approximation A/D converter, UART/SSIO interface, timer, general-use port, voice synthesis, digital filter, 16bit D/A converter, and speaker amplifier.





LINEUP	Consumer/Industrial Grade		Package
MASK 96 FAM VOICE	40c ML610340	4 SP. SSIO	
FLASH P6 S12B VOICE	-40c +85c ML610Q340	4MIL SP. SSIO	SS0P30
MASK ROM 128 RAM VOICE	-40° ML610346	32 AMD 12 BA ADD AMP SSIO UART	_1000000000
FLASH 128 RAM VOICE VOICE	ML610Q346	32 MF 12 DI AMP SSIO UART	
MASK 128 RAM VOICE	-40€ +85€ ML610347	SZAMPE 4MHE 12bit AMP SSIO UART	TQFP64
FLASH 128 RAM (KB) 1KB VOICE	⁻⁴⁰ ₅ ML610Q347	SP SSIO UART	111111111111111111111111111111111111111
MASK ROM 128 RAM VOICE WIDE WIDE WIDE	-40c ML610348	32 AMP SSIO UART	None
FLASH 128 RAM COLOR LATER TO THE PROPERTY OF T	40c ML610Q348	32kHz 4MHz 12kH	OFDEC

Please refer to pages 5 and 6 for icon descriptions.

Actual size shown

System Diagram

Low current consumption

Low voltage operation (2.2V to 5.5V) and low current consumption during Suspend (HALT) Mode (1.5µA)

Large internal memory

96kB/128kB Flash/mask ROM enables voice playback for 30 to 40 seconds

High quality, high output voice playback

A 16bit DAC, low-pass filter, 1W speaker amp, and high compression/high fidelity HQ-ADPCM are implemented using hardware

On-chip debugging reduces development time

OKI SEMICONDUCTOR's µEASE provides support for debugging and Flash memory writing

ML610(Q)346 **U8 Core RISC CPU** Successive Flash or approximation Mask ROM type ADC 4MHz (External) 32kHz (Internal) **RC** Oscillator Crystal Oscillator 8bit SSIO Operational 1 **UART** Timer Amplifier Sound generator Digital 16bit Speaker Synthesize DAC **Amplifier** filter On-chip Debugging Circuit

High speed RISC CPU "U8" Core

This high-speed processor executes one instruction per clock cycle via parallel processing utilizing pipeline architecture

3ch ADC built-in

The 3ch ADC is suitable for voltage output sensor input

4.096MHz Crystal Resonator

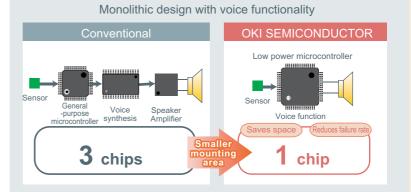
Fewer parts

A number of components are built in for analog processing, such as an opamp and speaker amp, reducing the number of parts required

Speaker

For consumer and industrial equipment

Many of the required features are built in, from a voice synthesis engine to a speaker amplifier, reducing the number of external parts. This increase integration reduces the susceptibility to failures and breakdowns.



Loud, high-fidelity audio playback

OKI SEMICONDUCTOR's audio microcontrollers feature hardware-based sound functionality. A high-fidelity sound circuit, comprised of a synthesizer, digital filer, 1W speaker amp, and 16bit DAC, is integrated, resulting in higher sound quality than competitors' middleware systems under the same conditions. In addition, 4 different compression methods can be selected: 4bit ADPCM2, HQ-ADPCM, 8bit non-linear PCM, and 8bit/16bit PCM). The sampling frequency is adjustable as well, from 6.4 to 32kHz, to meet set requirements. The result is superior audio playback of high compression audio.

5 algorithms built in with selectable Superior sound sound quality/compression size playba Audio Playbacl CPU (8U) Register for speech data input **ROM** Smaller Audio data written to the register CPU load from the ROM via software

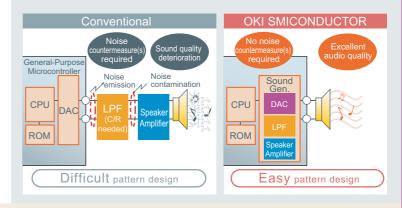
High fidelity playback through dedicated hardware

Built-in LPF and speaker amplifier minimize noise

Enabling audio playback utilizing middleware requires an external LPF (Low Pass Filter) and speaker amp. However, sound quality is adversely affected by the occurrence of noise between the microcontroller and speaker amp, necessitating countermeasures. Conversely, OKI SEMICONDUCTOR's audio microcontroller is equipped with both an LPF and speaker amp, eliminating the need for troublesome analog circuit and pattern designs while minimizing sound deterioration, noise mixing, and noise emission.

No noise-induced deterioration

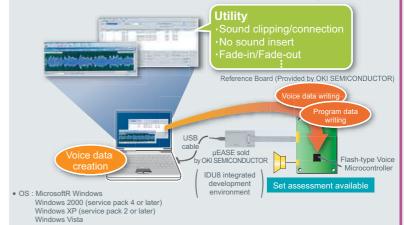
*4bit ADPCM2, HQ-ADPCM, 8bit non-linear PCM, 8/16bit PCM



Voice synthesis utility simplifies editing and data creation

OKI SEMICONDUCTOR's voice synthesis utility, along with a reference board and audio microcontroller, enable easy editing of the voice data and ROM data generation while listening to the actual audio. In addition, the application program is developed using a program development support system, while a Flash programming software (FWµEASE or MWµEASE), along with the µEASE on-chip debugging emulator, enable evaluation of application ROM codes and voice ROM data in the user's actual set.

Easy development with OKI SEMICONDUCTOR's voice synthesis utility



- Processor and memory: 1GHz IntelR Pentium or equivalent, 512MB RAM
 Hard disk space: 1GB of free space
 Audio system: Windows-compatible sound card supporting at least 16bit and speaker

*Program data should be developed under "IDEU8 integrated development environment" separately.

*1:4MHz generated via internal PLL/ceramic/crystal oscillation 500kHz via RC oscillation, 32kHz via crystal oscillation 42: Current consumption during Suspend (HALT) mode at low-speed 32: No compatible chip select signals exist for 8bit/16bit SPI bus 32kHz oscillation **Specifications**

		Оре	erating Conditions		ROM/F	RAM		Functi	ons/Features			
Part No.	Operating	Operating *1	Current *2	Operating	ROM	RAM		ADC	Operational	Seria	l port	Package
	voltage (V)	frequency	consumption (Typ.@HALT)	temperature (°C)	capacity (Byte)	capacity (Byte)	WDT	(method)	Amplifier	SSIO	UART	Ü
ML610340	2.2 to 5.5	4.096MHz	-	-40 to +85	Mask 96K	512	1	-	-	1	-	SSOP30
ML610Q340	2.2 to 5.5	4.096MHz	-	-40 to +85	Flash 96K	512	1	-	-	1	-	SSOP30
ML610346	2.2 to 5.5	4.096MHz 32kHz	1.5µA	-40 to +85	Mask 128K	1K	1	12bit × 3ch (Sequential)	3	1	1	TQFP64
ML610Q346	2.2 to 5.5	4.096MHz 32kHz	1.5µA	-40 to +85	Flash 128K	1K	1	12bit × 3ch (Sequential)	3	1	1	TQFP64
ML610347	2.2 to 5.5	4.096MHz 32kHz	1.5µA	-40 to +85	Mask 128K	1K	1	12bit × 12ch (Sequential)	-	1	1	TQFP64
ML610Q347	2.2 to 5.5	4.096MHz 32kHz	1.5µA	-40 to +85	Flash 128K	1K	1	12bit × 12ch (Sequential)	-	1	1	TQFP64
ML610348	2.2 to 3.6	4.096MHz 32kHz	1.5µA	-40 to +85	Mask 128K	1K	1	12bit × 3ch (Sequential)	2	1	1	QFP56
ML610Q348	2.2 to 3.6	4.096MHz 32kHz	1.5µA	-40 to +85	Flash 128K	1K	1	12bit × 3ch (Sequential)	2	1	1	QFP56

Dot Matrix Type with Built-in LCD Driver

These low-power microcontrollers integrate a boost circuit for driving LCDs, eliminating the need for an external LCD power supply. The lineup covers a wide range of dot counts, from 144 to 1,536. In addition, program memory (16kB-96kB), RAM (1kB-7kB), 3 types of serial communication interfaces (12C, SSIO, UART), a battery voltage detection circuit, 2 A/D converters (24bit RC oscillation, 12bit successive approximation), melody, buzzer, capture, timer, and general-purpose ports are all integrated into a single chip, making them ideal for compact, battery-driven Low power microcontroller LCD applications.





Actual size shown

LINEUP		Consumer Grade	Industrial Grade								Package
FLASH 16 RAI	M LOW LOW B HALT VOLTAGE B CURRENT 1.1V	-20°c ML610Q411	-40: ML610Q411P	144 3	32 _{kHz} 500kH		24 _{bit} 12 _{bit} RC ADC SA ADC	I/F BLD	8	MM Obit	
FLASH 16 RAI	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q412	⁴⁰ • ML610Q412P	176	32 _{kHz} 500kH:		24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	16	MM Obit	
FLASH 16 RAI ROM KB 1KI	M LOW VOLTAGE 1.1V	²⁰ c ML610Q415		144 100 T	БОПЕЛ 500кн С 11		24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	1	VМ Sbit	100
FLASH 32 RAI ROM KB 2K	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q421	ML610Q421P	400 3	32kHz 500kH:	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	MAPPING P.V.	Obit MELODY	TQFP120
FLASH 32 RAI ROM KB 2K	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q422	ML610Q422P	800	32kHz 500kHz	4M PLL	24 _{bit} 12 _{bit} IC ADC SA ADC	SERIAL BLD	MAPPING P.V.	Obit MELODY	
NEW FLASH 48 RAI 4K	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q428	ML610Q428P	1392	B2kHz 2MHz	4M PLL	24 _{bit} RC ADC	SERIAL BLD	RAM 1	Obit MELODY	
NEW FLASH 48 RAI KB 4K	M LOW LOW B HALT VOLTAGE 1.1V	²⁰ c ML610Q429	ML610Q429P	512 ³	Bullen B2kHz D CMHz	4M PLL	24 _{bit} RC ADC	SERIAL BLD	MAPPING P.V.	Obit MELODY	
FLASH 64 RAI ROM KB 3K	M LOW LOW B HALT VOLTAGE 1.1V	^{20°} _{+70°} ML610Q431		1024	32 _{kHz} 500 _{kHz}	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	RTC RAM 1	Dbit MELODY	TQFP128
NEW FLASH 64 RAI ROM KB 3K	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	^{20c} ML610Q432		15363	32 _{kHz} 500 _{kH}	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	RTC RAM 1	VM J	-
NEW FLASH 96 RAI 8 KB 3K	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q435		1024	32kHz 500kH:	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	RTC RAM 1	Dbit MELODY	
★ FLASH 96 RAI	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q436		1536	32 _{kHz} 500 _{kH}	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	RTC RAM 1	Dbit MELODY	
★ FLASH 96 RAI	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	²⁰ c ML610Q438	-40°c ML610Q438P	1344	B2kHz 2MHz	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	SERIAL BLD	MAPPING P.V. RAM 1	Dbit MELODY	
FLASH 96 RAI ROM KB 7K	M LOW LOW B HALT VOLTAGE CURRENT 1.1V	^{20c} ML610Q439	^{-40c} ML610Q439P	1024	Bulltain 32kHz 2MHz	4M PLL	24 _{bit} 12 _{bit} RC ADC SA ADC	I/F BLD	MAPPING P.V	Obit MELODY	LQFP144

Single-battery drive with remarkably low current consumption

This entire series features low operating voltage (1.1V to 3.6V) and a suspend (HALT) current of 0.5µA, making it OKI SEMICONDUCTOR's lowest power lineup. Operating current is reduced by up to 86% over conventional models, enabling operation from a single battery, reducing both costs and waster.

	Conventional 8bit Flash Microcontroller	OKI SEMICONDUCTOR Low power 8bit Flash Microcontroller
Operating voltage	1.8V to 3.6V	1.1V to 3.6V
Suspend(HALT) current	2.0μΑ	0.5μΑ
Standby(STOP) current	0.8μΑ	0.15μΑ
Operating current	50μA (32kHz CPU operation) 6mA (4MHz CPU operation)	
		*OKI SEMICONDUCTOR survey

Applicable models : All models except MI 610Q415

2 types of A/D converters enable a variety of measurements

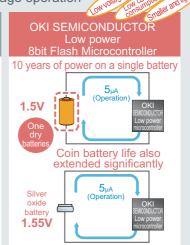
2 types of A/D converters are built in, a Successive Approximation type requiring analog voltage input and an RC Oscillation type that counts the number of oscillations caused by a resistive sensor (i.e. thermistor, humidity) and external capacitor. Select the optimum sensor based on the characteristics, measurement accuracy, and current consumption.

Applicable models : All models except ML610Q428, ML610Q428P, ML610Q429, ML610Q429P

Low current consumption/ Low voltage operation

Lithium battery **3V**

ease refer to pages 5 and 6 for icon descriptions



2 types of ADC

Air pressure/ pressure sensors, 2ch Successive Approximation ×2ch 12bit ADC 2ch 24bit RC -W ×2ch Oscillation **ADC** Thermistor

Supports various sensors (analog voltage output)

Optimized for resistance variation sensors (i.e. thermistor/humidity). Current consumption can be reduced by reducing oscillation frequency.

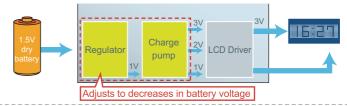
Stable LCD operation - even at low battery levels

A boost circuit is built in that generates all voltages required to drive the LCD, preventing the battery level from affecting the display quality. In addition, no regulator is required, reducing both parts and costs.

32-step contrast control eliminates display fluctuations

The LCD drive voltage is adjustable in 32 steps (at 1/3 bias) via software control, enabling precision contrast adjustment.

Stable power supply circuit for driving LCD without depending on battery voltage



LCD drive power supply setting



Help any panel to be even contrast

Triple clock capability facilitates low power consumption designs

Three clocks are selectable for optimum performance: a lowspeed 32kHz clock (crystal), a medium-speed 500kHz clock (RC), and a high-speed 4MHZ type (PLL). Both the mediumand high-speed clocks feature adjustable frequency division to maximize energy efficiency.

In addition, support for an external 4Mhz precision oscillator is enabled for applications requiring high reliability, such as communications circuits.

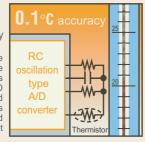
Current consumption per clock

Speed	Frequency	Current consumption
Low	32kHz, crystal oscillation	5μΑ
Medium	500kHz built-in, RC oscillation	70μA
High	4MHz built-in, PLL oscillation	800μΑ

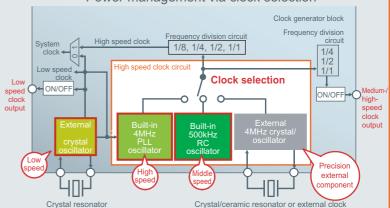
Applicable models : All models except ML610Q411, ML610Q411P, ML610Q412, ML610Q412P, ML610Q415

Precision RC oscillation-type A/D converter built in for 0.1C° temperature measurement accuracy

0.1°C temperature precision is possible through a combination of software correction and RC oscillation principles with thermistor. The RC oscillation A/D converted is considered the best method for temperature measurement since it is easy to adjust component costs and current consumption based on target precision and measurement time.



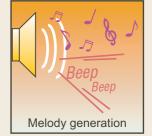
Power management via clock selection



Easily play a variety of melodies

Melody generation is possible based on musical score using a maximum of 29 scales, 63 sound lengths, and 5 tempos, simply via register setting. addition, the output waveform (digital) is generated from a hardware circuit, making it easy to control using software.

ML610Q411P, ML610Q412, ML610Q412P, ML610Q415



Applicable models: All models except ML610Q415 *1: 4MHz generated via internal PLL/ceramic/ crystal oscillation, 500kHz via RC oscillation 32kHz via crystal oscillation 2: Current consumption during Suspend (HALT) mode at low-speed 32kHz oscillation
 5: No compatible chip select signals exist for 8bit/16bit SPI bus
 6: Includes 1KByte test area
 7: All only the Master Function is upports standard mode (50kbps)
 7: Includes 1KByte LCD allocation RAM

			Operatir	ng Conditions		ROM/F	ROM/RAM Functions/Features								
	Part No.	Operating voltage(V)	Operating frequency	Current *2 consumption (Typ.@HALT)	Operating temperature(°C)	ROM capacity (Byte)	RAM capacity (Byte)	WDT	ADC(Method)	I ² C	Serial po	ort UART	LCD driver	Package	Chip Support
	ML610Q411 / ML610Q411P	1.1 to 3.6	500kHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to+85	Flash 16K ^{*6}	1K	1	2 (RC oscillation) 2 (Sequential)	1 *4	1	1	Max.144dot 36seg × 4com.	TQFP120	0
	ML610Q412 / ML610Q412P	1.1 to 3.6	500kHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 16K ^{*6}	1K	1	2 (RC oscillation) 2 (Sequential)	1 *4	1	1	Max.176dot 44seg × 4com.	TQFP120	0
	ML610Q415	1.1 to 3.6	500kHz	5.5µA	-20 to +70	Flash 16K	1K	1	2 (RC oscillation) 2 (Sequential)	1 *4	1	1	Max.144dot 36seg × 4com.	TQFP120	0
	ML610Q421 / ML610Q421P	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 32K	2K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.400dot 50seg × 8com.	TQFP120	0
	ML610Q422 / ML610Q422P	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 32K	2K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.800dot 50seg × 16com.	TQFP120	0
NE	ML610Q428 / ML610Q428P	1.1 to 3.6	4.096MHz 2MHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 48K	4K *7	1	2 (RC oscillation)	1 *3	1	1	Max.1392dot 58seg × 24com.	TQFP128	0
NE	WAML610Q429 / ML610Q429P	1.1 to 3.6	4.096MHz 2MHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 48K	4K *7	1	2 (RC oscillation)	1 *3	1	1	Max.512dot 64seg × 8com.	TQFP128	0
	ML610Q431	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70	Flash 64K	3K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.1024dot 64seg × 16com.	LQFP144	0
	ML610Q432	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70	Flash 64K	3K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.1536dot 64seg × 24com.	LQFP144	0
NE	ML610Q435	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70	Flash 96K	3K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.1024dot 64seg × 16com.	LQFP144	0
NE	W_ML610Q436	1.1 to 3.6	4.096MHz 500kHz 32.768kHz	0.5μΑ	-20 to +70	Flash 96K	3K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.1536dot 64seg × 24com.	LQFP144	0
	★ML610Q438 / ML610Q438P	1.1 to 3.6	4.096MHz 2MHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 96K	7K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.1344dot 56seg × 24com.	LQFP144	0
	★ ML610Q439 / ML610Q439P	1.1 to 3.6	4.096MHz 2MHz 32.768kHz	0.5μΑ	-20 to +70 / -40 to +85	Flash 96K	7K *7	1	2 (RC oscillation) 2 (Sequential)	1 *3	1	1	Max.1024dot 64seg × 16com.	LQFP144	0

Segment Type with Built-in LCD Driver

These low-power microcontrollers with segment LCD driver integrates a boost circuit for driving an LCD. The lineup ranges from 55 to 185 segments. Additional features include built-in program memory (6kB-16kB) and RAM (192B-1kB), 2 types of serial communication interfaces (SSIO, UART), 16bit RC A/D converter, melody, buzzer, timer, and general-purpose port, making them ideal for compact, LCD-equipped devices.





Actual size shown

LINEUP	Consumer Grade	Industrial Grade		Package
* MASK 6 RAM LOW VOTAGE 192B LOW CURRENT 1.25V	-20°c +70°c ML610401	-40c -485c ML610401P	555 324t 5000t 16bit RAM	
★ MASK 6 PAM LOW VOLTAGE 192B CURRENT 1.25V	^{20c} ML610402	⁴⁰ : ML610402P	SZERIE 5000er 16bit RAM	
* MASK 6 RAM LOW VOLTAGE 192B CURRENT 1.25V	²⁰ c ML610403	⁴⁰ : ML610403P	95 32kH 500ch RC ADC RAM	TQFP64
* MASK 8 PAM LOW VOLTAGE 1.25V	²⁰ c ML610404	⁴⁰ • ML610404P	105 32km 2mm 16bit SSIO UART RAM 16bit MELODY	<u> </u>
★ MASK 8 BAM LOW VOLTAGE 1.25V	²⁰ ML610405	ML610405P	125 324tz 2mtz 16bit SSIO UART RAM 16bit MELODY	10 10 10 10 10 10 10 10 10 10 10 10 10 1
★ MASK 8 PAM LOW VOLTAGE 1.25V	²⁰ c ML610406	ML610406P	145 32 RC ADC SSIO UART RAM 16bt MELODY	
* MASK 16 RAM LOW VOLTAGE 1.25V	²⁰ ML610407	ML610407P	145 32km 2mm 16bit SSIO UART RAM 16bit MELODY	TQFP80
* MASK 16 RAM LOW VOLTAGE 1KB CURRENT 1.25V	²⁰ c ML610408	⁴⁰ • ML610408P	165 32km 2min 16bit SSIO UART RAM 16bit MELODY	плиничиний
★ MASK ROM 16 RAM LOW VOLTAGE 1.25V	²⁰ c ML610409	ML610409P	185 32 PRINT PART RAM 16bit MELODY	Marie Mari
FLASH 16 RAM LOW VOLTAGE 1.25V	²⁰ ML610Q407	ML610Q407P	145 32442 27442 RC ADC SSIO UART RAM 16bit MELODY	
FLASH 16 RAM LOW VOLTAGE 1.25V	²⁰ ML610Q408	ML610Q408P	165 32 MAPPING TO LART RAM 16bit MELODY	TQFP100
FLASH ROM KB RAM LOW LOW VOLTAGE 1.25V	^{20c} _{470c} ML610Q409	ML610Q409P	185 324Hz 2MHz RCADC SSIO WART RAM 16bit MELODY	



★ Under development

Low current consumption

Low voltage operation (1.25V to 3.6V) with low current consumption during Suspend (HALT) Mode (0.9µA)

Flash/Mask ROM (Driveable from 1V)

Pin-compatible mask ROM available (1.25V to 3.6V operation)

2 types of serial I/F

Both SSIO and UART interfaces are included for testing or connection with external memory

> 32.768kHz Crystal Resonator

Dual-clock functionality

Control current consumption by adjusting the clock speed: Low (32kHz) High (2MHz)

ML610(Q)407/ ML610(Q)408 / ML610(Q)409

U8 Core RISC CPU

8bit

16bit PWM Mask ROM Timer SSIO(SPI) Melody/Buzzer UART

LCD

Driver

16bit

RC

ADC

Clock generator

Flash or

32kHz (External) Crystal Oscillator

2MHz (Internal) RC Oscillator

On-chip Debugging Circuit

On-chip debugging reduces development time

OKI SEMICONDUCTOR's µEASE provides support for debugging and Flash memory writing

High speed RISC CPU "U8" Core

Please refer to pages 5 and 6 for icon descriptions.

This high-speed processor executes one instruction per clock cycle via parallel processing utilizing pipeline architecture

Segment type LCD display

Supports a maximum of 185 segments (37seg x 5com)

Accommodates LCD design changes

Built-in display allocation RAM makes extremely easy to changes programmably the LCD design

×2ch

Built-in 2ch ADC enables both temperature and humidity measurements

OKI SEMICONDUCTOR'S µEASE provides support for debugging and Flash memory writing

Stable LCD operation - even at low battery levels

A boost circuit is built in that generates all voltages required to drive the LCD, preventing the battery level from affecting the display quality. In addition, no regulator is required, reducing both parts and costs.

Applicable models : All models

Easily supports changing LCD designs

Display allocation RAM for mapping separated from the display RAM, making it easy to change software when changing the LCD design. In addition, data creation and operation verification tools are available to facilitate development for the user.

Applicable models : All models

Integrated high precision RC oscillation A/D converter ideal for measuring temperature and humidity

Using thermistor and humidity sensor, with a small number of parts, it can measure the temperature and humidity precisely. It has many bits of counter and can improve the preciseness by making the measurement time longer.

Applicable models : All models

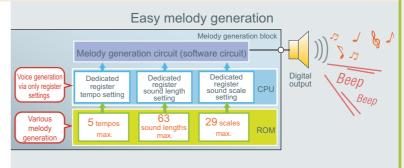
Easily play a variety of melodies

29 scales, 63 sound lengths, and 5 tempos are integrated, making it easy to create melodies based on a musical score via software.

Applicable models : All models except ML610401, ML610401P, ML610402, ML610402P, ML610403, ML610403P

Stable LCD drive regardless of battery voltage level Regulator Regulator Regulator Regulator Pump Adjusts to decreases in battery voltage Internal display allocation RAM easily modified via software Simply register the display RAM bits and addresses Display RAM Driving waveform generation block COM priver COM priver COM priver COM priver COM priver

Oscillation A/D converter ideal for temperature/humidity measurement Temp. differences detected at 16bit resolution RC oscillator in reference 16bit side Counter nts RC oscill RC oscillator Frequency ratio Read the count value in sensor side **CPU** Temperature table/ (U8) software correction Thermistor, hygrometer, etc.



Specifications

*1:500kHz generated via RC oscillation, 32kHz via crystal oscillation

*2: Current consumption during Suspend (HALT) mode at low-speed 32kHz oscillation

Change of position on board

12:54

*3: No compatible chip select signals exist for 8bit/16bit SPI bus

*4: includes 256Byte test are *5: includes 1KByte test area

SEG Driver

Requires only changes to the data settings in the display allocation

RAM - no complicated changes to display RAM control needed

		Operat	ing condition		ROM/F	RAM			Function	on/Feature	e		
Part No.	Operating voltage(V)	Operating frequency	Current *2 consumption (Typ.@HALT)	Operating temperature (°C)	ROM capacity (Byte)	RAM capacity (Byte)	WDT	ADC(method)		al port UART	LCD driver	Package	In chip
★ML610401 / ML610401P	1.25 to 3.6	500kHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 6K *4	192	1	2 (RC oscillation)	-	1	Max. 55dot 11seg × 5com.	TQFP64	0
★ML610402 / ML610402P	1.25 to 3.6	500kHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 6K *4	192	1	2 (RC oscillation)	1	1	Max. 75dot 15seg × 5com.	TQFP64	0
★ML610403 / ML610403P	1.25 to 3.6	500kHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 6K *4	192	1	2 (RC oscillation)	1	1	Max. 95dot 19seg × 5com.	TQFP64	0
★ ML610404 / ML610404P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 8K *4	256	1	2 (RC oscillation)	2	1	Max. 105dot 21seg × 5com.	TQFP80	0
★ML610405 / ML610405P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 8K *4	256	1	2 (RC oscillation)	2	1	Max. 125dot 25seg × 5com.	TQFP80	0
★ML610406 / ML610406P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 8K *4	256	1	2 (RC oscillation)	2	1	Max. 145dot 29seg × 5com.	TQFP80	0
★ML610407 / ML610407P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 16K ^{*5}	1K	1	2 (RC oscillation)	2	1	Max. 145dot 29seg × 5com.	TQFP100	0
★ML610408 / ML610408P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 16K ^{*5}	1K	1	2 (RC oscillation)	2	1	Max. 165dot 33seg × 5com.	TQFP100	0
★ML610409 / ML610409P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Mask 16K	1K	1	2 (RC oscillation)	2	1	Max. 185dot 37seg × 5com.	TQFP100	0
ML610Q407 / ML610Q407P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Flash 16K ^{*5}	1K	1	2 (RC oscillation)	2	1	Max. 145dot 29seg × 5com.	TQFP100	0
WAML610Q408 / ML610Q408P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Flash 16K ^{*5}	1K	1	2 (RC oscillation)	2	1	Max. 165dot 33seg × 5com.	TQFP100	0
ML610Q409 / ML610Q409P	1.25 to 3.6	2MHz 32.768kHz	0.9μΑ	-20 to +70 / -40 to +85	Flash 16K ^{*5}	1K	1	2 (RC oscillation)	2	1	Max. 185dot 37seg × 5com.	TQFP100	0

NE

Development Support System

Overview

OKI SEMICONDUCTOR's development support system consists of hardware and software tools that aid in program development for the ML610400/ML610300 series. The software tool utilizes an easy-to-understand GUI and facilitates the debugging operation from program creation.

- Integrated software simplifies repeated work during software development, including programming, building (object creation), and debugging
- User-friendly graphical user interface
- Optimized C compiler maximizes microcontroller performance (minimizing ROM code/increasing processing speed)
- Cost-effective, lightweight, compact debugging emulator

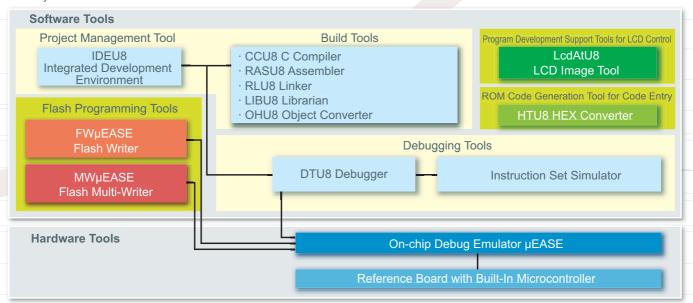


Compact, lightweight debugging emulator µEASE (50mm×90mm×9mm 50g)

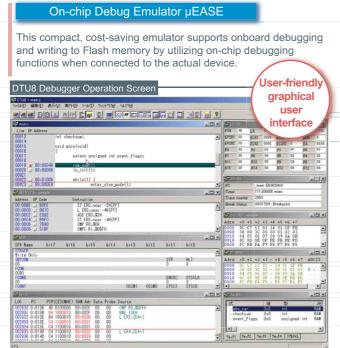


Program Development System Details

OKI SEMICONDUCTOR provides software tools for program building, Flash writing, and debugging. Build tool design and debugging startup is performed in an integrated IDEU8 environment. In addition, built-in project management and editor functions make software development more user-friendly. On the hardware side an emulator and reference board are offered that allows for on-chip debugging and writing to Flash memory while connected to the actual device.



Tool Screen and Connection Examples



LcdAtU8 LCD Image Tool

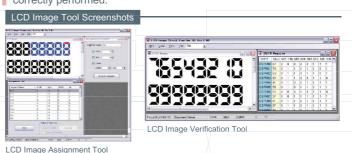
Inputting a bitmap file of an LCD panel image and LCD panel layout information automatically generates table data for LCD allocation RAM along with a sample control program.

The LCD image tools simplifies complicated mapping operation.

The LCD image tool is divided into 2 tools, one for LCD image assignment and the other for LCD image verification.

The LCD image assignment tool facilitates LCD panel image mapping with the microcontroller terminal using the LCD image displayed on the PC.

The image verification tool aids in checking that the mapping was correctly performed.



The reference board contains the microcontroller and the minimum number of parts required. Connection with µEASE allows evaluation of the operation of ML610400/ML610300 series products. Software development and Flash programming are possible



MWµEASE Multi-Writer*1

Supports simultaneous Flash programming of multiple boards of the same type.

*1:The requisite number of uEASE units must be purchased to use MWµEASE

μEASE Connection Method Using MWμEASE 32µEASE connections max.

A tool for creating ROM code data when writing the customer's code into the Flash memory (performed at OKI SEMICONDUCTOR's facility)

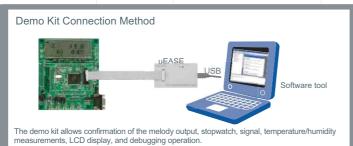
FWµEASE Flash writer

μEASE

USB hub

A software tool that acts a Flash writer for controlling the on-chip debugging emulator µEASE.

MWuEASE Screenshot



The demo kit contains an ML610Q431 reference board, a demo board, and sample program*2, and enables users to verify a host of parameters/functions, including temperature/humidity measurements, LCD display, UART signals, melody output, and the stopwatch.

Connecting to µEASE enables confirmation of the included sample

program via the DTU8 debugger.

ML610Q431 reference board + demonstration board

*2 The demo kit includes the ML610Q431 reference board with integrated temperature and humidity sensors. For other types of reference boards, OKI SEMICONDUCTOR, recommends that the user prepare all required parts after purchasing the desired

Target Products and Compatible Product Development Support Systems

				Hardware Tools
	Target Products	Software Tools	Development Tool	Reference Board
	ML610Q486 / ML610Q486P	· Project management tool		ML610Q486 Reference board
ype	ML610Q487 / ML610Q487P	(IDEU8 integrated development environment)		ML610Q487 Reference board
Þ	ML610Q488 / ML610Q488P	· Build tool		ML610Q488 Reference board
dal	ML610Q489 / ML610Q489P	· Debugging tool		ML610Q489 Reference board
Standard Type	ML610482 / ML610482P	· Flash programming tool ^{*4}	μEASE*3	ML610Q482 Reference board
_	ML610Q482 / ML610Q482P	· ROM code generation tool for code entry	p.27.102	ML610Q482 Reference board
all S	ML610340 / ML610Q340	<required environment=""> · Windows2000/XP</required>		ML610Q340 Reference board
Voice Functionality	/IL610346 / ML610Q346 / ML610348 / ML610Q348	· Windows2000/AP · Graphic adapter and display of SVGA (800×600) or more		ML610Q346 Reference board
_ E	ML610347 / ML610Q347	· At least 20MB of free hard disk space		ML610Q347 Reference board
	ML610Q411 / ML610Q411P			ML610Q411 Reference board
	ML610Q412 / ML610Q412P			ML610Q412 Reference board
e	ML610Q415			ML610Q415 Reference board
Dot Matrix Type with Built-In LCD Driver	ML610Q421 / ML610Q421P			ML610Q421 Reference board
Type CD Dr	ML610Q422 / ML610Q422P			ML610Q422 Reference board
	ML610Q428 / ML610Q428P			ML610Q428 Reference board
Dot Matrix Built-In LC	ML610Q429 / ML610Q429P			ML610Q429 Reference board
	ML610Q431	· Project management tool		ML610Q431 Reference board
8 8	ML610Q432	(IDEU8 integrated development environment)		ML610Q432 Reference board
===	ML610Q435	· Build tool	μEASE*3	ML610Q435 Reference board
	ML610Q436	· Debugging tool		ML610Q436 Reference board
	ML610Q438 / ML610Q438P	· Flash programming tool ^{*4}		ML610Q438 Reference board
	ML610Q439 / ML610Q439P	· Program development support tool for LCD control		ML610Q439 Reference board
<u>r</u>	ML610401 / ML610401P	· ROM code generation tool for code entry		ML610Q407 Reference board *5
e Driver	ML610402 / ML610402P			ML610Q407 Reference board *6
	ML610403 / ML610403P			ML610Q407 Reference board*7
T Jy	ML610404 / ML610404P			ML610Q407 Reference board*8
Segment Built-In L	ML610405 / ML610405P	<required environment=""> · Windows2000/XP</required>		ML610Q407 Reference board *9
Segmer Built-In	ML610406 / ML610406P	· Graphic adapter and display of SVGA (800×600) or more		ML610Q407 Reference board *10
Se	ML610407 / ML610407P / ML610Q407 / ML610Q407P	At least 20MB of free hard disk space		ML610Q407 Reference board
vith	ML610408 / ML610408P / ML610Q408 / ML610Q408P			ML610Q408 Reference board
>	ML610409 / ML610409P / ML610Q409 / ML610Q409P	150 W. II. MI0400407 C		ML610Q409 Reference board

- ** Setting the ML610Q407 reference board to ML610Q401 mode enables operation equivalent to the ML610401 Mask version.

 ** Setting the ML610Q407 reference board to ML610Q402 mode enables operation equivalent to the ML610402 Mask version.

 ** Setting the ML610Q407 reference board to ML610Q403 mode enables operation equivalent to the ML610Q408 version.

 ** Setting the ML610Q407 reference board to ML610Q404 mode enables operation equivalent to the ML610Q404 Mask version.

 ** Setting the ML610Q407 reference board to ML610Q405 mode enables operation equivalent to the ML610Q408 version.

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Specifications

Standard Type

			ROM/I	RAM				
	Part No.	Operating voltage (V)	Operating frequency	Minimum instruction execution time	Current *2 consumption (Typ.@HALT)	Operating temperature (°C)	ROM capacity (Byte)	RAM capacity (Byte)
	★ ML610Q486 / ML610Q486P	1.6 to 3.6	500kHz	2μs	15µA	-20 to +70 / -40 to +85	Flash 32K *7	1K
	★ ML610Q487 / ML610Q487P	1.8 to 3.8	1MHz / 32.768kHz	1μs 30.5μs	1.7µA	-20 to +70 / -40 to +85	Flash 48K *7	1.5K
	★ ML610Q488 / ML610Q488P	1.8 to 3.8	4MHz / 32.768kHz	0.25µs 30.5µs	1.7μΑ	-20 to +70 / -40 to +85	Flash 48K*7	1.5K
	★ ML610Q489 / ML610Q489P	1.8 to 3.8	4MHz / 31.25kHz(4MHz frequency division)	0.25µs 30.5µs	-	-20 to +70 / -40 to +85	Flash 48K *7	1.5K
	★ ML610482 / ML610482P	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Mask 64K *7	4K
NE	ML610Q482 / ML610Q482P	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 64K *7	4K

Voice Functionality Type

		Ор	erating Conditions			ROM/	RAM
Part No.	Operating voltage (V)	Operating frequency	Minimum instruction execution time	Current *2 consumption (Typ.@HALT)	Operating temperature (°C)	ROM capacity (Byte)	RAM capacity (Byte)
ML610340	2.2 to 5.5	4.096MHz	0.25µs	ı	-40 to +85	Mask 96K	512
ML610Q340	2.2 to 5.5	4.096MHz	0.25µs	-	-40 to +85	Flash 96K	512
ML610346	2.2 to 5.5	4.096MHz / 32kHz	0.25µs / 31µs	1.5μΑ	-40 to +85	Mask 128K	1K
ML610Q346	2.2 to 5.5	4.096MHz / 32kHz	0.25µs / 31µs	1.5μΑ	-40 to +85	Flash 128K	1K
ML610347	2.2 to 5.5	4.096MHz / 32kHz	0.25µs / 31µs	1.5μΑ	-40 to +85	Mask 128K	1K
ML610Q347	2.2 to 5.5	4.096MHz / 32kHz	0.25µs / 31µs	1.5µA	-40 to +85	Flash 128K	1K
ML610348	2.2 to 3.6	4.096MHz / 32kHz	0.25µs / 31µs	1.5μΑ	-40 to +85	Mask 128K	1K
ML610Q348	2.2 to 3.6	4.096MHz / 32kHz	0.25μs / 31μs	1.5µA	-40 to +85	Flash 128K	1K

Dot Matrix Type

			Ор	erating Conditions				
	Part No.	Operating voltage (V)	Operating frequency 1	Minimum instruction execution time	Current *2 consumption (Typ.@HALT)	Operating temperature (°C)	ROM capacity (Byte)	RAM capacity (Byte)
	ML610Q411 / ML610Q411P	1.1 to 3.6	500kHz / 32.768kHz	2μs / 30.5μs	0.5μΑ	-20 to +70 / -40 to +85	Flash 16K *7	1K
	ML610Q412 / ML610Q412P	1.1 to 3.6	500kHz / 32.768kHz	2µs / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 16K *7	1K
	ML610Q415	1.1 to 3.6	500kHz	2μs	5.5µA	-20 to +70	Flash 16K *7	1K
	ML610Q421 / ML610Q421P	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 32K	2K *9
	ML610Q422 / ML610Q422P	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 32K *7	2K *9
NI	ML610Q428 / ML610Q428P	1.1 to 3.6	4.096MHz / 2MHz / 32.768kHz	0.244µs / 2µs(@2MHz) / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 48K *7	4K *9
NI	ML610Q429 / ML610Q429P	1.1 to 3.6	4.096MHz / 2MHz / 32.768kHz	0.244µs / 2µs(@2MHz) / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 48K *7	4K *9
	ML610Q431	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70	Flash 64K *7	3K *9
	ML610Q432	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70	Flash 64K *7	3K *9
NE	ML610Q435	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70	Flash 96K *7	3K *9
NE	ML610Q436	1.1 to 3.6	4.096MHz / 500kHz / 32.768kHz	0.244µs / 2µs / 30.5µs	0.5μΑ	-20 to +70	Flash 96K *7	3K *9
	★ ML610Q438 / ML610Q438P	1.1 to 3.6	4.096MHz / 2MHz / 32.768kHz	0.244µs / 2µs(@2MHz) / 30.5µs	0.5μΑ	-20 to +70 / -40 to +85	Flash 96K *7	7K *9
	★ ML610Q439 / ML610Q439P	1.1 to 3.6	4.096MHz / 2MHz / 32.768kHz	0.244μs / 2μs(@2MHz) / 30.5μs	0.5μΑ	-20 to +70 / -40 to +85	Flash 96K *7	7K *9

[★] Under development

Segment Type

			Op	erating Conditions			ROM/	RAM
	Part No.	Operating voltage (V)	Operating frequency	Minimum instruction execution time	Current *2 consumption (Typ.@HALT)	Operating temperature (°C)	ROM capacity (Byte)	RAM capacity (Byte)
	★ ML610401 / ML610401P	1.2 to 3.6	500kHz / 32.768kHz	2µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 6K *7	192
	★ ML610402 / ML610402P	1.25 to 3.6	500kHz / 32.768kHz	2µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 6K *7	192
	★ ML610403 / ML610403P	1.25 to 3.6	500kHz / 32.768kHz	2µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 6K *7	192
	★ ML610404 / ML610404P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 8K *7	256
	★ ML610405 / ML610405P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 8K *7	256
	★ ML610406 / ML610406P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 8K *7	256
	★ ML610407 / ML610407P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 16K *8	1K
	★ ML610408 / ML610408P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 16K *8	1K
	★ ML610409 / ML610409P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Mask 16K *8	1K
N	ML610Q407 / ML610Q407P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Flash 16K *8	1K
N	ML610Q408 / ML610Q408P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Flash 16K *8	1K
N	ML610Q409 / ML610Q409P	1.25 to 3.6	2MHz / 32.768kHz	0.5µs / 30.5µs	0.9μΑ	-20 to +70 / -40 to +85	Flash 16K *8	1K

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										Funct	ions/Fea	itures					
	Port		01.71.17	1kHz	DIAGA		MOT	400/ # 1)		Serial p	oort	Supply	105 1:	Interrupt sources	0.11	Package	Chip Support
Input	Output	Input/ Output	8bit timer	timer	PWM	Capture	WDI	ADC(method)	I ² C	SSIO	SSIO UART	voltage detection	LCD driver	internal: external	Others		Опрроге
6	5	21	4(16bit×2)	-	16bit×1	-	1	4 (Sequential)	1 ^{*5}	1	1	1	-	14 : 5	_	TQFP48	0
8	4	24	8(16bit×4)	-	16bit×2	-	1	_	1	1	2	_	-	19 : 6	Low speed frequency correction	TQFP48	0
8	4	24	8(16bit×4)	-	16bit×2	2	1	-	1	1	2	-	-	19 : 6	Low speed frequency correction	TQFP48	0
8	4	24	8(16bit×4)	-	16bit×2	2	1	-	1	1	2	-	-	19 : 6	Low speed frequency correction	TQFP48	0
6	4	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	1*4	1	1	1	-	15 : 5	Low speed frequency correction/ Buzzer	TQFP48	0
6	4	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	1*4	1	1	1	-	15 : 5	Low speed frequency correction/ Buzzer	TQFP48	0

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										Funct	ions/Fea	itures					
	Por	Innut/	8bit timer	1kHz timer	PWM	Capture	WDT	ADC(method)	I ² C	Serial p		Supply voltage	LCD driver	Interrupt sources internal: external	Others	Package	Chip Support
Input	Output	Output		tilliei					I-C	SSIO	UART	detection		internal. external			
4	4	4	2(16bit×1)	-	-	-	1	-	-	1	-	-	-	9:5	Speech function / ADPCM decoder Built-in Speaker Amplifier	SSOP30	-
4	4	4	2(16bit×1)		_	-	1	_	-	1	_	-	-	9:5	Speech function / ADPCM decoder Built-in Speaker Amplifier	SSOP30	-
8	4	16	2(16bit×1)	-	_	-	1	12bit×3ch (Sequential)	-	1	1	-	-	11 : 9	Speech function / ADPCM decoder Built-in Speaker Amplifier/ Built-in 3ch Operational Amplifier	TQFP64	_
8	4	16	2(16bit×1)	-	-	-	1	12bit×3ch (Sequential)	-	1	1	-	-	11 : 9	Speech function / ADPCM decoder Built-in Speaker Amplifier/ Built-in 3ch Operational Amplifier	TQFP64	-
8	4	16	2(16bit×1)	-	-	-	1	12bit×12ch (Sequential)	ı	1	1	-	_	11 : 9	Speech function / ADPCM decoder Built-in Speaker Amplifier	TQFP64	-
8	4	16	2(16bit×1)	-	-	-	1	12bit×12ch (Sequential)	-	1	1	-	-	11 : 9	Speech function / ADPCM decoder Built-in Speaker Amplifier	TQFP64	-
4	4	8	2(16bit×1)	-	-	-	1	12bit×3ch (Sequential)	-	1	1	-	-	11 : 5	Speech function / ADPCM decoder Built-in Speaker Amplifier/ Built-in 2ch Operational Amplifier	QFP56	-
4	4	8	2(16bit×1)	-	-	-	1	12bit×3ch (Sequential)	-	1	1	-	-	11 : 5	Speech function / ADPCM decoder Built-in Speaker Amplifier/ Built-in 2ch Operational Amplifier	QFP56	-



										Funct	ions/Fea	itures					
	Port		8bit timer	1kHz	PWM	Capture	WDT	ADC(method)		Serial	port	Supply	LCD driver	Interrupt sources	Others	Package	Chip Support
Inpu	t Output	Input/ Output	obit timer	timer	PVVIVI	Capture	VVDT	ADC(method)	I ² C	SSIO	UART	detection	LCD driver	internal: external	Others		
6	3	22	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*5	1	1	1	Max. 144dot 36seg × 4com.	16 : 5	Low speed frequency correction/ Buzzer	TQFP120	0
6	3	14	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*5	1	1	1	Max. 176dot 44seg × 4com.	16 : 5	Low speed frequency correction/ Buzzer	TQFP120	0
6	3	22	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*5	1	1	1	Max. 144dot 36seg × 4com.	16 : 5	Low speed frequency correction/ Buzzer	TQFP120	0
6	3	22	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 400dot 50seg × 8com.	17 : 5	Low speed frequency correction/ Melody : Buzzer	TQFP120	0
6	3	14	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 800dot 50seg × 16com.	17 : 5	Low speed frequency correction/ Melody : Buzzer	TQFP120	0
6	3	14	2(16bit×1)	1	16bit×3	-	1	2 (RC oscillation)	1*4	1	1	1	Max. 1392dot 58seg × 24com.	20 : 5	RTC / Low speed frequency correction/ Melody : Buzzer	TQFP128	0
10	3	20	2(16bit×1)	1	16bit×3	-	1	2 (RC oscillation)	1*4	1	1	1	Max. 512dot 64seg × 8com.	20:9	RTC / Low speed frequency correction/ Melody : Buzzer	TQFP128	0
6	3	22	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 1024dot 64seg × 16com.	20 : 5	RTC / Low speed frequency correction/ Melody : Buzzer	LQFP144	0
6	3	14	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 1536dot 64seg × 24com.	20 : 5	RTC / Low speed frequency correction/ Melody : Buzzer	LQFP144	0
6	3	22	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 1024dot 64seg × 16com.	20 : 5	RTC / Low speed frequency correction/ Melody : Buzzer	LQFP144	0
6	3	14	4(16bit×2)	1	16bit×1	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 1536dot 64seg × 24com.	20 : 5	RTC / Low speed frequency correction/ Melody : Buzzer	LQFP144	0
10	3	20	4(16bit×2)	1	16bit×3	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 1344dot 56seg × 24com.	23 : 9	Low speed frequency correction/ Melody : Buzzer	LQFP144	0
10	3	20	4(16bit×2)	1	16bit×3	2	1	2 (RC oscillation) 2 (Sequential)	1*4	1	1	1	Max. 1024dot 64seg × 16com.	23 : 9	Low speed frequency correction/ Melody : Buzzer	LQFP144	0



										Fu	nctions/F	eatures					
	Port		01.71.0	1kHz	DIAMA		MDT	400/ # 10		Serial _I	oort	Supply	100 1:	Interrupt sources	O4h	Package	Chip Support
Inpu	Output	Input/ Output	8bit timer	timer	PWM	Capture	WDT	ADC(method)	I ² C	SSIO	UART	voltage detection	LCD driver	internal: external	Others		Support
4	12	18	2(16bit×1)	-	-	2	1	2 (RC oscillation)	-	-	-	-	Max. 55dot 11seg × 5com.	8 : 9 (include 4bit-OR input)	Low speed frequency correction/ Buzzer	TQFP64	
4	8	18	2(16bit×1)	-	-	2	1	2 (RC oscillation)	-	-	-	-	Max. 75dot 15seg × 5com.	8 : 9 (include 4bit-OR input)	Low speed frequency correction/ Buzzer	TQFP64	
4	4	18	2(16bit×1)	-	-	2	1	2 (RC oscillation)	-	-	-	-	Max. 95dot 19seg × 5com.	8 : 9 (include 8bit-OR input)	Low speed frequency correction/ Buzzer	TQFP64	
5	12	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 105dot 21seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP80	
5	8	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 125dot 25seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP80	
5	4	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 145dot 29seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP80	
5	12	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 145dot 29seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP100	
5	8	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 165dot 33seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP100	
5	4	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 185dot 37seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP100	
5	12	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 145dot 29seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP100	
5	8	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 165dot 33seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP100	
5	4	22	4(16bit×2)	-	16bit×1	2	1	2 (RC oscillation)	-	2	1	-	Max. 185dot 37seg × 5com.	15 : 13 (include 8bit-OR input)	Low speed frequency correction/ Melody : Buzzer	TQFP100	

●U8 Core is an OKI SEMICONDUCTOR's original 8bit CPU of RISC method. ●IDEU8 is an OKI SEMICONDUCTOR's project management tool for program development. ●CCU8 is an OKI SEMICONDUCTOR's compiler for program development. ●RLU8 is an OKI SEMICONDUCTOR's linker for program development. ●LIBU8 is an OKI SEMICONDUCTOR's librarian (library generation tool) for program development. ●OHU8 is an OKI SEMICONDUCTOR's object converter for program development. ●LCdAtU8 is an OKI SEMICONDUCTOR's program development support tool for LCD control. ●FWµEASE is an OKI SEMICONDUCTOR's flash writing of the semiconductor of the semico

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