ON Semiconductor®



4- and 8-Channel EMI Filter Arrays with ESD Protection

CM1436

Features

- Four, six and eight channels of EMI filtering with ESD protection
- Greater than 30dB of attenuation from 800MHz to 3GHz
- ±15kV ESD protection (IEC 61000-4-2, contact discharge)
- ±30kV ESD protection (HBM)
- Fabricated with Centurion™ advanced low capacitance zener process technology
- Space saving, low-profile 8-, 12- and 16-lead 0.4mm pitch TDFN packages
- Lead-free version available

Applications

- I/O port protection for mobile handsets, notebook computers, PDAs etc.
- EMI filtering for data ports in cell phones, PDAs or notebook computers.
- EMI filtering for LCD, camera and chip-to-chip data lines

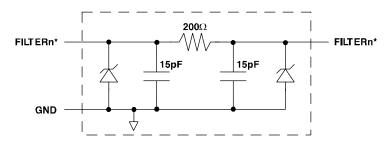
Product Description

The CM1436 is an EMI filter array with ESD protection, which integrates either four, six or eight pi filters (C-R-C). Each CM1436 filter has component values of $15pF-200\Omega-15pF$. These parts include ESD protection diodes on every pin, providing a very high level of protection for sensitive electronic components that may be subjected to electrostatic discharge (ESD). The ESD diodes connected to the filter ports safely dissipate ESD strikes of $\pm 15kV$ contact discharge, twice the specification requirement of the IEC 61000-4-2, Level 4 international standard. Using the MIL-STD-883 (Method 3015) specification for Human Body Model (HBM) ESD, the pins are protected for contact discharges at greater than $\pm 30kV$.

This device is particularly well-suited for portable electronics (e.g. mobile handsets, PDAs, notebook computers) because of its small package and easy-to-use pin assignments. In particular, the CM1436 is ideal for EMI filtering and protecting data lines from ESD in wireless handsets.

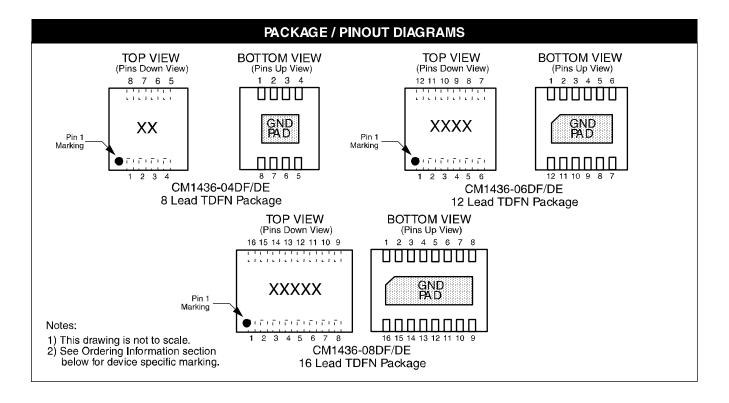
The CM1436 is available in space-saving, low-profile, 8-lead, 12-lead and 16-lead 0.4mm pitch TDFN packages. It is fabricated with *Centurion*™ process and available with lead-free finishing.

Electrical Schematic



1 of 4/6/8 EMI Filtering + ESD Channels

* See Package/Pinout Diagram for expanded pin information.



	PIN DESCRIPTIONS								
	Pins					Pins			
1436- 04Dx	1436- 06Dx	1436- 08Dx	NAME	DESCRIPTION	1436- 04Dx	1436- 06Dx	1436- 08Dx	NAME	DESCRIPTION
1	1	1	FILTER1	Filter Channel 1	8	12	16	FILTER1	Filter Channel 1
2	2	2	FILTER2	Filter Channel 2	7	11	15	FILTER2	Filter Channel 2
3	3	3	FILTER3	Filter Channel 3	6	10	14	FILTER3	Filter Channel 3
4	4	4	FILTER4	Filter Channel 4	5	9	13	FILTER4	Filter Channel 4
	5	5	FILTER5	Filter Channel 5		8	12	FILTER5	Filter Channel 5
	6	6	FILTER6	Filter Channel 6		7	11	FILTER6	Filter Channel 6
		7	FILTER7	Filter Channel 7			10	FILTER7	Filter Channel 7
		8	FILTER8	Filter Channel 8			9	FILTER8	Filter Channel 8
	GND Pad		GND	Device Ground					

Ordering Information

PART NUMBERING INFORMATION								
		Standard Finish Lead-free Finish						
Leads/Pins	Package	Ordering Part Number ¹	Part Marking	Ordering Part Number ¹	Part Marking			
8	TDFN-08	CM1436-04DF	6F	CM1436-04DE	6E			
12	TDFN-12	CM1436-06DF	N36F	CM1436-06DE	N36E			
16	TDFN-16	CM1436-08DF	N368F	CM1436-08DE	N368E			

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

Specifications

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	RATING	UNITS					
Storage Temperature Range	-65 to +150	℃					
DC Power per Resistor	100	mW					
Package DC Power Rating	300	mW					

STANDARD OPERATING CONDITIONS							
PARAMETER	RATING	UNITS					
Operating Temperature Range	-40 to +85	∞					

	ELECTRICAL	OPERATING CHARACTE	RISTIC	S (SEE NOTI	≣ 1)	
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
R	Resistance		160	200	240	Ω
С	Capacitance	At 2.5V DC, 1MHz, 30mV AC	12	15	18	pF
V _{DIODE}	Diode Standoff Voltage	$I_{\text{DIODE}} = 10 \mu A$		6.0		V
I _{LEAK}	Diode Leakage Current (reverse bias)	V _{DIODE} = 3.3V		0.1	1	μА
V _{SIG}	Signal Voltage Positive Clamp Negative Clamp	$I_{LOAD} = 10 \text{mA}$ $I_{LOAD} = -10 \text{mA}$	5.6 -0.4	6.8 -0.8	9.0 -1.5	V V
V _{ESD}	In-system ESD Withstand Voltage a) Human Body Model, MIL-STD- 883, Method 3015 b) Contact Discharge per IEC 61000-4-2 Level 4	Note 2	±30 ±15			kV kV
f _c	$\begin{array}{c} \text{Cut-off Frequency} \\ \text{Z}_{\text{SOURCE}} {=} 50\Omega, \text{Z}_{\text{LOAD}} {=} 50\Omega \end{array}$	R = 200Ω, C = 15pF;		100		MHz
A _{1GHz}	Absolute Attenuation @ 1GHz from 0dB Level	$Z_{\text{SOURCE}} = 50\Omega, Z_{\text{LOAD}} = 50\Omega,$ DC Bias = 0V; Notes 1		35		dB
A _{800MHz - 6GHz}	Absolute Attenuation @ 800MHz to 6GHz from 0dB Level	$Z_{\text{SOURCE}} = 50\Omega, Z_{\text{LOAD}} = 50\Omega,$ DC Bias = 0V; Notes 1 and 3		30		dB

Note 1: $T_A=25\,^{\circ}\text{C}$ unless otherwise specified. Note 2: ESD applied to input and output pins with respect to GND, one at a time. Note 3: Attenuation / RF curves characterized by a network analyzer using microprobes.

Performance Information

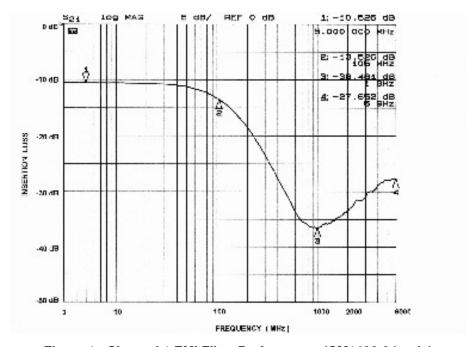


Figure 1. Channel 1 EMI Filter Performance (CM1436-04 only)

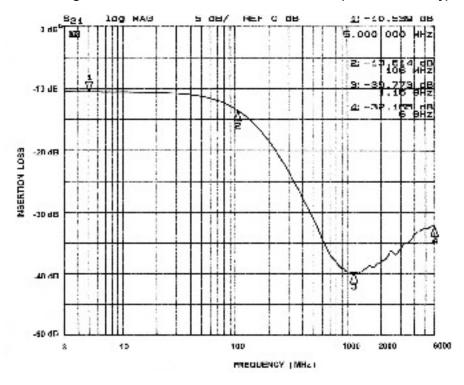


Figure 2. Channel 2 EMI Filter Performance (CM1436-04 only)

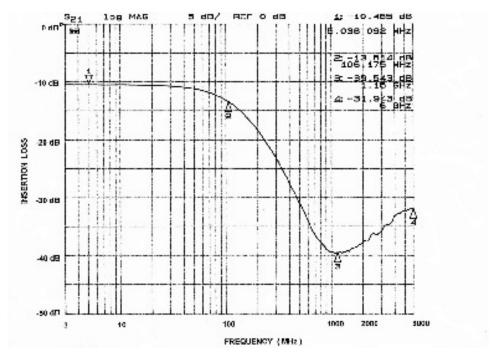


Figure 3. Channel 3 EMI Filter Performance (CM1436-04 only)

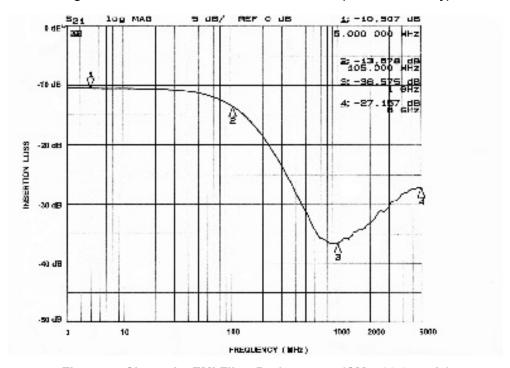


Figure 4. Channel 4 EMI Filter Performance (CM1436-04 only)

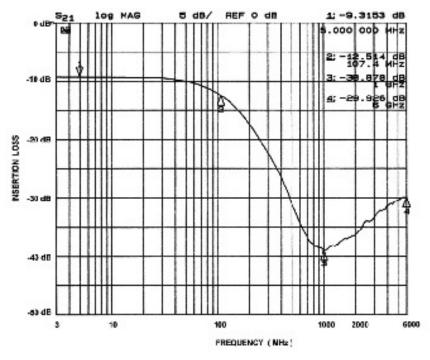


Figure 5. Channel 1 EMI Filter Performance (CM1436-06/08 only)

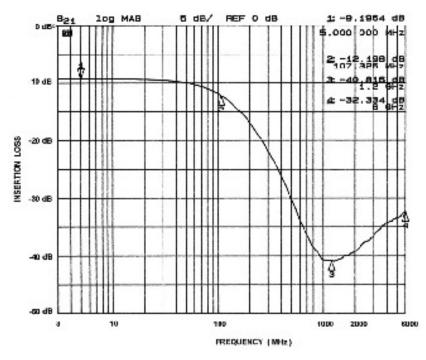


Figure 6. Channel 2 EMI Filter Performance (CM1436-06/08 only)

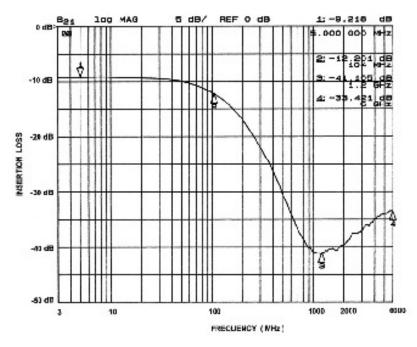


Figure 7. Channel 3 EMI Filter Performance (CM1436-06/08 only)

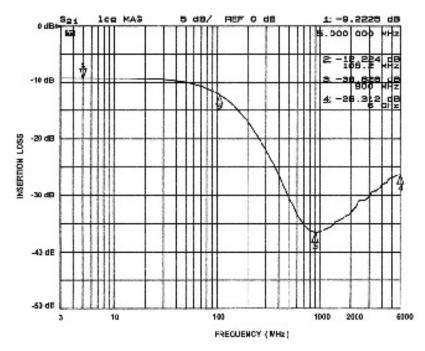


Figure 8. Channel 4 EMI Filter Performance (CM1436-06/08 only)

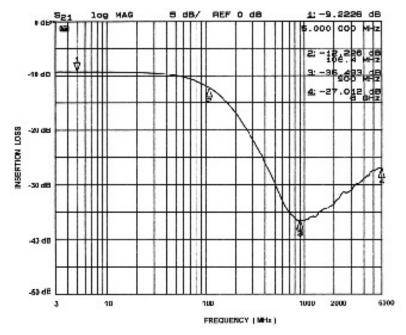


Figure 9. Channel 5 EMI Filter Performance (CM1436-06/08 only)

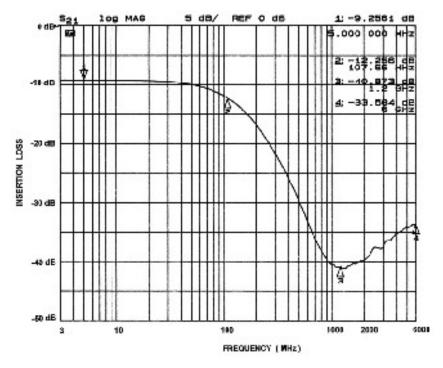


Figure 10. Channel 6 EMI Filter Performance (CM1436-06/08 only)

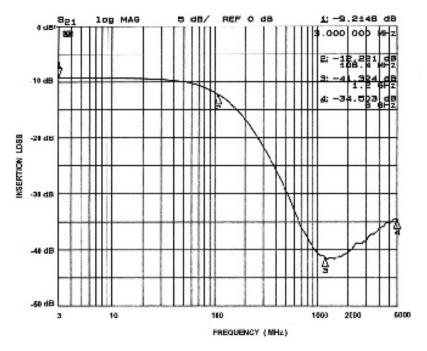


Figure 11. Channel 7 EMI Filter Performance (CM1436-08 only)

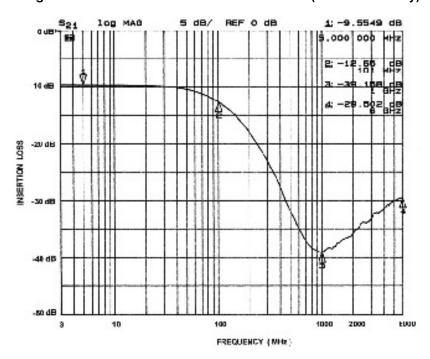


Figure 12. Channel 8 EMI Filter Performance (CM1436-08 only)

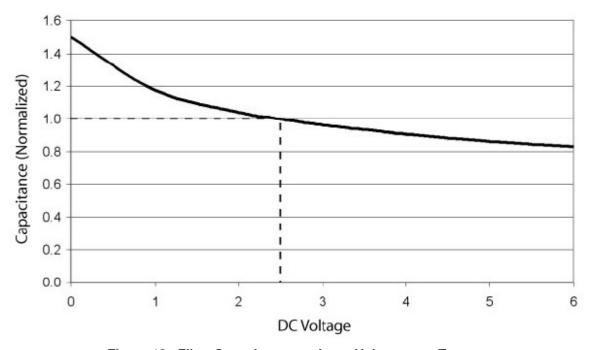


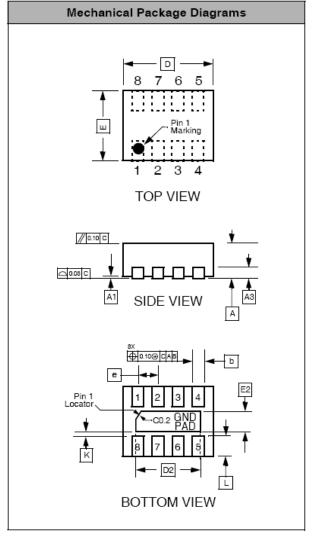
Figure 13. Filter Capacitance vs. Input Voltage over Temperature (normalized to capacitance at 2.5VDC and 25 ℃)

Mechanical Details

CM1436-04DF/DE Mechanical Specifications

Dimensions for the CM1436-04DF/DE supplied in a 8-lead, 0.4mm pitch TDFN package are presented below.

	PAC	KAGE	DIME	NSIO	NS			
Package	TDFN							
JEDEC No.		MO-229C [†]						
Leads				8				
Dim.	N	lillimete	rs		Inches			
Diiii.	Min	Nom	Max	Min	Nom	Max		
Α	0.70	0.75	0.80	0.028	0.030	0.031		
A1	0.00	0.02	0.05	0.000	0.001	0.002		
А3	(0.20 RE	F	0.008 REF				
b	0.15	0.20	0.25	0.006	0.008	0.010		
D	1.65	1.70	1.75	0.065	0.067	0.069		
D2	1.10	1.20	1.30	0.043	0.047	0.051		
E	1.30	1.35	1.40	0.051	0.053	0.055		
E2	0.30	0.40	0.50	0.012	0.016	0.020		
е		0.40 BS	С	0	.016 BS	SC SC		
К	0.20			0.008				
L	0.15	0.25	0.35	0.006	0.010	0.014		
# per tape and reel	3000 pieces							
	Controlling dimension: millimeters							



Dimensions for 8-Lead, 0.4mm pitch TDFN Package

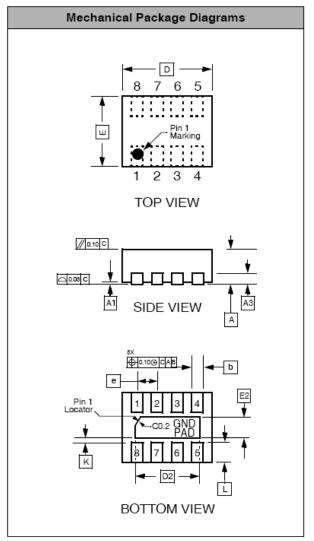
[†]This package is compliant with JEDEC standard MO-229C with the exception of the "D", "D2", "E", "E2", "K" and "L" dimensions as called out in the table above.

Mechanical Details (cont'd)

CM1436-06DF/DE Mechanical Specifications

Dimensions for the CM1436-06DF/DE suplied in a 12-lead, 0.4mm pitch TDFN package are presented below. For complete information on the TDFN-12, see the California Micro Devices TDFN Package Information document.

	PAC	KAGE	DIME	NSIO	NS			
Package		TDFN						
JEDEC No.		MO-229C [†]						
Leads			1	12				
Dim.	N	lillimete	rs		Inches			
5	Min	Nom	Max	Min	Nom	Max		
Α	0.70	0.75	0.80	0.028	0.030	0.031		
A1	0.00	0.02	0.05	0.000	0.001	0.002		
А3	(0.20 RE	F	0.008 REF				
b	0.15	0.20	0.25	0.006	0.008	0.010		
D	2.45	2.50	2.55	0.096	0.098	0.100		
D2	1.90	2.00	2.10	0.075	0.079	0.083		
E	1.30	1.35	1.40	0.051	0.053	0.055		
E2	0.25	0.35	0.45	0.010	0.014	0.018		
е	(0.40 BS	С	0	.016 BS	C		
К	0.20			0.008				
L	0.15	0.25	0.35	0.006	0.010	0.014		
# per tape and reel	3000 pieces							
	Controlling dimension: millimeters							



Dimensions for 12-Lead, 0.4mm pitch TDFN package

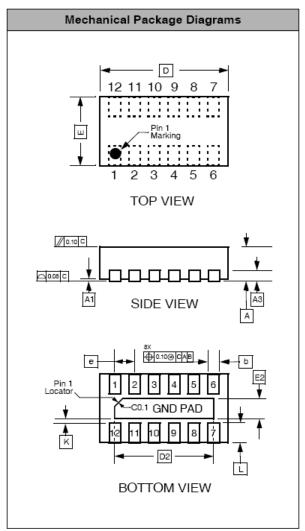
[†]This package is compliant with JEDEC standard MO-229C with the exception of the "D", "D2", "E", "E2", "K" and "L" dimensions as called out in the table above.

Mechanical Details (cont'd)

CM1436-08DF/DE Mechanical Specifications

The CM1436-08DF/DE is supplied in a 16-lead, 0.4mm pitch TDFN package. Dimensions are presented below. For complete information on the TDFN-16, see the California Micro Devices TDFN Package Information document.

	PAC	KAGE	DIME	NSIO	NS			
Package		TDFN						
JEDEC No.	MO-229C [†]							
Leads			1	6				
Dim.	N	lillimete	rs		Inches			
Diiii.	Min	Nom	Max	Min	Nom	Max		
Α	0.70	0.75	0.80	0.028	0.030	0.031		
A1	0.00	0.02	0.05	0.000	0.001	0.002		
А3	0.40	0.55	0.70	0.016	0.022	0.028		
b	(0.20 RE	F	0.008 REF				
D	3.25	3.30	3.35	0.128	0.130	0.132		
D2	2.80	2.90	3.00	0.110	0.114	0.118		
E	1.30	1.35	1.40	0.051	0.053	0.055		
E2	0.35	0.40	0.45	0.014	0.016	0.018		
е	(0.40 BS	С	0	.016 BS	O		
К	0.20			0.008				
L	0.15	0.25	0.35	0.006	0.010	0.014		
# per tape and reel	3000 pieces							
	Controlling dimension: millimeters							



Dimensions for 16-Lead, 0.4mm pitch TDFN package

[†]This package is compliant with JEDEC standard MO-229C with the exception of the "D", "D2", "E", "E2", "K" and "L" dimensions as called out in the table above.

CM1436

ON Semiconductor and Ware registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any on semiconductor and are registered trademarks of seminoriductor components industries, LLC (SCILLC). SCILLC) reserves the right to make dranges without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative