

March 2011

FGH40N60SMDF 600V, 40A Field Stop IGBT

Features

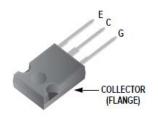
- Maximum Junction Temperature: T_{.1} =175°C
- Positive Temperaure Co-efficient for easy parallel operating
- · High current capability
- Low saturation voltage: $V_{CE(sat)} = 1.9V(Typ.) @ I_C = 40A$
- · High input impedance
- · Fast switching
- Tighten Parameter Distribution
- RoHS compliant

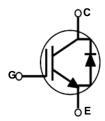
Applications

- · Solar Inverter, UPS, SMPS, PFC
- · Induction Heating

General Description

Using Novel Field Stop IGBT Technology, Fairchild's new series of Field Stop IGBTs offer the optimum performance for Solar Inverter, UPS, SMPS, IH and PFC applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Units
V _{CES}	Collector to Emitter Voltage		600	V
V _{GES}	Gate to Emitter Voltage		± 20	V
I _C	Collector Current	@ T _C = 25°C	80	А
1.0	Collector Current	@ T _C = 100°C	40	А
I _{CM (1)}	Pulsed Collector Current		120	А
I _F	Diode Forward Current	@ T _C = 25°C	40	А
, F	Diode Forward Current	@ T _C = 100°C	20	А
I _{FM (1)}	Pulsed Diode Maximum Forward Current		120	А
P _D	Maximum Power Dissipation	@ T _C = 25°C	349	W
. 0	Maximum Power Dissipation	@ T _C = 100°C	174	W
T _J	Operating Junction Temperature		-55 to +175	°C
T _{stg}	Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes

1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	0.43	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	-	1.45	°C/W
R _{θJA} Thermal Resistance, Junction to Ambient		-	40	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGH40N60SMDF	FGH40N60SMDF	TO-247	=	-	30

Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	teristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250\mu A$	600	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_{J}}$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250\mu A$	-	0.6	-	V/°C
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	250	μА
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	±400	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 250 \mu A, V_{CE} = V_{GE}$	3.5	4.5	6.0	V
. ,		I _C = 40A, V _{GE} = 15V	-	1.9	2.5	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 40A, V _{GE} = 15V, T _C = 175°C	-	2.1	-	V
Dynamic C	haracteristics					
C _{ies}	Input Capacitance		-	1880	-	pF
C _{oes}	Output Capacitance	$V_{CE} = 30V_{,} V_{GE} = 0V_{,}$ f = 1MHz	-	180	-	pF
C _{res}	Reverse Transfer Capacitance	1 - 11011 12	-	50	-	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	12	16	ns
t _r	Rise Time		-	20	28	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 400V, I_{C} = 40A,$	-	92	120	ns
t _f	Fall Time	$R_G = 6\Omega$, $V_{GE} = 15V$,	-	13	17	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 25°C	-	1.3	2.0	mJ
E _{off}	Turn-Off Switching Loss		-	0.26	0.34	mJ
E _{ts}	Total Switching Loss		-	1.56	2.34	mJ
t _{d(on)}	Turn-On Delay Time		-	15	-	ns
t _r	Rise Time	$V_{CC} = 400V, I_{C} = 40A,$ $R_{G} = 6\Omega, V_{GE} = 15V,$	-	22	-	ns
t _{d(off)}	Turn-Off Delay Time		-	116	-	ns
t _f	Fall Time		-	16	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 175°C	-	2.1	-	mJ
E _{off}	Turn-Off Switching Loss		-	0.6	-	mJ
E _{ts}	Total Switching Loss		-	2.7	-	mJ

Electrical Characteristics of the IGBT (Continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Units
Q_g	Total Gate Charge		-	119	180	nC
Q _{ge}	Gate to Emitter Charge	$V_{CE} = 400V, I_{C} = 40A,$ $V_{GE} = 15V$	-	13	20	nC
Q_{gc}	Gate to Collector Charge	VGE - 10 V	-	58	90	nC

Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Units
V _{FM}	Diode Forward Voltage	I _E = 20A	$T_C = 25^{\circ}C$	-	1.3	1.7	V
FIVI	V _{FM} Blode i diward voltage	F	$T_{\rm C} = 175^{\rm o}{\rm C}$	-	1.15	-]
E _{rec}	Reverse Recovery Energy		$T_{\rm C} = 175^{\rm o}{\rm C}$	-	138	-	uJ
t _{rr}	Diode Reverse Recovery Time	$I_F = 20A$, $dI_F/dt = 200A/\mu s$	$T_C = 25^{\circ}C$	-	70	100	ns
111	2.000 1.010.00 1.00010.9 1	ης -20/λ, αιρ/αι - 200//μ3	$T_{\rm C} = 175^{\rm o}{\rm C}$	-	210	-	1
Q _{rr}	Diode Reverse Recovery Charge		$T_C = 25^{\circ}C$	-	250	350	nC
}	2.646 Hereige Heester, Change		$T_{\rm C} = 175^{\rm o}{\rm C}$	-	1875	1	

Figure 1. Typical Output Characteristics

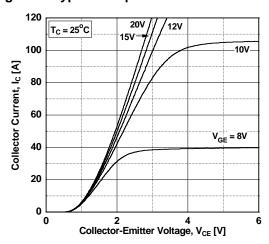


Figure 3. Typical Saturation Voltage Characteristics

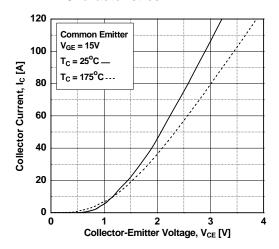


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level

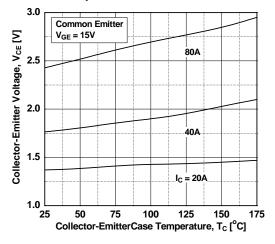


Figure 2. Typical Output Characteristics

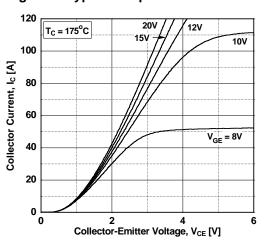


Figure 4. Transfer Characteristics

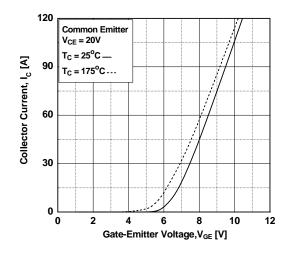


Figure 6. Saturation Voltage vs. V_{GE}

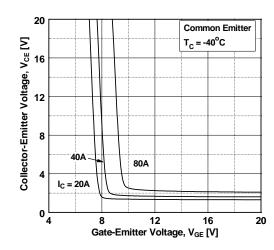


Figure 7. Saturation Voltage vs. V_{GE}

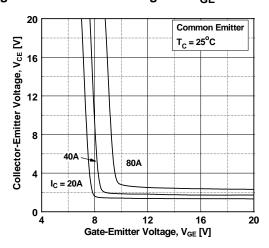


Figure 9. Capacitance Characteristics

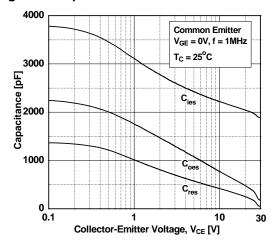


Figure 11. SOA Characteristics

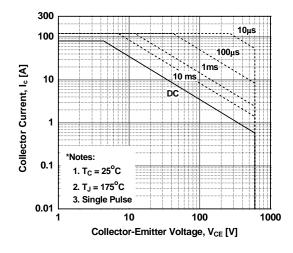


Figure 8. Saturation Voltage vs. V_{GE}

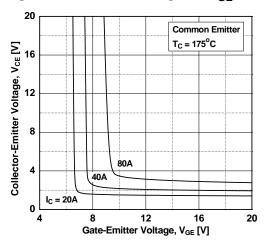


Figure 10. Gate charge Characteristics

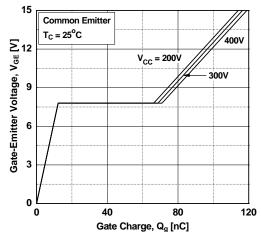


Figure 12. Turn-on Characteristics vs. Gate Resistance

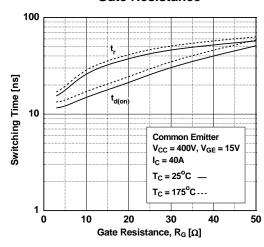


Figure 13. Turn-off Characteristics vs.
Gate Resistance

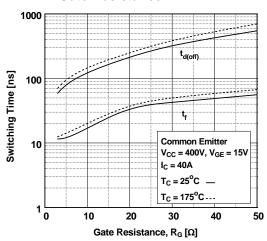


Figure 15. Turn-off Characteristics vs. Collector Current

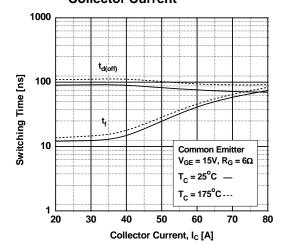


Figure 17. Switching Loss vs. Collector Current

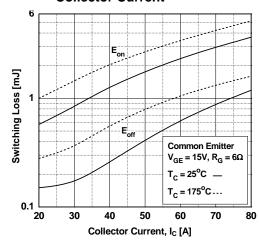


Figure 14. Turn-on Characteristics vs.
Collector Current

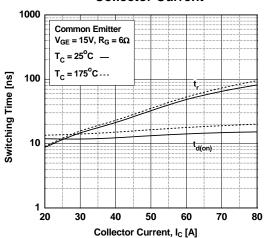


Figure 16. Switching Loss vs.

Gate Resistance

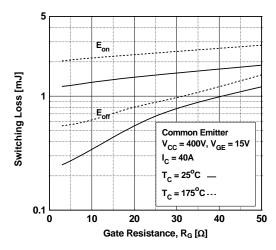


Figure 18. Turn off Switching SOA Characteristics

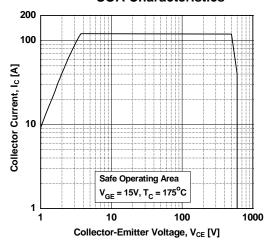


Figure 19. Current Derating

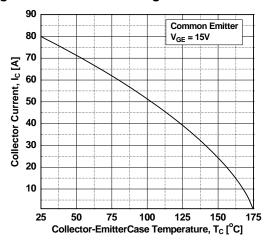


Figure 21. Forward Characteristics

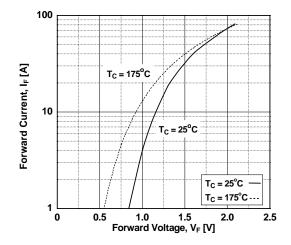


Figure 23. Stored Charge

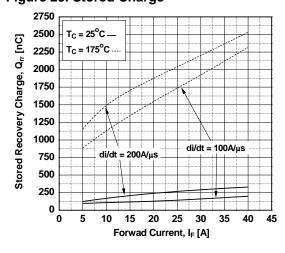


Figure 20. Load Current Vs. Frequency

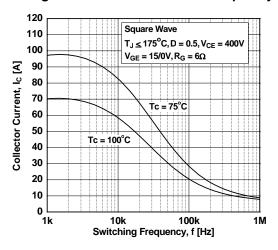


Figure 22. Reverse Current

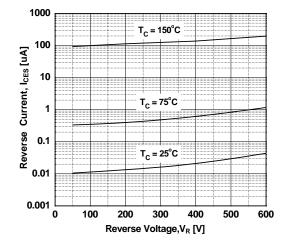
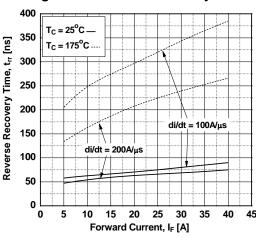
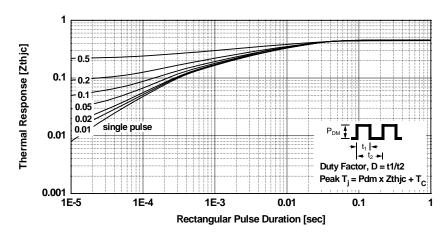


Figure 24. Reverse Recovery Time







Mechanical Dimensions TO - 247AB (FKS PKG CODE 001) A +/-0.07 MATTED AREA 12.86 4.700 ₱ 1.500 X 0.15 DP. 10.600 1.500±0.03 (3X) POLISH AREA 5.50 7.620 Ø 3.580±0.05 Ø 5.000 Ø 6.800±0.05 MARKING-MATTED AREA (4x) POLISH AREA 1.65±0.12 1.64 2.54±0.12 1.26±0.09 0.600±0.09 5.560 (TYP) -- 5.560 (TYP) OLISH AREA R0.150 (TYP) APPLY TO ALL CORNER RADIUS OLISH AREA





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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